

The desired wave form produced by two pulse width modulators is shown below. Pulse Width Modulator #1 has an output pulse repetition interval of  $PRI_1$  and a pulse width of  $PW_1$ , while Pulse Width Modulator #2 has an output pulse repetition interval of  $PRI_2$  and a pulse width of  $PW_2$ .  $PRI_1$  is equal to  $PRI_2$  as is  $PW_1$  is equal to  $PW_2$ .

Phase delay,  $PD$ , is the offset in time between the leading edge of a PWM #1 pulse and the leading edge of a subsequent PWM #2 pulse. Our intent is to control  $PD$  in 31ns steps.

