

TERMINAL					DESCRIPTION
NAME	PZ		PN		
	NO.	Seg.	NO.	Seg.	
P4.3/UCA0SOMI/UCA0RXD/ UCB1STE	1		1		General-purpose digital I/O USCI_A0: Slave out, master in (SPI mode) USCI_A0: Receive data (UART mode) USCI_B1: Slave transmit enable (SPI mode)
P1.4/UCB0CLK/UCA0STE/ TA1.0/Sx	2	S1	2	S3	General-purpose digital I/O USCI_B0: Clock signal input (SPI slave mode), Clock signal output (SPI master mode) USCI_A0: Slave transmit enable (SPI mode) Timer_A TA1 CCR0 capture: CCI0A input, compare: Out0 output LCD segment output (segment number is package specific)