

workspace\_v8 - 2833x\_RFFT\_ADC\_RT/source/Main.c - Code Composer Studio

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Quick Access

Debug

28335-ControCard-NewTargetConfiguration.cxml [Code Composer Studio - Device Debugging]

main() at Main.c:196 0x0086C2

\_args\_main() at args\_main.c:81 0x008BD4

c\_int00() at boot28.asm:261 0x008A33 (the entry point was reached)

Variables Expressions Registers

Expression	Type	Value	Address
> rfft	struct <unnamed>	[InBuf=0xA11908B ???].Out...	0x0000C00E@Data
> rfft_adc	struct <unnamed>	[InBuf=0x0000C040 4095].Tail...	0x0000C0A@Data
freq	float	9000.0	0x000040E@Data
FFTReal_Magnitude	float[257]	[305.642029, 0.0998369753, 0...]	0x0000C240@Data
_ 0 ... 99]			

Disassembly Memory Browser

```
C$19: 7700 NOP
C$19: 173 while(1)
C$19: 0086c2: 6FAF SB C$15, UNC
C$19: 0086c3: F02 ADDB SP, #2
C$19: 0086c4: A842 MOVL *-SP[2], XAR
C$19: 0086c5: 764086CC LCR $..RFFT_adc
C$19: 0086c6: 76408484 LCR $..RFFT_adc
C$19: 0086c7: 76408484 LCR $..RFFT_adc
```

Main.c 0x3ff9ce C:\t\controlSUITE\libs\dsp\FPU\v120\examples\_ccsv4\2833x\_RFFT\_ADC\_RT\Example\_RFFT\_ADC\_RT\_SCREENSHOT...

Lab.h

```
193
194     FFTStartFlag = 0;           // Start collecting the next frame of data
195 }
196 asm("NOP");
197 } //end of main()
198
199 interrupt void adc_isr(void)
200 {
201     static Uint16 *AdcBufPtr = AdcBuf;
202     // Pointer to ADC data buffer
203     // End of main() interrupt
204 }
```

Console

No consoles to display at this time.

Memory Browser

Target Configurations Search Single Time -1

sample

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Debug

28335-ControCard-NewTargetConfiguration.cxml [Code Composer Studio - Device Debugging]

main() at Main.c:175 0x008672

\_args\_main() at args\_main.c:81 0x008BD4

c\_int00() at boot28.asm:261 0x008A33 (the entry point was reached)

Variables Registers

Name	Type	Value	Location
freq	float	1031.25	0x000040E@Data
i	unsigned int	257	0x000040D@Data
j	unsigned int	11	0x000040C@Data

Disassembly Memory Browser

```
C$15: 761F0300 MOVW DP, #0x300
C$15: 9202 MOV AL, @R2
C$15: 008674: 614D SB C$19, EQ
C$15: 008675: 177 RFFT_adc_f32u(&rfft_adc);
C$15: 008676: 8F00C00A MOVL XAR4, #0x00c
C$15: 008678: FF69 SPM #0
```

Main.c 0x3ff9ce C:\t\controlSUITE\libs\dsp\FPU\v120\examples\_ccsv4\2833x\_RFFT\_ADC\_RT\Example\_RFFT\_ADC\_RT\_SCREENSHOT...

data\_input.h main.c coeffs.h 0x3ff9ce

```
173     while(1)           // endless loop - wait for an interrupt
174
175     if(FFTStartFlag)    // If one frame data ready, then do FFT
176     {
177         RFFT_adc_f32u(&rfft_adc); // This version of FFT doesn't need buffer alignment
178         RFFT_f32_mag(&rfft);   // Calculate spectrum amplitude
179
180         j = 1;
181     }
```

Console

No consoles to display at this time.

Memory Browser

type filter text

Projects

User Defined

Click the New button to create a new target configuration file. Click [here](#) to hide this message.

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Quick Access

Debug

Variables Registers

Name	Type	Value	Location
freq	float	1968.75	0x0000040E@Data
i	unsigned int	257	0x0000040D@Data
j	unsigned int	21	0x0000040C@Data

Main.c data\_input.h main.c coeffs.h 0x3ff9ce

```

194     FFTStartFlag = 0;           //Start collecting the next frame of data
195 }
196     asm(" NOP");
197 }
198 //end of main()
199
200 interrupt void adc_isr(void)
201 {

```

Disassembly Memory Browser

```

C$19: 7700 NOP
0086c2: 6FAF SB C$15, UNC
$../RFFT_adc_f32u.asm:128:176$, RFFT_
0086c3: FE02 ADDB SP, #2
0086c4: A842 NOVL *-SP[2], XAR

```

Console

No consoles to display at this time.

Writable Smart Insert 196 : 1

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Quick Access

Debug

Variables Registers

Name	Type	Value	Location
freq	float	3000.0	0x0000040E@Data
i	unsigned int	257	0x0000040D@Data
j	unsigned int	32	0x0000040C@Data

Main.c data\_input.h main.c coeffs.h 0x3ff9ce

```

173     while(1)           // endless loop - wait for an interrupt
174     {
175         if(FFTStartFlag) // If one frame data ready, then do FFT
176         {
177             RFFT_adc_f32u(&rfft_adc); // This version of FFT doesn't need buffer alignment
178             RFFT_f32_mag(&rfft);      // Calculate spectrum amplitude
179
180         j = 1;

```

Disassembly Memory Browser

```

C$15: 761F0300 MOVW DP, #0x300
008672: 9202 MOV AL, @0x2
008674: 614D SB C$19, EQ
177     RFFT_adc_f32u(&rfft_adc);
008676: 8F00C00A NOVL XAR4, #0x00c
008678: FF69 SPM #0

```

Console

No consoles to display at this time.

Writable Smart Insert 175 : 1