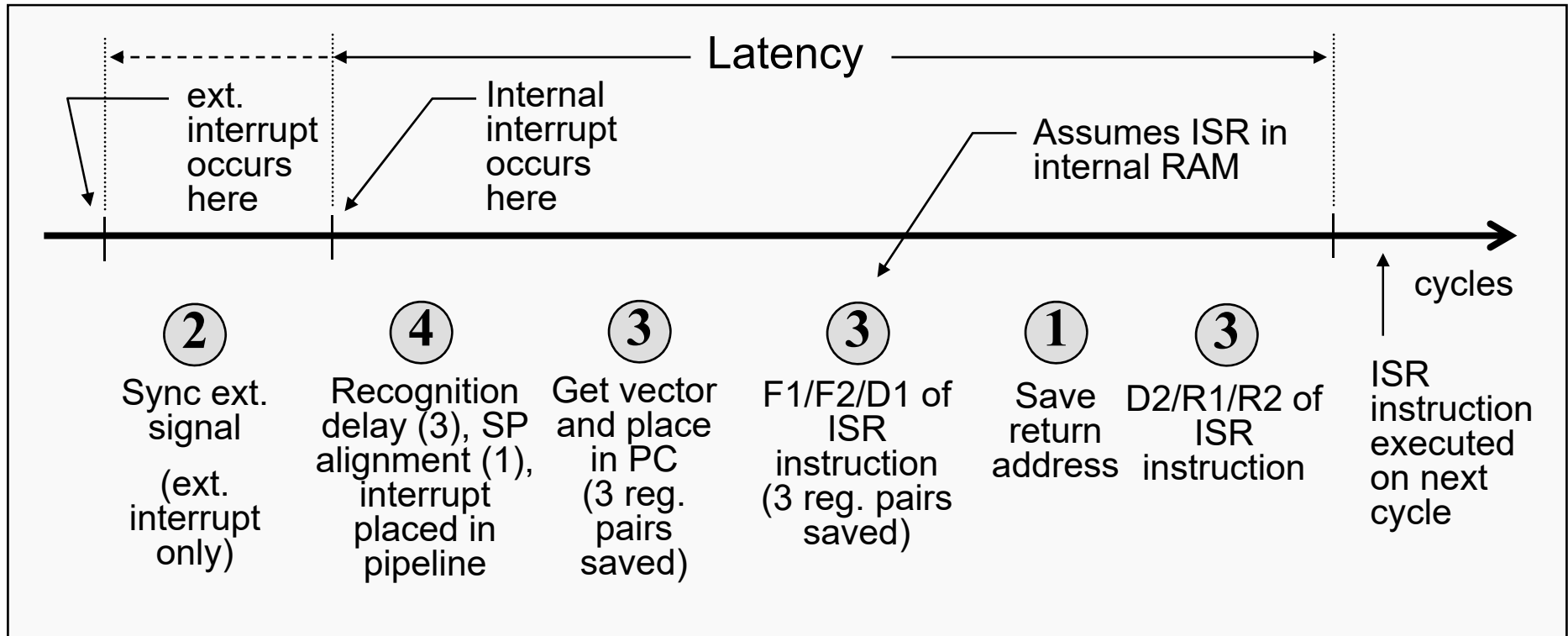


Interrupt Latency



- ◆ **Minimum latency (to when real work occurs in the ISR):**
 - **Internal interrupts: 14 cycles**
 - **External interrupts: 16 cycles**
- ◆ **Maximum latency: Depends on wait states, INTM, etc.**