Woodside Electronics
Elmer Camera CPU
A/D Issues, Continued
Prepared by Don Cadwell
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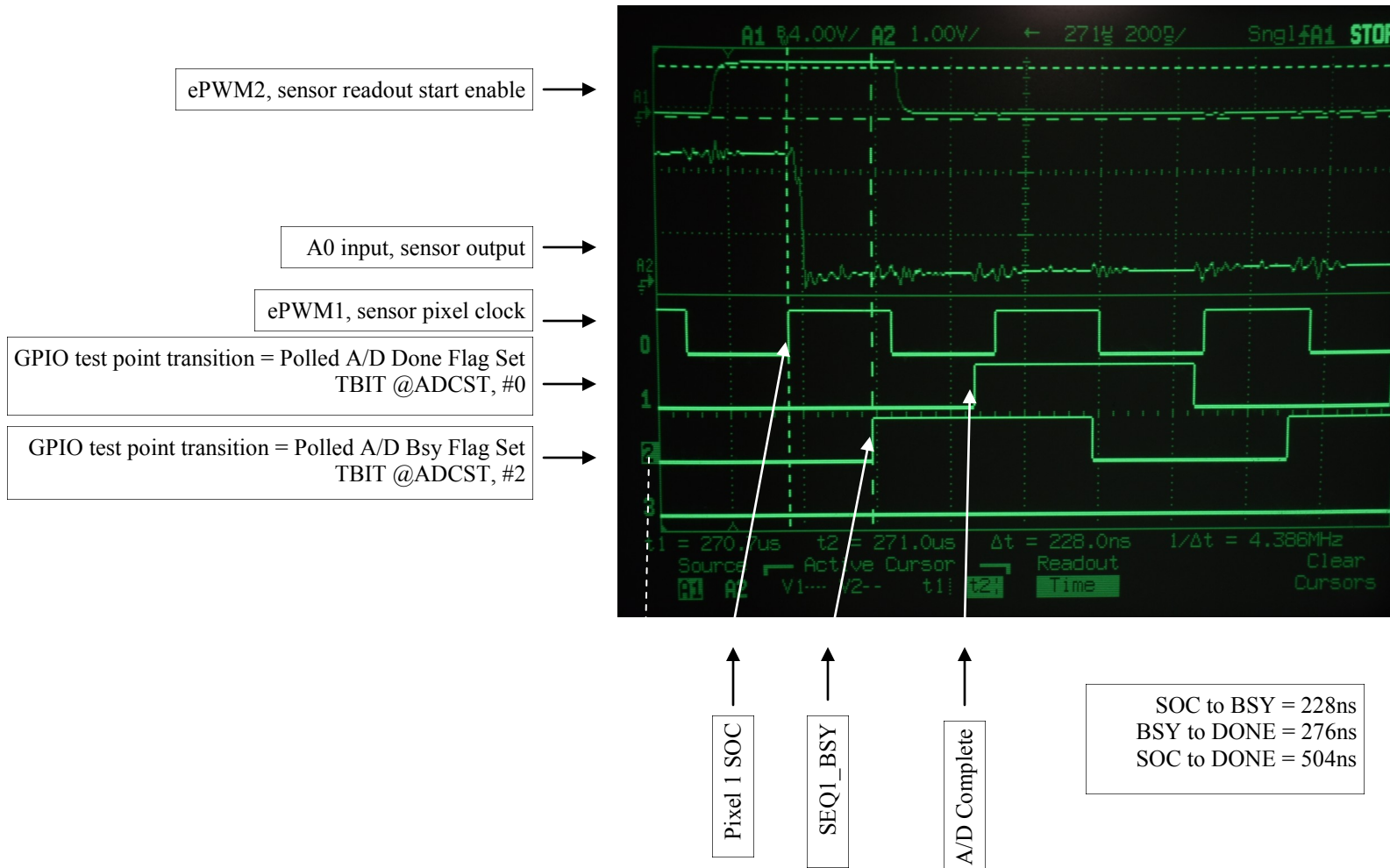
General
TMS320F28335PGFA
CA-65COYSW
XCLKIN = 30mHz
SYSCLOCKOUT=150mHz
VDDIO=VDDA2=VDDAIO = 3.3V
VDD=VDD1A18=VDD2A18 = 1.9V

ePWM2, sensor readout start enable →

A0 input, sensor output →

ePWM1, sensor pixel clock →

GPIO test point transition = Polled A/D Done Flag Set
TBIT @ADCST, #0 →

GPIO test point transition = Polled A/D Bsy Flag Set
TBIT @ADCST, #2 →

General ADC

ADC, Bandgap and Reference are always powered.
Internal reference selected.
ADCMAXCONV = 0
CONT_RUN = 0

Pixel 1 SOC

SEQ1_BSY

A/D Complete

SOC to BSY = 228ns
BSY to DONE = 276ns
SOC to DONE = 504ns