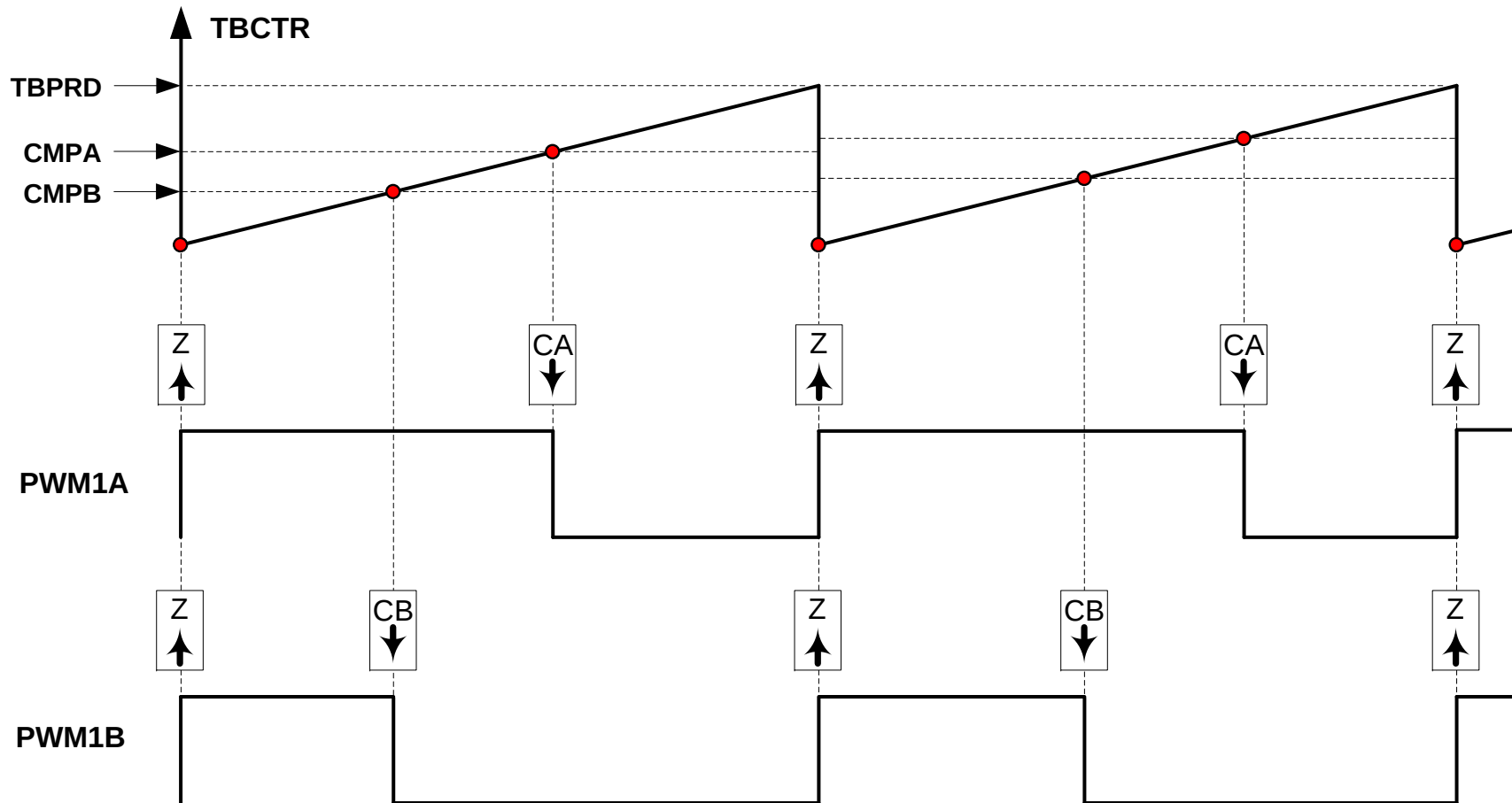


Basic PWM Example

In this example, an asymmetrical up-count time-base is used to generate two PWM waveforms at the same frequency but with different duty cycles.



CMPA and CMPB are adjusted to control the trailing edges of each PWM waveform.