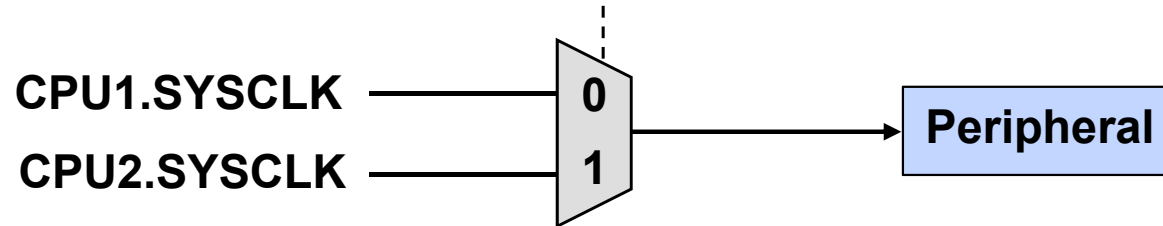


Dual-Core CPU Select Registers

DevCfgRegs.CPUSELx.bit.PeripheralName = 0



0 = connected to CPU1 (default) 1 = connected to CPU2

Note: CPUSELx must be configured before PCLKCRx

Register	PeripheralName
CPUSEL0	EPWM1, EPWM2, EPWM3, EPWM4, EPWM5, EPWM6, EPWM7, EPWM8, EPWM9, EPWM10, EPWM11, EPWM12
CPUSEL1	ECAP1, ECAP2, ECAP3, ECAP4, ECAP5, ECAP6
CPUSEL2	EQEP1, EQEP2, EQEP3
CPUSEL4	SD1, SD2
CPUSEL5	SCI_A, SCI_B, SCI_C, SCI_D
CPUSEL6	SPI_A, SPI_B, SPI_C
CPUSEL7	I2C_A, I2C_B
CPUSEL8	CAN_A, CAN_B
CPUSEL9	McBSP_A, McBSP_B
CPUSEL11	ADC_A, ADC_B, ADC_C, ADC_D
CPUSEL12	CMPSS1, CMPSS2, CMPSS3, CMPSS4, CMPSS5, CMPSS6, CMPSS7, CMPSS8
CPUSEL14	DAC_A, DAC_B, DAC_C

Note: DEVCFGLOCK1 register can be used to lock above registers (lock bit for each register)