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Disassembly
Enter location here
1154          if(resetFlag_CPU)
0002dc: 761F0208 MOVW   DP, #0x208
0002de: 9203     MOV    AL, @0x3
0002df: FFE10352 B      C$!L172, EQ
1156          resetFlag_CPU = 0;
0002e1: 2B03     MOV    @0x3, #0
1158          EALLOW;
0002e2: 7622     EALLOW
1159          DINT;
0002e3: 3B10     SETC  INTM
1161          mywDog->WDCR = 0x17; // 0X17 Ke
0002e4: 8A2E     MOVL  XAR4, @0x2e
0002e5: 56BF17FC MOVB  *+XAR4[7], #0x17, UNC
1163          EINT;
0002e7: 2910     CLRC  INTM
1164          EDIS;
0002e8: 761A     EDIS
1168          break;
0002e9: FFEF0348 B      C$!L172, UNC
1173

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Disassembly
Enter location here
0002eb
0002ec
0002ee      C$!L172:
0002f0: FF69     SPM    #0
0002f1: FE90     SUBB  SP, #16
1176      000633: 0006     LRETR
1645

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Disassembly
Enter location here
000634
000635
000636      501          runCommand(cm);
000637: 3f471f: 8F008230 MOVL  XAR4, #0x008230
000638: 3f4721: 764000AC LCR   runCommand
1649      505          }
000639: 3f4723: FFEFFCF3 B      -781, UNC
513

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Disassembly
Enter location here
322
3f441b
324
C$L48:
3f4416: 761F0208 MOVW   DP, #0x208
3f4418: 8A2E     MOVL  XAR4, @0x2e
3f4419: 767F51BF LCR   WDOG_clearCounter
322
3f441b
324
3ff5f5: 7625     ESTOP0
3ff5f6: 6FFF     SB     -1, UNC
3ff5f7: 7622     EALLOW
3ff5f8: 761F01BE MOVN  DP, #0x1be
3ff5fa: CC16FFFC AND   AL, @0x16, #0xffffc
3ff5fc: 5001     ORB   AL, #0x1
3ff5fd: 9616     MOV   @0x16, AL
3ff5fe: CC16FFF3 AND   AL, @0x16, #0xffff3
3ff600: 5004     ORB   AL, #0x4
3ff601: 9616     MOV   @0x16, AL
3ff602: 181CFFFE AND   @0x1c, #0xffffe
3ff604: 181CFFFD AND   @0x1c, #0xffffd
3ff606: 1A120003 OR    @0x12, #0x0003

```