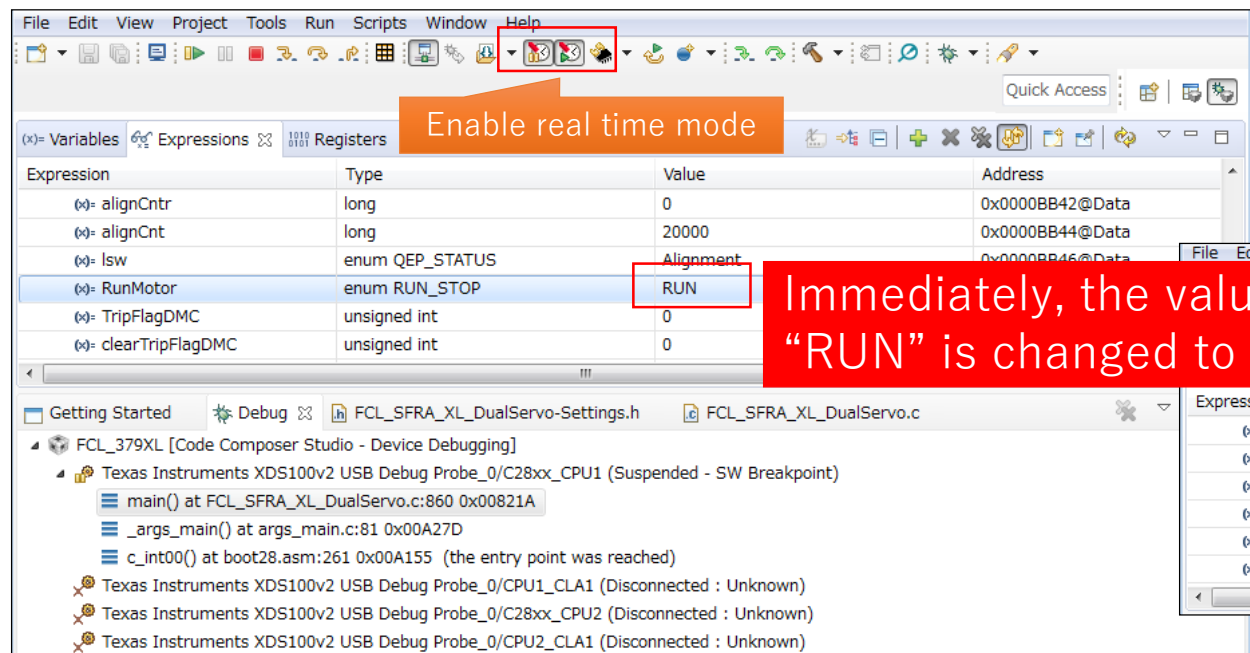


FCL_LEVEL2

- JP1 - JP5 in the launch pad are not populated
- JP6 is **POPULATED**
- QEP_A header is connected to the motor shown page2.
- 48V Power ON (supply 48V to BOOSTXL-3PhGaNInv)
- Click: CCS->Debug
- Enable real time mode and click “Run” in CCS
- Set “EnableFlag” to 1 in the expressions window in CCS
- Set the variable named “RunMotor” to ‘RUN’ like below.
 - > Immediately, the value “RUN” is changed to “STOP”

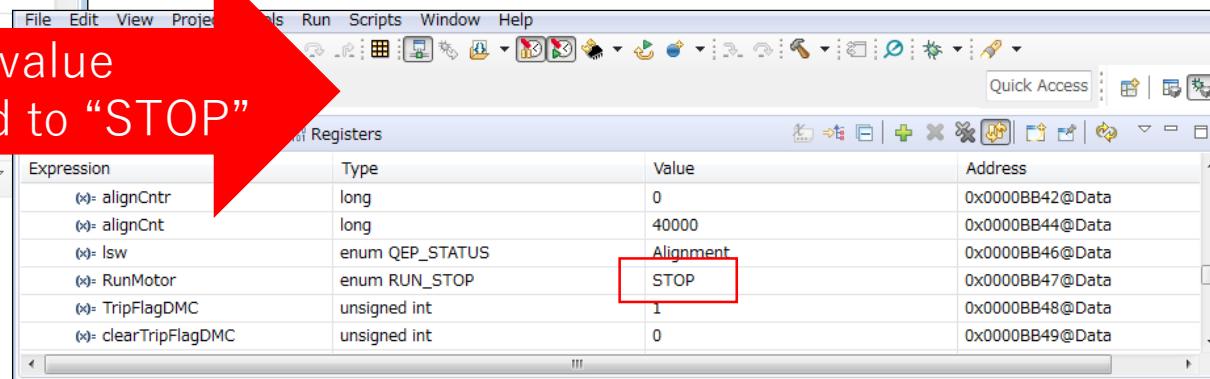
**Result:NG-> PWM generation is not found
(Motor remains STOP)**



NG
(PWM generation is not found)

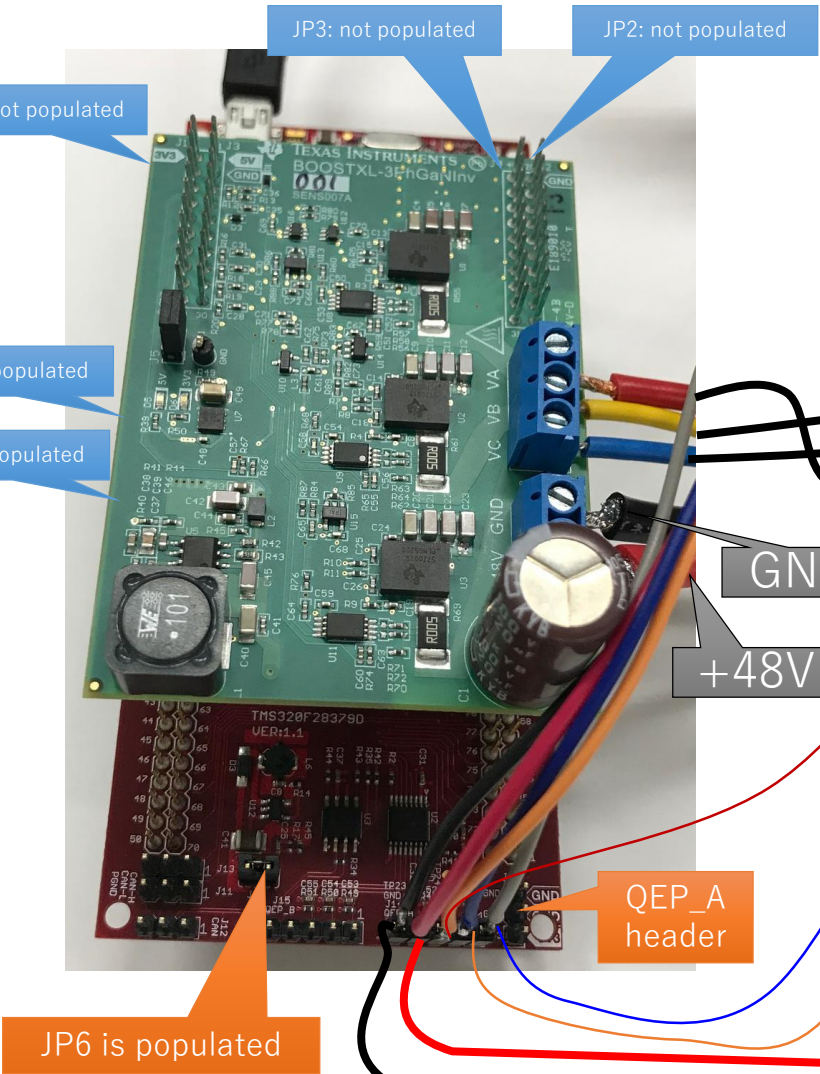


Immediately, the value
“RUN” is changed to “STOP”



FCL_LEVEL2

```
Getting Started FCL_SFRA_XL_DualServo-Settings.
38 */
39 #define MOTOR1_DRV GaN_BP //select between
40 #define MOTOR2_DRV GaN_BP //select between
41
42 #if MOTOR1_DRV != GaN_BP
43 #define MOTOR1_EXT_DEADBAND // To use dead
44 #endif
45 #if MOTOR2_DRV != GaN_BP
46 #define MOTOR2_EXT_DEADBAND // To use dead
47 #endif
48
49 /*-----
50 Following is the list of the Build Level choices
51 -----
52 #define FCL_LEVEL1 1 // Module c
53 #define FCL_LEVEL2 2 // Verify Abc, p
54 #define FCL_LEVEL3 3 // Verify closed
55 #define FCL_LEVEL4 4 // Verify s
56 #define FCL_LEVEL5 5 // Verify dual
57 #define FCL_LEVEL6 6 // Get SFRA plot
58
59 /*-----
60 This line sets the SAMPLING FREQUENCY to one of
61
62 #define SINGLE_SAMPLING 1
63 #define DOUBLE_SAMPLING 2
64
65 /*-----
66 Here below, pick current loop controller option
67 -----
68 //#define CMPLX_CNTLR 1
69 //#define PI_CNTLR 2
70
71 /*-----
72 This line sets the user choice to one of the av
73
74 #define BUILDLEVEL FCL_LEVEL2
```



Pin	Color	Signal
1	DRAIN x3	P DRAIN
2	N/A	N/A
3	GRN	COMM S-T
4	GRN/WHT	COMM R-S
5	GRY/WHT	COMM T-R
6	DRAIN x1	E DRAIN
7	BLK	GND
8*	BLU/WHT	ENC A~
9	16AWG BLK	PHASE R
10	16AWG RED	PHASE S
11	16AWG WHT	PHASE T
12	RED	+5VDC IN
13	BRN	ENC I
14	ORN	ENC B
15	BLU	ENC A
16*	ORN/WHT	ENC B~

