

7. Benchmarks

The following table lists the execution time in CPU cycles for the fastRTS library routines. These numbers assume that both the code and stack are in zero wait-state memory. These numbers include function-call/return overhead but do not include any cycles for setting up the input data or storing the result.

Function	FPUMathTables in zero-wait SARAM	FPUMathTables in single-wait Boot ROM
atan	47	51
atan2	49	53
cos	38	42
division	24	24
isqrt	25	25
sin	37	41
sincos	44	50
sqrt	28	28