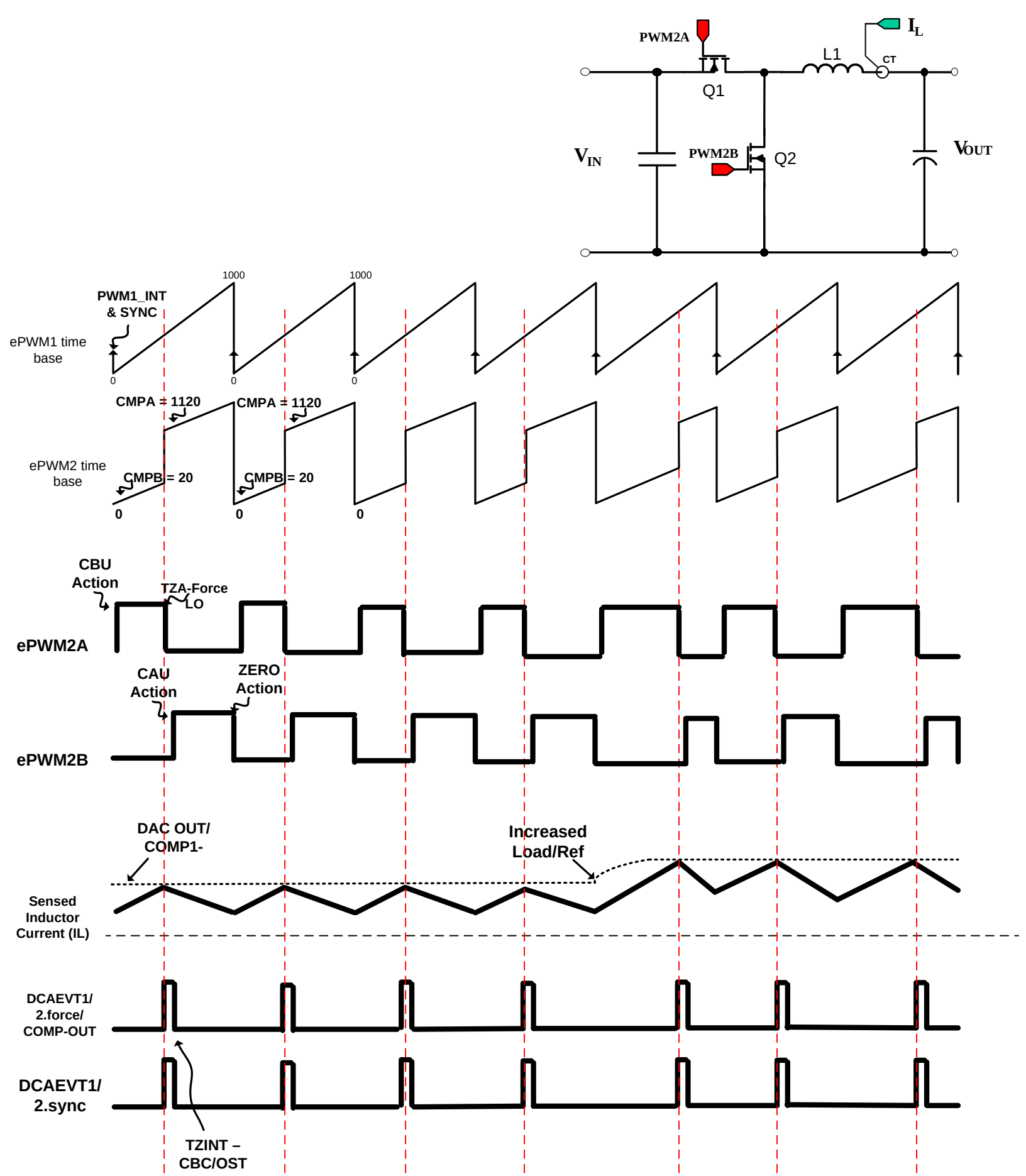

NOTE 1: Inside the TZ interrupt you will need to change the TBPHS2 register from 1100 to 0. Inside the PWM1 CTR = Zero Interrupt, you will need to change TBPHS2 register from 0 to 1100. There will be some software overhead here that will require careful switching/timing management for very low duty cycles/current reference commands.

NOTE 2: The second ISR is not needed. It is only used here for easy explanation. The PWM1 ISR should be generated at the exact correct time before CTR1 = PRD such that TBPHS2 can be changed here to 0 just before CTR1 = PRD event. Inside the same ISR TBPHS2 should then be changed to 1100 immediately after CTR1 = ZERO event has occurred.