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Project Explorer 33		B# 0	€ Expressions S			(1) 에 티 🔶 🗶 強國 🗂 러 👘 👘
<ul> <li>2837x RFFT /</li> <li>Binaries</li> <li>Includes</li> <li>FLASH_FAST</li> <li>RAM</li> <li>examples_st</li> <li>F2837xD_At</li> <li>F2837xD_Ct</li> </ul>			Expression © EFFTin1Buff (on freq Add new expression	Type unsigned int[1024] float	Value [2584,2315,2098,2037,2025] 50000.0	Address DxXD010000@Data DxXD00A00E@Data
<ul> <li>F2837xD_D(</li> <li>F2837xD_EF</li> <li>F2837xD_FF</li> <li>F2837xD_FF</li> </ul>	Move Rename F2 Import		🚳 Getting Started 🙁 NewTargetConfiguration.c	ccomi 🔒 main.c 🕫		🕱 Target Configurations % 🚿 🦑 🖨 🐡
<ul> <li>@F2837D_Gi</li> <li>@F2837D_Gi</li> <li>@F2837D_H</li> <li>@F2837D_H</li> <li>@F2837D_H</li> <li>@F2837D_H</li> <li>@F2837D_H</li> <li>@F2837D_V</li> <li>@F2837D</li></ul>	Export.     Show Build Settings.      Build Project     Clean Project     Clean Project     Clean Project     Clean Project     Clean Project     Clean Project     Alate Targets     Debug As     Restore from Local History     Team     Properties     Alt-Enter	Manage Set Adve > Build All Clean All Build Selected	201         RFF[f32]phase(hnd, rff1);           202         rendif // IPS3062300,170/	//Calculate phase puency component of signal frequency gov/the is only used for finding frequency gov/the sample spunce wave () () () () () () () () () ()		type filter text > Ge Forjects → Guse Defined K NewTargetConfiguration.comt
			313 } 314		•	Click the New button to create a new target configuration file. Click <u>here</u> to hide this message.
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			New Target Configuration.ccm		0 errors, 2 warnings, 0 others Description	Resource Path cified: "code, 2837x_RFFT_ADC, main.c /2837x_RFFT_ADC_F
G 2837x RJ						

## Rebuild project:

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F2837xD_DefaultISR.c	313 } 314					> 🧀 Projects		
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F2837xD_FPU_RFFT_ADC_RT_Ink.cmd	1316 return 1;	peare				R NewTargetConf	figuration.ccxml	
F2837xD_GlobalVariableDefs.c	317 }							
F2837xD_Gpio.c	318							
F2837xD_Headers_nonBIOS_cpu1.cmd	319// End of main							
F2837xD_PieCtrl.c	320							
F2837xD_PieVect.c	321//! \brief ADC Interrupt Service Ro 322//! The ISR will store each sampled							
F2837xD_SysCtrl.c	323//! raise the flag once the buffer		1					
F2837xD_usDelay.asm	324//1	15 1011						
a main.c	325interrupt void adcaIsr()							
R NewTargetConfiguration.ccxml (Active/Default)	326 {							
📄 rfft_in_out.graphprop	327 RFFTin1Buff[sampleIndex++] = Ad							
📄 rfft_mag_phase.graphprop	<pre>328 if(sampleIndex == (RFFT_SIZE -</pre>	1) ){						
📄 setupdebugenv.js	<pre>329 sampleIndex = 0;</pre>							
	<pre>330 flagInputReady = 1; 331 }</pre>							
	332							
	333 AdcaRegs.ADCINTFLGCLR.bit.ADCIN	T1 = 1: //clear INT1 flag						
	334 PieCtrlRegs.PIEACK.all = PIEACK							
	335 }	_						
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	Console :: Console :: Conso	28x_fpu_dsp_library.lib ^ D	errors, 2 warnings, 0 others escription & Warnings (2 items) & entry-point symbol other than "_c_int00" :	specified: "code_2837x_RFFT_ADO	c		C/C++ Problem	
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Power up the board and connected to ccs:

S1 boot switch (Launchpad 28379): 1/1/1

After uploading program to the flash:

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		Expression	Type unsigned int[1024] float	Value [0.0.0.0.0] 6.90840143e-41 (DEN)	Address 0x00010000@Data 0x0000A000@Data
grgc_main() at args_main.ca10.069572     grgc_int00 at optimizer (int00 does not contain frame information)     grgc_int00 does not contain frame information     grgc_int00 does not contain frame information)		•			
Final installinets ADD 1001 000 0000 probe_0/CPU2_CLA1 (Disconnected : Unknown)					
© Gening Stand № NewTrepsetConfiguration.com © Innic :: 2007/1 (55%), we show(512 - 50.5) and all its odd harmonics. Since the sampled 2007/1 waveform is not an integer multiple of the FF_SIZE, you will see some 2007/1 spectral leakage, a portion of the fundamental will leak into the fit bin. 210//1 one way to overcome this is to window the input before running the FFT on 211//1 it. 211//1 211//1					
<pre>214 { 215 // Locals 215 // Locals 216 uintEct1, j; 217 floatfreq = 0.0; 218 218 219 219 210 fALLOH 210 fALLOH 221 FloatFictOr(10.2)_t * %RemfuncsUndStart, (uint32_t * %RemfuncsLoadStart, 222 mecop((uint32_t), %RemfuncsLoadStare); 223 FPU_initPlan(); 234 FPU_initPlan(); 235 FPU_initPlan(); 235 FPU_initPlan(); 236 FPU_initPlan(); 237 FPU_initPlan(); 237 FPU_initPlan(); 238 FPU_iniPlan(); 238 FPU_initPlan(); 238 FPU_initPlan(); 238 FPU_init</pre>					
22:sendif //FLASH 226 227 FPU_initSystemClocks(); 228 229 FPU_initEpie(); 230					
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د		> 115712	+25 +50	+75 +100 +125 sample	+150 +175
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workspace_v7_RFFT_ADC - CCS Debug - 2837x_RFFT_ADC_RT/main.c - Code Composer Studio					- 0	×
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<ul> <li><terminated> NewTargetConfiguration.coml [Code Composer Studio - Device Debugging]</terminated></li> <li>2837x, RFFT_ADC_RT [Code Composer Studio - Device Debugging]</li> </ul>	Expression		Type unsigned int[1024]	Value [2500,2273,2080,2033,2021]	Address 0x00010000@Data	
Ver Des North Statuments XDS100/2 USB Debug Phote_0/CPU2_CLA1 (Disconnected : Unknown)     Preas Instruments XDS100/2 USB Debug Phote_0/CPU2_LLA1 (Disconnected : Unknown)     Preas Instruments XDS100/2 USB Debug Phote_0/CPU2_LLA1 (Disconnected : Unknown)     Preas Instruments XDS100/2 USB Debug Phote_0/CPU2_LLA1 (Disconnected : Unknown)	♥ Carter in Hourd 0+ Freq Add new eq		unsgned int[1024] float	[29002273200020332021-] 7421.875	0x0000A00E@Data	
Getting Started & NewTargetConfiguration.ccxml						
<pre>273 } 274 274 275 for [i-0; i &lt; RFF_SIZE/2; i++)[ 276 RFF[haseBuff[] = 0; //Clean up phase buffer 277 while(lagInputReady == 0)[; // Wait on ADC ISR to set the flag 281 while(lagInputReady == 0)[; // Wait on ADC ISR to set the flag 282 RFF_ade_f32(hm_ffft_adc); // Calculate real FFT (12-bit ADC input) 283 flagInputReady =0; // Kest the flag 284 RFF_f32_hase_RMSQ(hm_ffft); //Calculate nagnitude 286 RFF_f32_hase_RMSQ(hm_ffft); //Calculate magnitude 288 RFF_f32_hase_RMSQ(hm_ffft); //Calculate magnitude 289 RFF_f32_hase_RMSQ(hm_ffft); //Calculate magnitude 280 RFF_f32_hase_RMSQ(hm</pre>						
296 //of one component frequency signal; in this example it gives the						~
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2837x_RFFT_ADC_RT		3500				
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Now I can see the frequency of input signal is correct and = 7421Hz.

Next	step:	load	symbo	ls:

workspace_v7_RFFT_ADC - C	CS Debug - 2837x_RFFT_ADC_RT/main.c - Code	e Composer Stud	lio					-	o ×
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🛩 🧬 Texas Instruments XI	II Suspend	Alt+F8	load Symbols			float	7421.875	0x0000A00E@Data	
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c_int00() at boot2	🥵 Go Main				-				
📌 Texas Instruments XE	Reset	,	C:\ti\libs\\FLASH_FASTRTS\2837x_RFFT_ADC_F C:\ti\libs\\FLASH_FASTRTS\2837x_RFFT_ADC_F						
P Texas Instruments XE	Step Into	F5	C:(II)IDS((FLASH_PASTRIS(2037)_RFF1_ADC_F	KI.OUt	_				
📌 Texas Instruments XE	Step Over	F6	(n)						
	Assembly Step Into	Ctrl+Shift+F5							
	Assembly Step Over	Ctrl+Shift+F6							
273 }	R Step Return	F7							~ =
	🕅 Run to Line	Ctrl+R							
275 for (i=0; i <		Ctrl+F8							
276 RFFTphase 277 }	Step Into Selection		er						
277 }	Clock	>							
279 while(1){	Advanced	>							
280 while(fla <sup>4</sup>	🇞 Debug	F11	e flag						
281	Debug History	>							
282 RFFT_adc_ 283 flagInput	Debug As	>	t ADC input)						
294	Debug Configurations								
285 #ifdef TMS320C2	<ul> <li>Toggle Breakpoint</li> </ul>	Ctrl+Shift+B	e project						
200	Skip All Breakpoints	Ctrl+Alt+B							
287 RFFT_f32_	Remove All Breakpoints								
	Breakpoint Types New Breakpoint (Code Composer Studio)								
		te magnitude	1						
	hase(hnd rfft); //Calcula								100 C
292 #endif //TMS320C									
293									
294 //Find out	the maximum frequency component of	signal freq	uency						
	t signal. This algorithm is only us omponent frequency signal; in this								~
( // OF OHE C	ouponent frequency signal, in this	example it g	ives the						
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2837x_RFFT_ADC_RT					3500 ]		and the group of the		GE18   GE2
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			e, the other core should not execute fr	rom share	2700 -		$\Lambda / \Lambda / \Lambda / \Lambda / \Lambda$		
	ading Memory Block at 0x0 on Page 6				2300	/ \ / \ / \ /	$\Lambda / \Lambda / \Lambda / \Lambda / \Lambda$		$\Lambda / \Lambda / I$
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				,			sample		
1									

Now, pressing mechanical button reset on the board :

## S1 boot switch (Launchpad 28379): 1/1/1



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Pexas Instruments XDS100v2 USB Debug Probe_0/C28xx_CPU2 (Disconnected : Unknown) Pexas Instruments XDS100v2 USB Debug Probe_0/CPU2_CLA1 (Disconnected : Unknown)				
tting Started 🕱 NewTargetConfiguration.ccxml 🗟 main.c 🔅				
<pre>for (i=0; i &lt; RFFT_SIZE/2; i++){     RFFTphaseBuff[i] = 0; //Clean up phase buffer</pre>				
}				
<pre>while(1){     while(flagInputReady == 0){}; // Wait on ADC ISR to set the flag</pre>				
// before proceeding				
<pre>RFFT_adc_f32(hnd_rfft_adc); // Calculate real FFT (12-bit ADC input) flagInputReady = 0; // Reset the flag</pre>				
ifdefTMS320C28XX_TMU //defined whentmu_support=tmu0 in the project				
// properties RFFT_f32_mag_TMU0(hnd_rfft); //Calculate magnitude				
RFFT_f32_phase_TMU0(hnd_rfft); //Calculate phase else				
RFFT_f32_mag(hnd_rfft); //Calculate magnitude				
RFFT_f32_phase(hnd_rfft); //Calculate phase endif //_TMS320C280X_TMU_				
//Find out the maximum frequency component of signal frequency				
<pre>//component signal. This algorithm is only used for finding frequency //of one component frequency signal; in this example it gives the</pre>				
	🗏 🖻 🔹 📑 👻 Single Time -1	1 22	開宇田事・会・気気。	£ • Ø <mark>\$* Ø %</mark> 10 <b>8</b> 3 • <b>5</b> 10 11
FFT_ADC_RT CPU1: GEL Output:	3500			ΛΛΛ
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k_C_0: Trouble Reading Memory Block at 0x0 on Page 0 of Length 0xc8	2300 -		/ \ . / \& / ¥\ / \&	$/ \langle N \rangle \langle N \rangle \langle N \rangle$
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