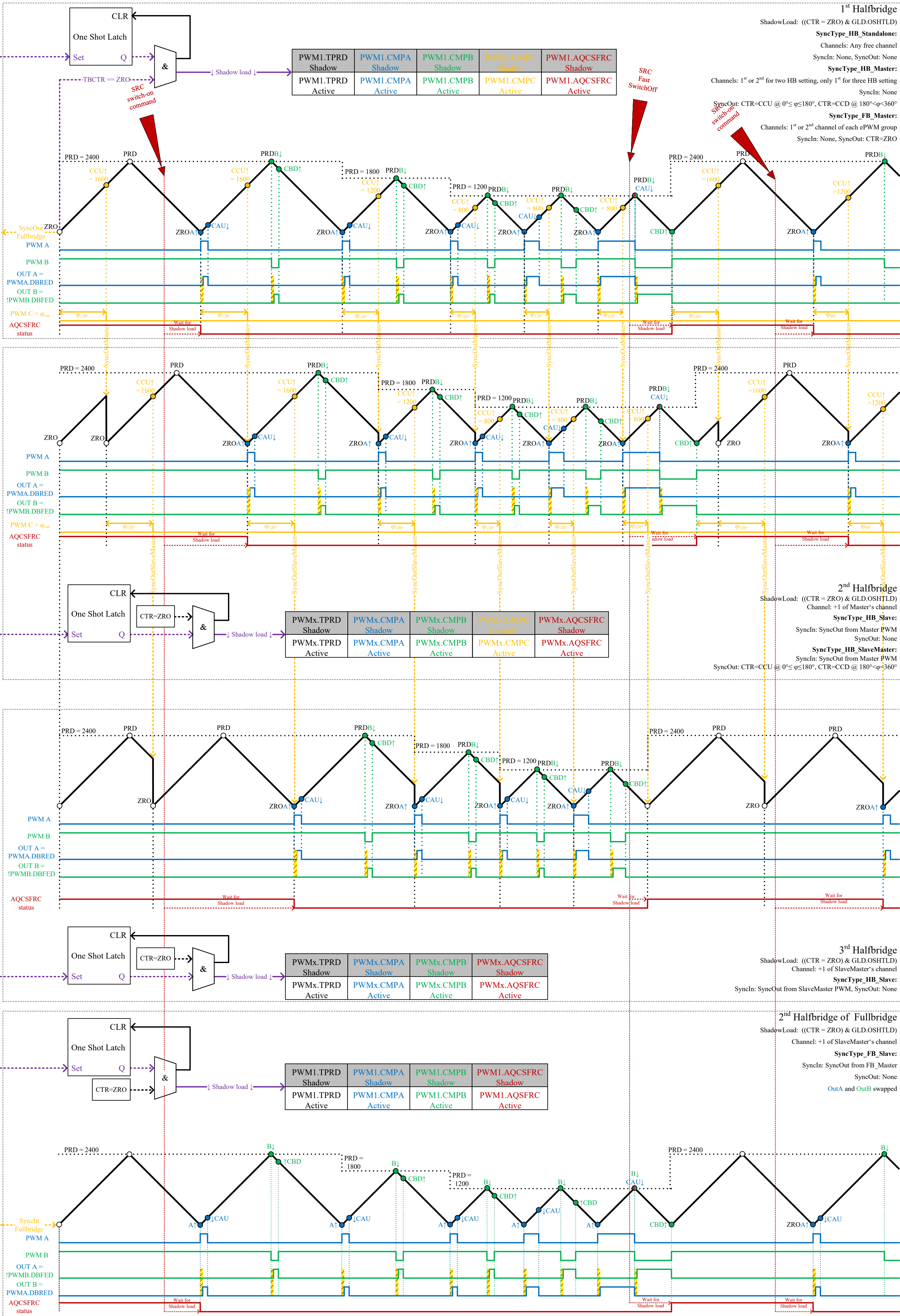




Synchronized_PWM_Interleaved:

PWM Pattern for Solo HalfBridge, 2 or 3 synchronized Halfbridges and Fullbridge