DesignDRIVE Development Kit IDDK v2.2 - User's Guide





2015

C2000 Systems and Applications

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DesignDRIVE is a single hardware and software platform that makes it easy to develop and evaluate solutions for many industrial drive and servo topologies. The DesignDRIVE kit and example software offer an easy path to begin exploring a wide variety of motor types, sensing technologies, encoder standards and communications networks, as well as easy expansion to develop with real-time Ethernet communications and functional safety topologies, enabling more comprehensive, integrated system solutions.

With C2000 DesignDRIVE Software and Kit (IDDK), TI wants to help you spend more of your time differentiating your product in your core areas and less of it evaluating new technologies that will eventually become table stakes in the industry. Using DesignDRIVE will ultimately help you get to market faster, with a more valuable product.

Based on the real-time control architecture of TI's C2000 microcontrollers, DesignDRIVE is ideal for the development of industrial inverter and servo drives used in robotics, computer numerical control (CNC) machinery, elevators, materials conveyance and other industrial manufacturing applications.

1 Introduction

This guide explains the steps needed to run the motor with IDDK using the software supplied through controlSUITE. ControlSUITE is a suite of example projects using TI's C2000 devices, containing technical literature that includes software, to verify the stated functional objective of the project. It is an open source code from Texas Instruments supporting C2000 devices and can be downloaded from <u>http://www.ti.com/tool/controlsuite</u>.

The following projects are currently available for IDDK based on TMS320F28377x MCU.

• IDDK_PM_Servo_F2837x: Sensored Field Oriented Control of Permanent Magnet Motor

Project features:

- Position, Speed and Torque control loops
- On-chip Position Sensor Support: Resolver, Incremental Encoder (QEP)
- Simultaneous Current Sensing Support for: Fluxgate/Hall, Delta-Sigma, Shunt

It is important for the user to read the kit's Hardware Reference Guide and understand all the safety measures needed to be taken to work with the kit. When ControlSUITE is installed, the Hardware Reference Guide can be found at:

controlSUITE\development_kits\TMDSIDDK_v1.0\~Docs\

And, the software can be found at:

controlSUITE\development_kits\TMDSIDDK_v1.0\IDDK_PM_Servo_F2837x

Please read this document and the Hardware Reference Guide thoroughly before attempting to use the kit the first time.

WARNING



This EVM is meant to be operated in a lab environment only and is not considered by TI to be a finished end-product fit for general consumer use.

This EVM must be used only by qualified engineers and technicians familiar with risks associated with handling high voltage electrical and mechanical components, systems and subsystems.

This equipment operates at voltages and currents that can result in electrical shock, fire hazard and/or personal injury if not properly handled or applied. Equipment must be used with necessary caution and appropriate safeguards must be employed to avoid personal injury or property damage.

It is the user's responsibility to confirm that the voltages are identified and understood, prior to energizing the board and or simulation. When energized, the EVM or components connected to the EVM should not be touched.

Isolation transformers must be used when connecting grounded equipment to the EVM.

2 Hardware Configuration

To experiment with IDDK for digital motor control, the following components are needed:

- An IDDK EVM
- TMDXCNCD28377D
- USB-B to A Cable
- PMSM motor for evaluation
 - Not included with TMDXIDDK377D
 - o Included with TMDXIDDK377D-MTR-BNDL bundle
 - Also available standalone from TI eStore, part # HVPMSMMTR
- An incremental encoder or resolver; (QEP included with motor available on TI eStore)
- An external isolated 15V power supply needed for MCU code development (preferably with a barrel connector commonly referred to as DC jack)
- A high voltage DC power supply (isolated)
- PC with Code Composer Studio (CCSv6 or greater) installed;
- Additional instruments such as an oscilloscope, a digital multimeter, a current sensing probe and a function generator.

The experimental setup and connection are illustrated in the following sections. For details about the kit hardware, refer to the Hardware Reference Guide located at

controlSUITE\development_kits\TMDSIDDK_v1.0\~Docs\

For the schematic details of IDDK EVM, refer to the schematic file located at controlSUITE\development_kits\TMDSIDDK_v1.0\DDK_HwDevPkg\DDK_HwDevPkg_v2.2.

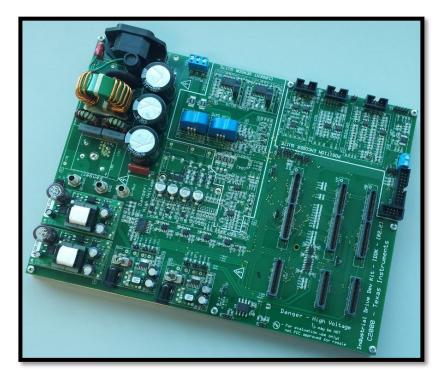


Figure 1 DesignDRIVE Kit (IDDK)

2.1 Hardware Layout of IDDK

The IDDK is designed by integrating various function specific macro blocks as shown in fig.2 below. Following is the list of all macros with description of what each is responsible for on the board.

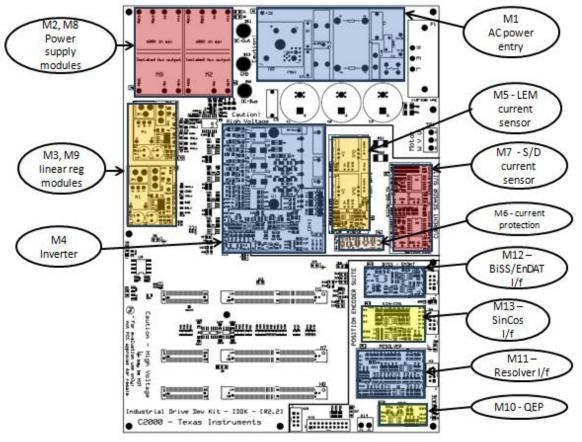


Figure 2 Layout of IDDK EVM with its functional macros

- [Main] processor slots, jumpers, inter processor communications and DACs and all sections not covered by any other macro
- [M1] AC power entry takes AC power from the wall/mains power supply and rectifies it. This can then be used as the DC bus for the inverter.
- [M2] Auxiliary power supply-1, 400V to 5V and 15V module, can generate 15V, 5V power for the inverter section of the board from rectified AC power. However, through proper jumper settings, it can be used to provide 15V to the entire board.
- [M8] Auxiliary power supply-2, 400V to 5V and 15V module, can generate 15V, 5V power for the control section of the board from rectified AC power. However, through proper jumper settings, it can be configured to provide 15V to the entire board.
- [M3] DC power Entry 1 generates 15V, 5V and 3.3V for the HV section of the board from DC power fed through the DC-jack using an external or [M2] power supply. Through proper jumper settings, this can power the entire board.
- [M9] DC power Entry 2 generates 15V, 5V and 3.3V for the control section of the board from DC power fed through the DC-jack using an external or [M8] power supply. Through proper jumper settings, this can power the entire board.

- [M4] Three-phase inverter, to enable control of high voltage 3-phase motors.
- [M5] Flux gate current sensor module that provides isolated voltage feedback of motor phase currents V and W.
- [M6] Over current protection module that generates a TRIP signal to shutdown the inverter in the event of over current through the motor.
- [M7] Sigma-delta interface module that provides motor phase current feedback to the CPU for digital control of motor
- [M10] Interface module that translates 5V logic signals from an incremental encoder into 3.3V for the C2000 and its QEP peripheral
- [M11] Analog interface module to work with RESOLVER position sensor
- [M12] Digital interface module to work with a BISS or EnDAT position encoder
- [M13] Analog interface module to work with Sin/Cos position encoder

In this document, each component is referenced with their macro number in the brackets followed by a dash and the reference number. For example,

- **[M3]-J1** refers to the jumper J1 located in the macro M3.
- [Main]-J1 refers to the jumper J1 located on the board, but outside of the macro blocks defined above.

2.2 IDDK Power Supplies

Typical of motor drive platforms, IDDK has a low voltage domain represented by the controller and a high voltage domain represented by rectifier and inverter

2.2.1 Low Voltage Power Domain

This represents the 15V, 5V and 3.3V needed to power the MCU, logic, sensing and driver circuits on IDDK. Power input for this domain can be provided by:

- 1. Connecting an isolated 15V DC power supply to the DC Jack ([M3]-JP1 / [M9]-JP1) present on the DC Power entry Macro.
- 2. An auxiliary power supply module [M2] / M[8]) present on the board that can generate 15V and 5V DC from the high voltage DC link.
 - Since the input voltage range of this module is 90V-400V, auxiliary modules will not be suitable if the DC bus voltage can go below 90V during the tests. In such cases, option 1 described above is recommended.
- 3. [M3] and [M9] are identical macros that provide the same set of output voltages, which can be configured to power the entire controller or just the gate drives of the inverter, depending on choice as explained in the Hardware Guide.

Default configuration of the kit :-

- [M9] is set up as low voltage power source
- Both control GND and power GND planes are connected

For user applications requiring a COLD control GND :-

Shunt current sensing method cannot be used. Refer to Section 3.7 on Power Supplies and GND plane configurations in the Hardware Guide for details

The control GND and power GND can be separate or tied together depending on need. If they are tied together, then obviously the control GND will be HOT, otherwise control GND will be COLD. Refer to the Hardware Guide for details.

For safety and convenience during general code development, testing and verification, it is preferable to use an external isolated 15V power supply to power the control section of the board through DC power entry macro [M9] (or [M3]).

2.2.2 High Voltage Power Domain

This represents the DC link section powering the inverter that generates three phase voltages for the motor. There are two ways to provide this voltage to the inverter, as given below:

- 1. Connecting an **external isolated variable DC power supply** (Max 350V) using banana jacks to [Main]-BS2 and [Main]-BS3 of IDDK. Using this power supply is recommended during experiments. To power the auxiliary power supply modules [M2] / M[8], connect [Main]-BS3 and [Main]-BS1
- Connecting the kit to AC Mains through P1 (110V* or 220V AC). An on board rectifier converts AC to DC. For safety, use of a variac (variable AC transformer) and isolator is recommended when using this power source.

NOTE: Keep all power supplies at zero unless directed to energize

* Note that the 3-ph Induction motors are typically rated at 220V AC, so a 320V DC-bus voltage is needed. Thus, when using 110V AC power source to generate the DC Bus for the inverter (150V), the motor can run properly up to a certain speed and torque range without saturating the PID regulators in the control loop. As an option, the user can directly connect a DC power supply.



Note that the ground planes of both the power domains can be same or different depending on hardware configuration, hence *proper isolation requirements must be met* before connecting any test equipment with the board.

Review the GND connections in the Hardware Guide before powering the board

3 Test Hardware Setup Instructions

The experiment uses a single common **HOT GND**, connecting the control and power circuits, as shown in line entry 4 of Table 2 in IDDK Hardware Reference Guide, and is set as the default configuration.

Follow the steps given below for setting up the experimental hardware

- Ensure default configuration Make sure that jumpers [Main]-J6, [Main]-J7 and [Main]-J8 in front of macro M9 and that resistors [Main] R8-R13 are populated, and that GND plane resistors R14 and R15 are mounted as shown in Figure 13 and Figure 14 of Power Supplies Section of Hardware Reference Guide
- 2. Unpack the TMDXCNCD28377D control card and slide it down in the connector slot of [Main]-H1. Push down vertically using even pressure on both ends of the card until it cannot slide down further. (to remove the card spread open the retaining clips, if present, and pull the card out applying even force at the far edges)
- 3. Connect a USB cable to connector J1 on the control card. The control card isolates the JTAG signals between the C2000 device and the computer. LED D2 on the control card should light.
- 4. Ensure that toggle switch [M9]-SW1 is in "Int" position. Connect an isolated 15V DC power supply to [M9]-JP1.
- 5. Turn on toggle switch [M9]-SW1. Now [M9]-LD1 should turn on. Note that more LEDs on the control card should light up indicating that the control card is receiving power from the board.
- 6. Note that the motor should be connected to [Main]-TB1 terminals only after finishing with the first incremental build step.
- 7. Note the DC Bus power should only be applied when instructed to do so. The two options to get DC Bus power are discussed below
 - 1.To use an external variable DC power supply, set the power supply output to zero and connect [Main]-BS2 and [Main]-BS3 to the + and terminal of the DC power supply respectively as shown in Figure 3 below.
 - 2.To use AC Mains Power, connect [Main]-BS1 to [Main]-BS2 using banana plug cord. Then connect one end of the AC power cord to [Main]-P1. Connect the other end to the output of a variac after ensuring that its output is set to zero and that it is connected to the wall supply through an isolator as shown in Figure 4 below.



WARNING: DC bus capacitors will remain charged for a long time after the mains supply is disconnected. Use caution!

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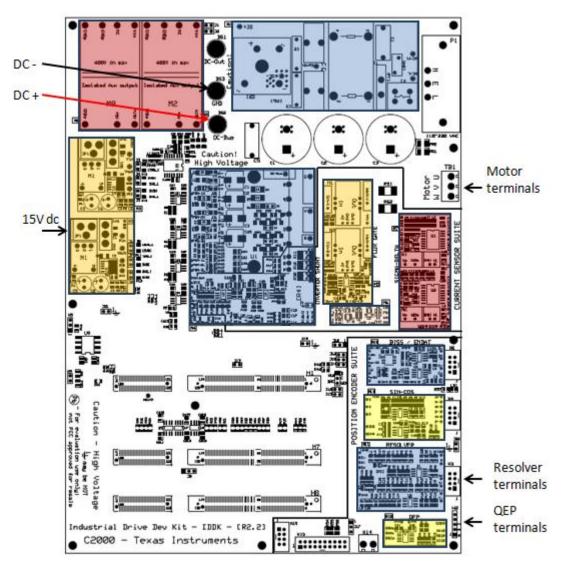


Figure 3 Connection diagram with external variable DC power supply providing DC bus voltage

WARNING: DC bus capacitors will remain charged for a long time after the mains supply is disconnected. Use caution!

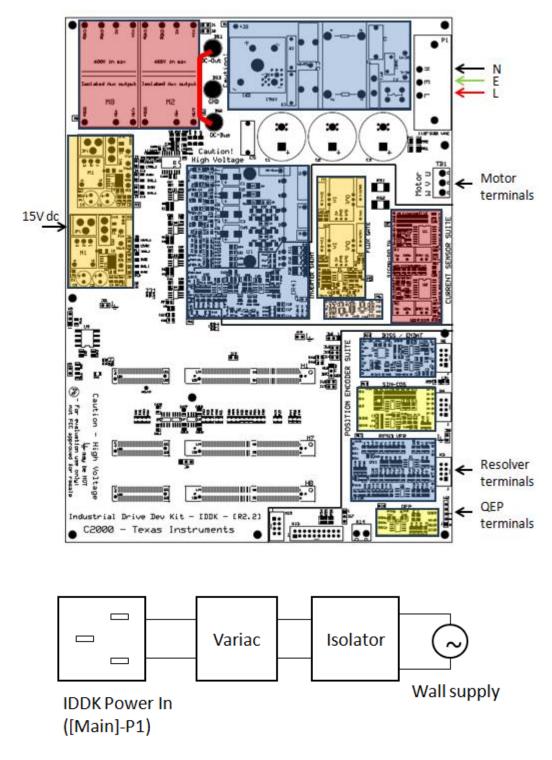


Figure 4 Connection diagram with AC input



WARNING: DC bus capacitors will remain charged for a long time after the mains supply is disconnected. Use caution!

4 Software Setup for IDDK Projects

Installing Code Composer and controlSUITE

- 1. If not already installed, please install Code Composer v6.x or later from http://www.ti.com/tool/CCSTUDIO
- 2. Go to <u>http://www.ti.com/controlsuite</u> and run the controlSUITE installer. Allow the installer to download and update any automatically checked software for C2000.

Setup Code Composer Studio to Work with TMDXIDDK377D

- 3. Open "Code Composer Studio". Note that this document assumes version 6 or later.
- 4. Once Code Composer Studio opens, the workspace launcher may appear that would ask to select a workspace location,: (please note workspace is a location on the hard drive where all the user settings for the IDE i.e. which projects are open, what configuration is selected etc. are saved, this can be anywhere on the disk, the location mentioned below is just for reference. Also note that if this is not your first-time running Code Composer the dialog below may not appear)
 - Click the "Browse..." button
 - Create the path below by making new folders as necessary.
 - "C:\c2000 projects\CCSv6_workspaces\IDDK_workspace"
 - Uncheck the box that says "Use this as the default and do not ask again".

Click "OK"	
💱 Workspace Launcher	×
Select a workspace Code Composer Studio stores your projects in a folder called a workspace. Choose a workspace folder to use for this session.	
Workspace: C:\c2000 projects\CCSv6_workspaces\IDDK_workspace	Browse
Use this as the default and do not ask again	OK Cancel

Figure 5 Workspace Launcher

- 5. This will open a 'Getting Started' tab with links to various tasks from creating a new project, importing an existing project to watching a Tutorial on CCS. The user can click the 'Import Project' Icon that will skip the procedure to step 10 in this list. Or, we can close the 'Getting Started' Tab and go to next step.
- 6. Next we will configure Code Composer to know which MCU it will be connecting to. This is done by setting up the 'Target Configuration'. All these are already set up and configured in "xds100v2_F2837x.ccxml" provided as part of the files in project, and the user can skip to step 9. However, for general information regarding setting up this configuration file, steps 6, 7 and 8 can be used.
- 7. A new configuration file can be set by clicking "View → Target Configuration. This will open the Target Configuration window. In this window, click on ^{SC}. Give a name to the new configuration file

depending on the target device. If "Use shared location" checkbox is checked, then this config file can be stored in a common location by CCS for use by other projects as well. Then click Finish.

- 8. This should open up a new tab as shown in Figure 6. Select and enter the options as shown:
 - Connection Texas Instruments XDS100v2 USB Emulator (or)

Texas Instruments XDS100v2 USB Debug Probe

- Device the C2000 MCU on the control card, TMS320F28377D, for example
- Click Save and close

Basic		
General Setup		Advanced Setup
This section describes the general configuration about the target.		
Connection Texas Instruments XDS 100v2 USB Debug Probe	•	Target Configuration: lists the configuration options for the target.
Board or Device 320f28377d	a	Save Configuration
TMS320F28377D		Save
		Test Connection
		To test a connection, all changes must have been saved, the configuration file contains no errors and the connection type supports this function.
		Test Connection
		Alternate Communication
		Uart Communication
Note: Support for more devices may be available from the update manager.	×	To enable host side (i.e. PC) configuration necessary to facilitate data communication over UART, target application needs to include a monitor implementation. Please check example project in TI Resource Explorer. If your target application leverages TIATOS, then please check documentation on how to enable Uart Monitor module.
		To add a port in the target application for Uart Monitor, click the Add button.



- 9. Click "View->Target Configurations". In the "User Defined" section, find the file that was created in step 6 and 7. Right-click on this file and select "Set as Default". To use the configuration file supplied with the project, click "View->Target Configurations, then expand "Projects-→ IDDK_PM_Servo_F2837x and right-click on the file "xds100v2_F2837x.ccxml" and "Set as Default". This tab also allows you to reuse existing target configurations and link them to specific projects.
- 10. Add the motor control projects into the current workspace by clicking "Project->Import CCS Project".
 - Select the IDDK project by browsing to:

"C:\TI\ controlSUITE\development_kits\TMDSIDDK_v1.0\IDDK_PM_Servo_F2837x"

💱 Import CCS Eclipse Projec	ts	_ _ ×			
Select CCS Projects to Import Select a directory to search for existing CCS Edipse projects.					
© Select search-directory:	:\TI\controlSUITE\development_kits\TMDSIDDK_v1.0\IDDK_PM_Servo_F2837x	Browse			
C Select archive file:		Browse			
Discovered projects:					
🗹 👕 IDDK_PM_Servo_F2	837x [C:\TI\controlSUITE\development_kits\TMDSIDDK_v1.0\IDDK_PM_Servo_F2837x]	Select All			
		Deselect All			
Automatically import referenced projects found in same search-directory					
Copy projects into workspace					
Open the Resource Explorer and browse available example projects					
?	Finish	Cancel			

Figure 7 Adding IDDK project to workspace

 If there are multiple projects in this directory, then the user can click and choose the projects to import, and then click Finish. This will copy all the selected projects into the workspace. In this figure above, we have one project only, select it and click finish.

Configuring a Project

- 11. Assuming this is your first time using Code Composer, the xds100v2-F2837x should have been set as the default target configuration. Verify this by viewing the xds100v2-f2837x.ccxml file in the expanded project structure and a [Active/Default] status written next to it. By going to "View-> Target Configurations" you may edit existing target configurations or change the default or active configuration. You can also link a target configuration to a project in the workspace by right clicking on the Target configuration name and selecting Link to Project.
- 12. The project can be configured to create code and run in either flash or RAM. You may select either of the two, however for lab experiments we will use RAM configuration most of the time and move to the FLASH configuration for production. As shown in Figure 8, right-click on an individual project and select Active Build Configuration-> F2837x RAM configuration.

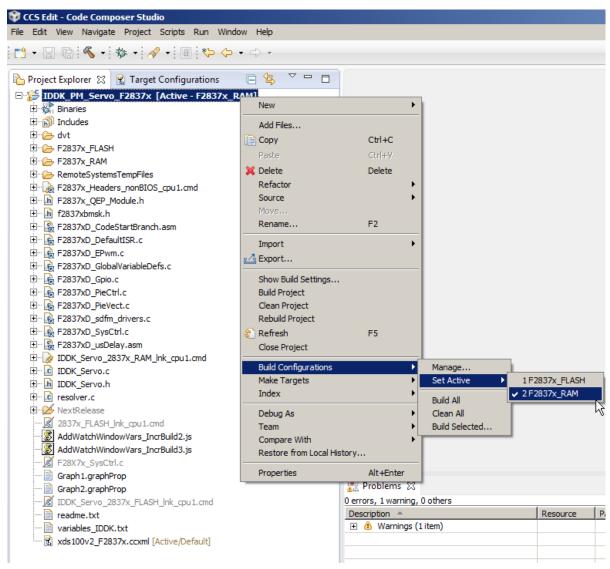


Figure 8 Selecting the F2837x_RAM configuration

Build and Load the Project

- 13. The TI motor control software is provided with incremental builds where different components / macro blocks of the system are pieced together one by one to form the entire system. This helps in step by step debug and understanding of the system. From the CCS Edit Perspective, open the file [Project-Name]-Settings.h and make sure that BUILDLEVEL is set to LEVEL1 and save this file. After we test build 1, this variable will need to be redefined to move on to build 2, and so on until all builds are complete.
- 14. Open the [Project-Name].c file and go to the function MainISR(). Locate the following piece of code in incremental build 1 and confirm that the Datalog buffers are pointing to the right variables. These Datalog buffers are large arrays that contain value-triggered data that can then be displayed to a graph. Note that in other incremental builds different variables may be put into this buffer to be graphed. Following is an example where the datalog are pointed to the space vector generator module.

dval1=rg1.Out; dval2=svgen1.Ta; dval3=svgen1.Tb; dval4=svgen1.Tc;

- 15. Now Right Click on the Project Name and click on "Rebuild Project" and watch the Console window. Any errors in the project will be displayed in the Console window.
- 16. On successful completion of the build click the ³⁵ "Debug" button, located in the top-left side of the screen, and the following window may pop up, if it is the first time, requesting select which of the two CPUs in F28377x to connect to. Choose CPU1 by clicking the box next to CPU1.

💱 Launching Debug Session		_ _ ×
The target configuration has changed since the la	ast debug session.	
Please select the CPUs to load the program on:		
 Texas Instruments XDS 100v2 USB Emulator Texas Instruments XDS 100v2 USB Emulator 		
	Select All D	eselect All
 Create a debug group for selected cores Make the group synchronous 		
?	ОК	Cancel

Figure 9 Selecting the CPU(s) to connect

- 17. The IDE will now automatically connect to the target, load the output file into the device and change to the Debug perspective.
- 18. Click "Tools->Debugger Options->Program / Memory Load Options". You can enable the debugger to reset the processor each time it reloads program by checking "Reset the target on program load or restart" and click "Remember My Settings" to make this setting permanent.
- 19. Now click on the "Enable silicon real-time mode" button ¹⁰/₁₀ which also auto selects "Enable polite real-time mode" button ¹⁰/₁₀. This will allow the user to edit and view variables in real-time. Do not reset the CPU without disabling these realtime options!
- 20. A message box *may* appear. If so, select YES to enable debug events. This will set bit 1 (DGBM bit) of status register 1 (ST1) to a "0". The DGBM is the debug enable mask bit. When the DGBM bit is set to "0", memory and register values can be passed to the host processor for updating the debugger windows.

Setup Watch Window & Graphs

Click: View \rightarrow Expressions on the menu bar to open a *watch window* to view the variables being used in the project. Add variables to the watch window as shown below. It uses the number format associated with variables during declaration. You can select a desired number format for the variable by right clicking on it and choosing. Figure below shows a typical expressions window.

(x)= Variables 🚱 Expressions 🖇	। ११११ Registers 🐁 🕫 🖪	3 🕂 🗶 🎉 🤴	0 🔅 🔁 🔁
Expression	Туре	Value	Address
(×)= EnableFlag	unsigned int	0	0x0000B010@Data
(×)= IsrTicker	unsigned long	0	0x0000B03E@Data
(×)= SpeedRef	float	0.15	0x0000B09A@Data
(×)= rc1.TargetValue	float	0.0	0x0000B12C@Data
(×)= rc1.SetpointValue	float	0.0	0x0000B136@Data
(×)= rg1.Out	float	0.0	0x0000B0F0@Data
(×)= VdTesting	float	0.0	0x0000B09E@Data
(×)= VqTesting	float	0.1	0x0000B094@Data
(×)= offset_SDFM1	float	0.0	0x0000B070@Data
(×)= offset_SDFM2	float	0.0	0x0000B06E@Data
(×)= offset_lemV	float	0.0	0x0000B074@Data
(×)= offset_lemW	float	0.0	0x0000B072@Data
🕀 휻 svgen 1	struct <unnamed></unnamed>	{}	0x0000B1C0@Data
🐈 Add new expression			

Figure 10 Configuring the Expressions Window

Alternately, a group of variables can be imported into the Expressions window, by right clicking within Expressions Window and clicking Import, and browse to the .txt file containing these variables. Here, browse to the root directory of the project and pick 'Variables_IDDK_Level1.txt' and click OK to import the variables shown in Figure 10.

21. Click on the Continuous Refresh button in the watch window. This enables the window to run with real-time mode. By clicking the down arrow in this watch window, you may select "Customize Continuous Refresh Interval" and edit the refresh rate of the watch window. Note that choosing too fast an interval may affect performance.

22. The datalog buffers point to different system variables depending on the build level. They provide a means to visually inspect the variables and judge system performance. Open and setup time graph windows to plot the data log buffers as shown below. Alternatively, the user can import graph configurations files in the project folder by clicking: Tools -> Graph -> DualTime... and select import and browse to the following location

C:\TI\ControlSUITE\developement_kits\TMDSIDDK_v1.0\IDDK_PM_Servo_F2837x

and select Graph1.graphProp, the Graph Properties window should now look like the Figure 11. Hit OK, this should add the Graphs to your debug perspective. Click on Continuous Refresh button B on the top left corner of the graph tab.

Note: If a second graph window is used, you could import Graph2.prop, the start Addresses for this should be DBUFF_4CH3 and DBUFF_4CH4.

Note: The default dlog.prescaler is set to 5 which will allow the dlog function to only log one out of every five samples.

Note: The default dlog.trig_value should be set to the right value to generate trigger for the plot as in oscilloscopes.

iraph Properties		
Property	Value	
 Data Properties 		
Acquisition Buffer Size	200	
Dsp Data Type	32 bit floating point	
Index Increment	1	
Interleaved Data Sources	□ false	
Q_Value	0	
Sampling Rate Hz	1	
Start Address A	DBUFF_4CH1	
Start Address B	DBUFF_4CH2	
 Display Properties 		
Axis Display	✓ true	
Data Plot Style	Line	
Display Data Size	200	
Grid Style	No Grid	
Magnitude Display Scale	Linear	
Time Display Unit	sample	
Use Dc Value For Graph A	🗌 false	
Use Dc Value For Graph B	🗌 false	
I	mport Export OK Cancel	

Figure 11 Graph window settings

Run the Code

- 23. Run the code by pressing Run Button^{\square} in the Debug Tab.
- 24. In the Expressions window, set the variable 'EnableFlag' to 1.
- 25. The project should now run, and the values in the graphs and watch window should continuously update. Below are some screen captures of typical CCS perspectives while using this project. You may want to resize the windows according to your preference.
- 26. Once complete, reset the processor image (Run->Reset->CPU Reset) and then terminate the debug session by clicking (Run->Terminate). This will halt the program and disconnect Code Composer from the MCU.

			Edit perspect	ive	
🖞 CES Lift - IDOK_Servic_FJR37x/IDOK_Service - Code Comprise	r Mardin		and the second	1	_ (# X
the full New Navapate Present Surgits Kurs window Help				K	3.200
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🔁 Project Explorer 22 🔮 Target Configurations 👘 2	1 100K,Server,c 11		🕞 IDDK, Serve, 2837x, RAM, Ink, cpub.cmd 🛛 🐁 I	ecot28.inc 💽 0x3H56a	
E 15 OnvestMersoriess / 2837x	System Name:	IDDK Seve Control			
BIDDK_Servo_F2837x [Active - F2837x_RAH]	A File Name:	IDDK_Servo.C			
H - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	6 Target:	Rev 1.1 of the #2637x control card			
18 (20) 0v1 18 (20) F2837X_FLASH	7 Il Author I	C2000 Systems Lab, 05th March 2015			14
II LO F2837X_PLAD	-9				-
18 👝 RenoteGystemsTempFiles	10 Description:	Project uses two ISRs 1. Resolver ISR triggered by ADC INT, this is d	direction and the second second second second	and an Inderson	44
18 P2837x_Headers_non8005_cpu1.cmd	32	2. Inverter ISR rate is 10khz and is triggered	by EPMI1, reason to choose EPAPII is becau	ie of the 50 reading	
HI B F2837x_QEP_Module.h	13	EPAPEL provides periodic sync resets to the SDF	H ecduls,		-
 B 2837xbmsk.h B F2837xD. CodeStartBranch.aum 	34	after each resat the SDPH data is ready after 3			45
II S 728376D_DefaultSR.c	16				
18 92837xD_EPwm.c		ader files used in the main function			
11 St P2837xD_GobalivariableDefs.c	18 // define floa	at maths and then include IQnath library			4.00
18 g #2837xD_Gpo.c	20 #include "IDD	Serve h"			1.0
III & F2837xD_PeCbl.c	21	94 (28 97 F) - 1			
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H & P2837KD_SVICELC		SECTION(MotorControlISR, "ramfuncs"); SECTION(ResolverISR, "ramfuncs");			
IE _ #2837xD _usDelay.eam	25 Bendif	ere and the second state of the			
H DOK_Serve_2837x_RAM_hk_cpu1.ond	26				440
(H) IDOK_Serve.c	27 sprages INTER	NUPI (ResolverISR, HPI) RUPI (MotorControlISR, LPI)			
H R 20K Serve.h	29	ent (notorcontrolism, tra)			
B all resolver.c	30 // Prototype	statements for functions found within this file.			/ -
B-Q6 NextRelease		d MotorControlISR(void);			/ .
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F25X7v_SHCHLE	Console SS		🗄 Problems 🔢 🦞 Advice 🖋 Search		·
- III Graph LigraphProp	IDDK_Servo_F2837x		errors, 1 warring, 0 others		
Graph2.graphProp	e core, the other	core should not execute from shared-RAM (SA	Description +		_
- M 100K, Serve 2037k JUADH, HK, spart and	is they are used for	r the E/P code. Also, CPU1 will be halted to	e le manage (1 len)		
readme.txt	etermine SR owners	hip for the CPU which will run the Flash			
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% xds100v2_f2837x.ccsml [Active_Default]	oth flash banks an				
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Figure 12 CCS IDE showing Edit Perspective

	Expressio	ons win	dow [Debug Pers	pective
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El de ILDK (Servo J-2837x (Code Composer Studo - Device Debugging)	Expression	Type	Value	Address	
Texas Instruments XXXXX2U28 Enulater_QC20xx,CVU2 (Running) Texas Instruments XXXX2U8 Enulater_QC20xx,CVU2 (Running) Texas Instruments XXXX2U8 Enulator_QC20xx,CVU2 (Rusomected : Uninovn) Texas Instruments XXXX2U2.U88 Enulator_QC20x2_CALL (Discomected : Uninovn)	00- Speedkef 00- rc1.Setpoint/alue 00- rg1.Out 00- VdTesting	float float float float	0.05 0.04999922 0.6054779 0.0	0x00008048@Deta 0x00008164@Deta 0x00008164@Deta 0x00008108@Deta 0x00008040@Deta	
Debug window – view	00+ VqTesting 00+ offset_lemV	float	0.1	0x0000809A@Deta 0x00008040@Deta	
<u> </u>	00+ offset_lemW	float	0.0	0v0000803E@Data	
active CPUs in debug	00- offsetA	float	0.001071585 0.0002373937	Ox0000B0AC@Data Ox0000B0AE@Data	
3 4 File Name: IDDC_Servo.C 5 5 Target: Rev 1.1 of the F2837x control card 7 8 Author: C2000 Systems Lab, 05th March 2015 9 10 Description: Project uses two ISRs 11 1. Resolver ISR triggered by AOC INT, this is defined as the high priority im 12 2. Inverter ISR rate is 180%2 and is triggered by BMCL, reason to choose EM 3 ENMIL provides periodic sync resets to the SDM module, 4 ENMIL provides periodic sync resets to the SDM module.	4	+25 +5	9 +75	al time aph window	+175
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and the second s	0.000				
Press vap unstabilitation complete		+25 +5		100 +125 +150 mp/e	+175

Figure 13 CCS IDE showing Debug Perspective

Next Steps

- 27. It is not necessary to terminate the debug session each time the user changes or runs the code again. Instead the following procedure can be followed. After rebuilding the project, (Run->Reset->CPU Reset) , (Run->Restart), and enable realtime options. Once complete, disable realtime options, and reset CPU. Terminate the project if the target device or the configuration is changed (Ram to Flash or Flash to Ram) and prior to shutting down CCS.
- 28. Customize the project to meet your motor and feedback sensor options. Change the sensor type in IDDK_PM_Servo_F2837x-Settings.h. Feel free to also change the PWM switching frequency (ISR frequency). The ISR frequency should be chosen as a submultiple of PWM frequency for which some code modification needs to be done
- 29. Now the user can open the lab manual found in:

C:\TI\controlSUITE\development_kits\TMDSIDDK_v1.0\IDDK_PM_Servo_F2837x\~Docs

and start the experiments.

5 Revision History

Version	Date	Change Description
1.0	May 2015	First Release

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U.S. Federal Communications Commission Compliance

For EVMs Annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant

Caution

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation. Changes or modifications could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at its own expense.

FCC Interference Statement for Class B EVM devices

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.

• Consult the dealer or an experienced radio/TV technician for help.

Industry Canada Compliance (English)

For EVMs Annotated as IC – INDUSTRY CANADA Compliant:

This Class A or B digital apparatus complies with Canadian ICES-003.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Concerning EVMs Including Radio Transmitters

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concerning EVMs Including Detachable Antennas

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Canada Industry Canada Compliance (French)

Cet appareil numérique de la classe A ou B est conforme à la norme NMB-003 du Canada

Les changements ou les modifications pas expressément approuvés par la partie responsable de la conformité ont pu vider l'autorité de l'utilisateur pour actionner l'équipement.

Concernant les EVMs avec appareils radio

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante.

Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

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Important Notice for Users of EVMs Considered "Radio Frequency Products" in Japan

EVMs entering Japan are NOT certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If user uses EVMs in Japan, user is required by Radio Law of Japan to follow the instructions below with respect to EVMs:

- Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
- 2. Use EVMs only after user obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
- 3. Use of EVMs only after user obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless user gives the same notice above to the transferee. Please note that if user does not follow the instructions above, user will be subject to penalties of Radio Law of Japan.

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