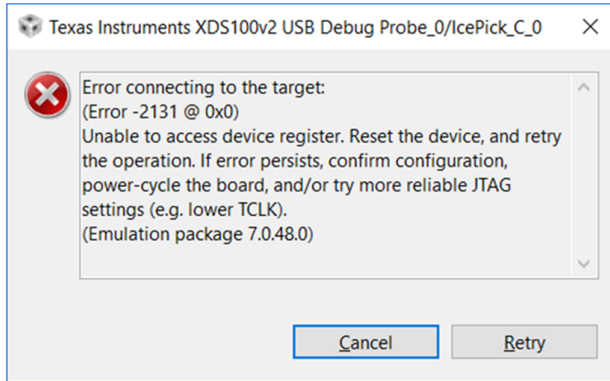


Error connecting to the target: TMS320F28379D

1 – Error message on connecting the target.



[Start: Texas Instruments XDS100v2 USB Debug Probe]
Execute the command:

```
%ccs_base%/common/uscif/dbgjtag -f %boarddatafile% -rv -o -F inform,logfile=yes -S pathlength -S integrity
```

[Result]

-----[Print the board config pathname(s)]-----

```
C:\Users\Weder\AppData\Local\TEXAS~1\CCS\
ti\0\0\BrdDat\testBoard.dat
```

-----[Print the reset-command software log-file]-----

This utility has selected a 100- or 510-class product.
This utility will load the adapter 'jioserdesusb.dll'.
The library build date was 'Jul 21 2017'.
The library build time was '19:36:41'.
The library package version is '7.0.48.0'.
The library component version is '35.35.0.0'.
The controller does not use a programmable FPGA.
The controller has a version number of '4' (0x00000004).
The controller has an insertion length of '0' (0x00000000).
This utility will attempt to reset the controller.
This utility has successfully reset the controller.

-----[Print the reset-command hardware log-file]-----

The scan-path will be reset by toggling the JTAG TRST signal.
The controller is the FTDI FT2232 with USB interface.
The link from controller to target is direct (without cable).
The software is configured for FTDI FT2232 features.
The controller cannot monitor the value on the EMU[0] pin.
The controller cannot monitor the value on the EMU[1] pin.
The controller cannot control the timing on output pins.
The controller cannot control the timing on input pins.
The scan-path link-delay has been set to exactly '0' (0x0000).

-----[The log-file for the JTAG TCLK output generated from the PLL]-----
There is no hardware for programming the JTAG TCLK frequency.

-----[Measure the source and frequency of the final JTAG TCLKR input]-----
There is no hardware for measuring the JTAG TCLK frequency.

-----[Perform the standard path-length test on the JTAG IR and DR]-----
This path-length test uses blocks of 64 32-bit words.

The test for the JTAG IR instruction path-length failed.

The JTAG IR instruction scan-path is stuck-at-ones.

The test for the JTAG DR bypass path-length failed.

The JTAG DR bypass scan-path is stuck-at-ones.

-----[Perform the Integrity scan-test on the JTAG IR]-----

This test will use blocks of 64 32-bit words.

This test will be applied just once.

Do a test using 0xFFFFFFFF.

Scan tests: 1, skipped: 0, failed: 0

Do a test using 0x00000000.

Test 2 Word 0: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 1: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 2: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 3: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 4: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 5: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 6: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 7: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

The details of the first 8 errors have been provided.

The utility will now report only the count of failed tests.

Scan tests: 2, skipped: 0, failed: 1

Do a test using 0xFE03E0E2.

Scan tests: 3, skipped: 0, failed: 2

Do a test using 0x01FC1F1D.

Scan tests: 4, skipped: 0, failed: 3

Do a test using 0x5533CCAA.

Scan tests: 5, skipped: 0, failed: 4

Do a test using 0xAACC3355.

Scan tests: 6, skipped: 0, failed: 5

Some of the values were corrupted - 83.3 percent.

The JTAG IR Integrity scan-test has failed.

-----[Perform the Integrity scan-test on the JTAG DR]-----

This test will use blocks of 64 32-bit words.

This test will be applied just once.

Do a test using 0xFFFFFFFF.

Scan tests: 1, skipped: 0, failed: 0

Do a test using 0x00000000.

Test 2 Word 0: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 1: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 2: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 3: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 4: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 5: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 6: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

Test 2 Word 7: scanned out 0x00000000 and scanned in 0xFFFFFFFF.

The details of the first 8 errors have been provided.

The utility will now report only the count of failed tests.

Scan tests: 2, skipped: 0, failed: 1

Do a test using 0xFE03E0E2.

Scan tests: 3, skipped: 0, failed: 2

Do a test using 0x01FC1F1D.

Scan tests: 4, skipped: 0, failed: 3

Do a test using 0x5533CCAA.

Scan tests: 5, skipped: 0, failed: 4

Do a test using 0xAACC3355.

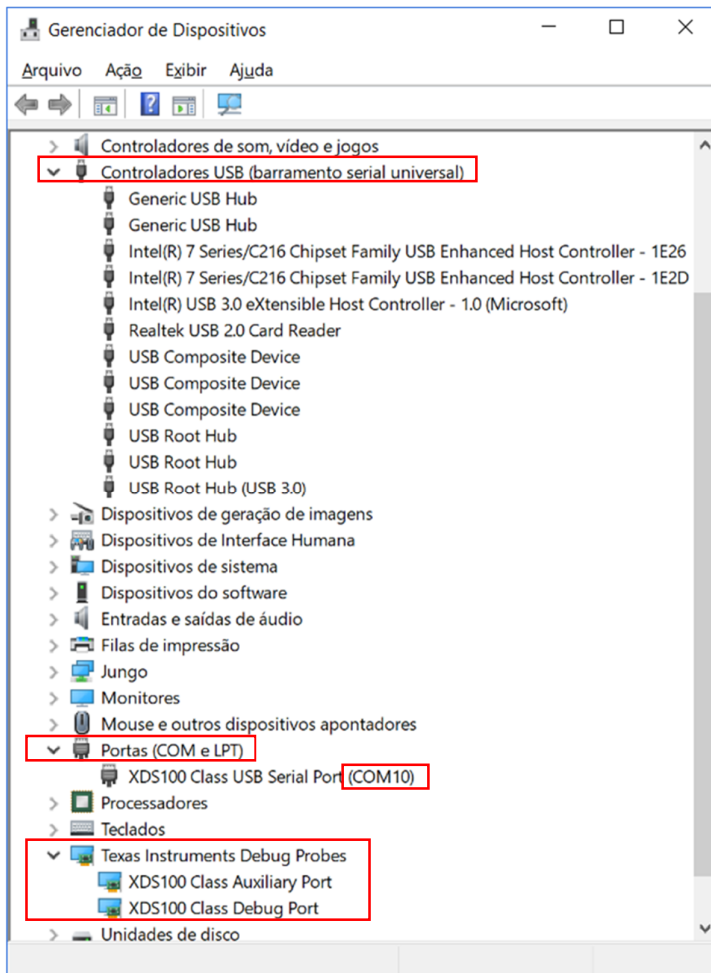
Scan tests: 6, skipped: 0, failed: 5

Some of the values were corrupted - 83.3 percent.

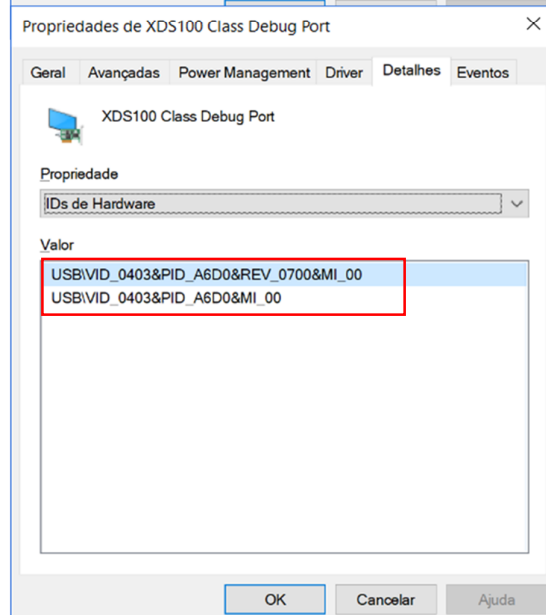
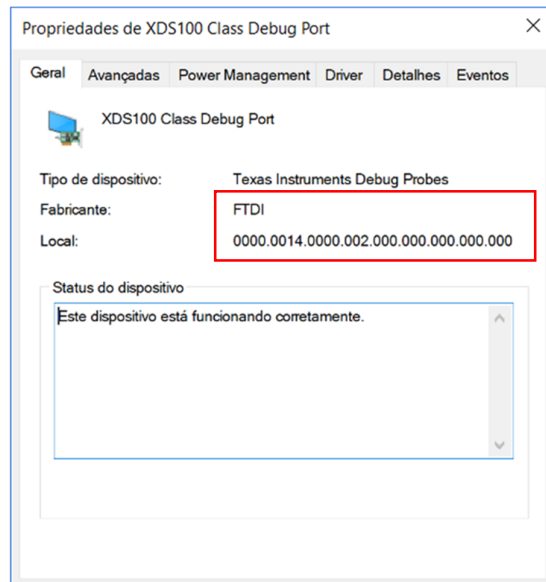
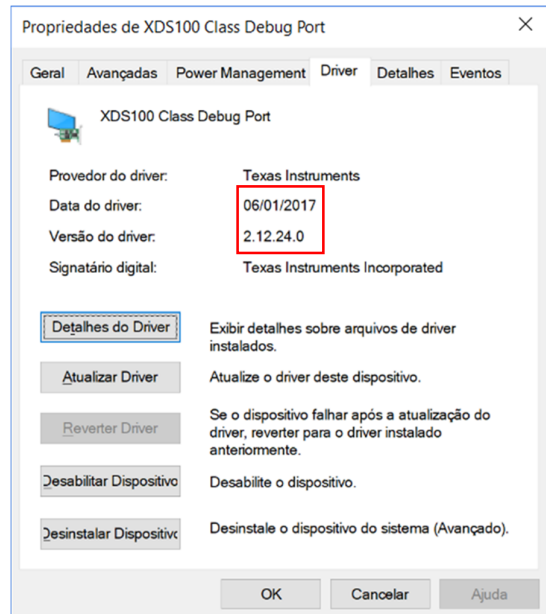
The JTAG DR Integrity scan-test has failed.

[End: Texas Instruments XDS100v2 USB Debug Probe]

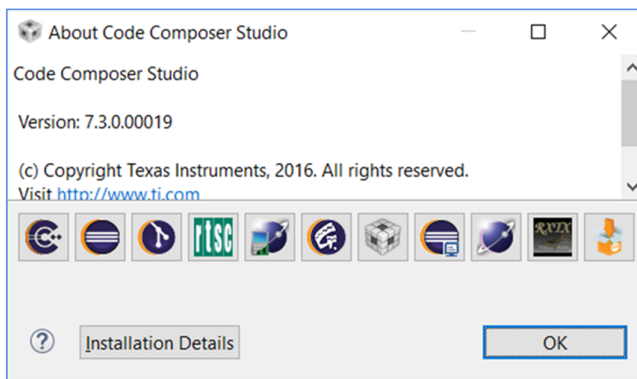
2 –XDS100V2 instalation.



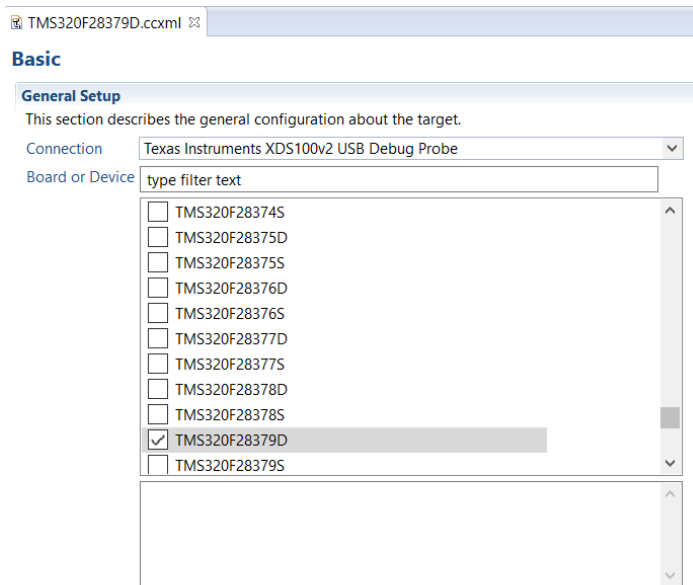
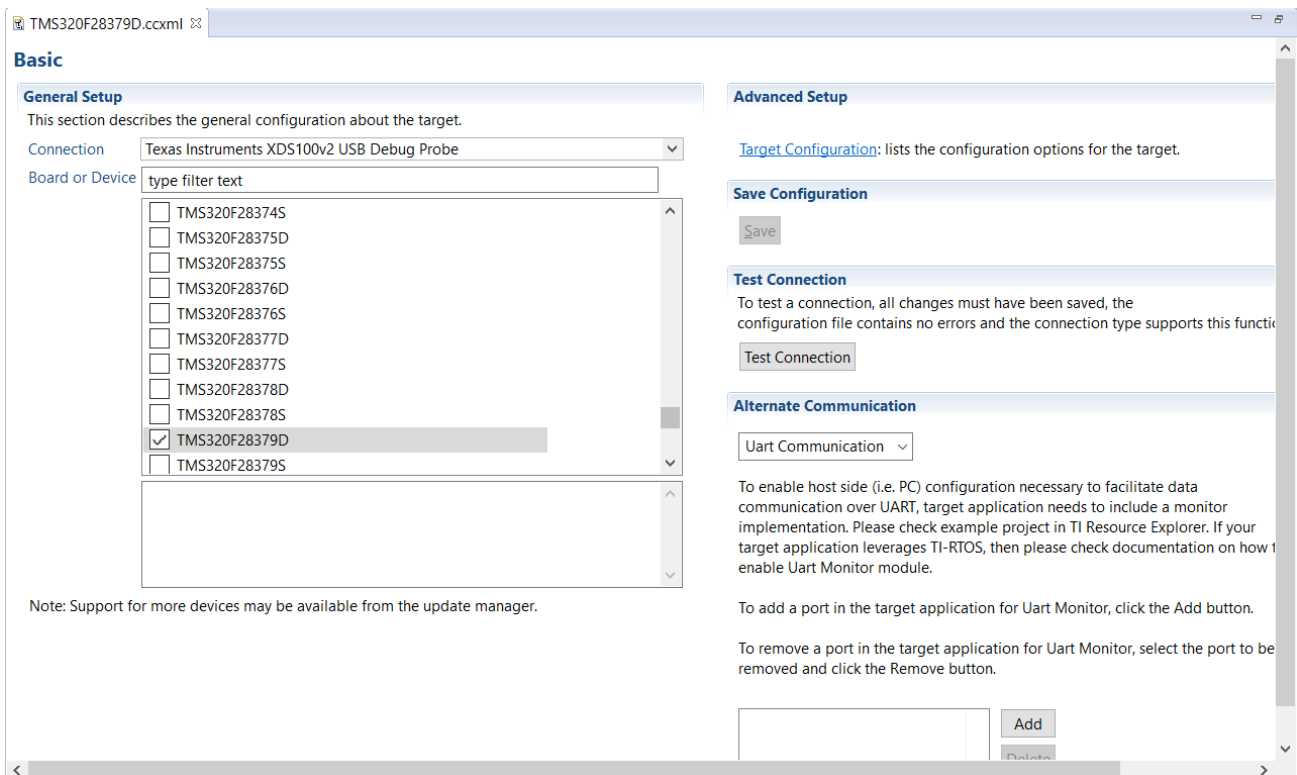
There is another version (2.8.14.0 - 03/18/2011). In this version we see 'XDS100 Channel A' and 'XDS100 Channel B' in "Universal Serial Bus Controllers" on Device Manager. I tried connecting using this version but I did not success.



3 – CCS → Version: 7.3.0.00019



3.1 –TARGET configuration



TMS320F28379D.ccxml

Target Configuration

All Connections

Texas Instruments XDS100v2 USB Debug Probe_0

TMS320F28379D_0

IcePick_C_0

Subpath_0

C28xx_CPU1

CPU1_CLA1

cs_child_0

Subpath_1

C28xx_CPU2

CPU2_CLA1

cs_child_1

Import...

New...

Add...

Delete

Up

Down

Test Connection

Save

Connection Properties

Set the properties of the selected connection.

Board Data File

auto generate

Debug Probe Selection

Only one XDS100 installed

The JTAG nTRST Boot-Mode

Disabled - Both EMU pins remain hi-z

The Power-On-Reset Boot-Mode

Disabled - Both EMU pins remain hi-z

The JTAG TCLK Frequency (MHz)

Fixed default 1.0MHz frequency

BasicAdvancedSource

TMS320F28379D.ccxml

Target Configuration

All Connections

Texas Instruments XDS100v2 USB Debug Probe_0

TMS320F28379D_0

IcePick_C_0

Subpath_0

C28xx_CPU1

CPU1_CLA1

cs_child_0

Subpath_1

C28xx_CPU2

CPU2_CLA1

cs_child_1

Import...

New...

Add...

Delete

Up

Down

Test Connection

Save

Router Properties

ICEPick_C router

Set the properties of the selected router.

☐ Bypass

initialization script

Browse...

BasicAdvancedSource

TMS320F28379D.ccxml

Target Configuration

All Connections

Texas Instruments XDS100v2 USB Debug Probe_0

TMS320F28379D_0

IcePick_C_0

Subpath_0

C28xx_CPU1

CPU1_CLA1

cs_child_0

Subpath_1

C28xx_CPU2

CPU2_CLA1

cs_child_1

Import...

New...

Add...

Delete

Up

Down

Test Connection

Save

Subpath Properties

Set the properties of the selected subpath.

Port Number

0x10

☐ Initial Configuration

☐ Custom Configuration

☒ Force Configuration

BasicAdvancedSource

Target Configuration

All Connections

- ✓ Texas Instruments XDS100v2 USB Debug Probe_0
 - ✓ TMS320F28379D_0
 - ✓ IcePick_C_0
 - ✓ Subpath_0
 - ✚ C28xx_CPU1
 - ✚ CPU1_CLA1
 - ✚ cs_child_0
 - ✓ Subpath_1
 - ✚ C28xx_CPU2
 - ✚ CPU2_CLA1
 - ✚ cs_child_1

Import...

New...

Add...

Delete

Up

Down

Test Connection

Save

Cpu Properties

C28xx CPU

Set the properties of the selected cpu.

☐ Bypassinitialization script

Browse...

☐ Slave Processor

Basic | Advanced | Source

Target Configuration

All Connections

- ✓ Texas Instruments XDS100v2 USB Debug Probe_0
 - ✓ TMS320F28379D_0
 - ✓ IcePick_C_0
 - ✓ Subpath_0
 - ✚ C28xx_CPU1
 - ✚ CPU1_CLA1
 - ✚ cs_child_0
 - ✓ Subpath_1
 - ✚ C28xx_CPU2
 - ✚ CPU2_CLA1
 - ✚ cs_child_1

Import...

New...

Add...

Delete

Up

Down

Test Connection

Save

Cpu Properties

CLA Accelerator

Set the properties of the selected cpu.

☐ Bypassinitialization script

Browse...

☐ Slave Processor

Basic | Advanced | Source

Target Configuration

All Connections

- ✓ Texas Instruments XDS100v2 USB Debug Probe_0
 - ✓ TMS320F28379D_0
 - ✓ IcePick_C_0
 - ✓ Subpath_0
 - ✚ C28xx_CPU1
 - ✚ CPU1_CLA1
 - ✚ cs_child_0
 - ✓ Subpath_1
 - ✚ C28xx_CPU2
 - ✚ CPU2_CLA1
 - ✚ cs_child_1

Import...

New...

Add...

Delete

Up

Down

Test Connection

Save

Cpu Properties

cs_child

Set the properties of the selected cpu.

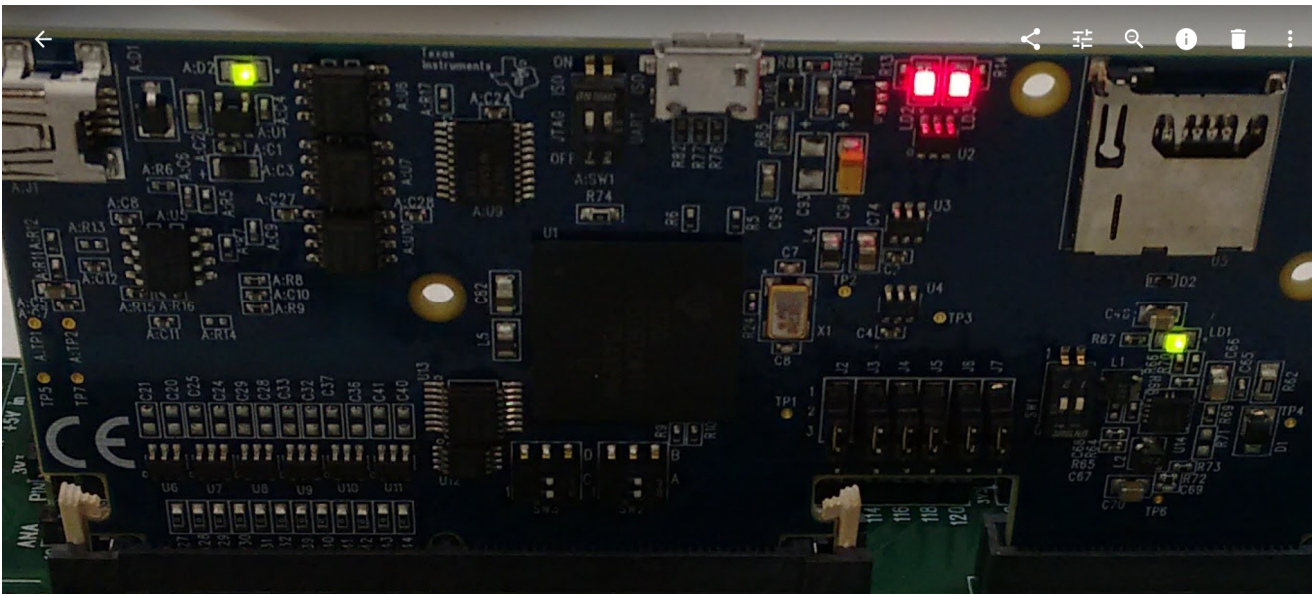
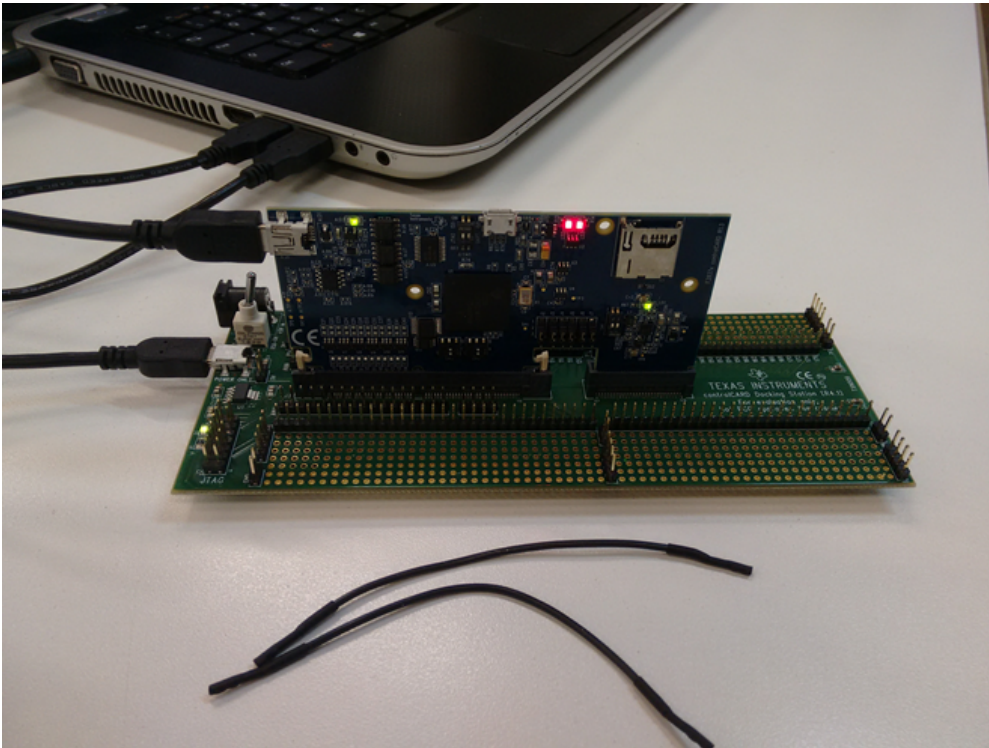
☐ Bypassinitialization script

Browse...

☐ Slave ProcessorAddress Identity

Basic | Advanced | Source

4 – Control Card turned on



Key position:

A:SW1 → Position 1: ON (up); Position 2: ON (up)

SW1 → Position 1: OFF (up); Position 2: ON (down)

J2-J7 → All Jumper up