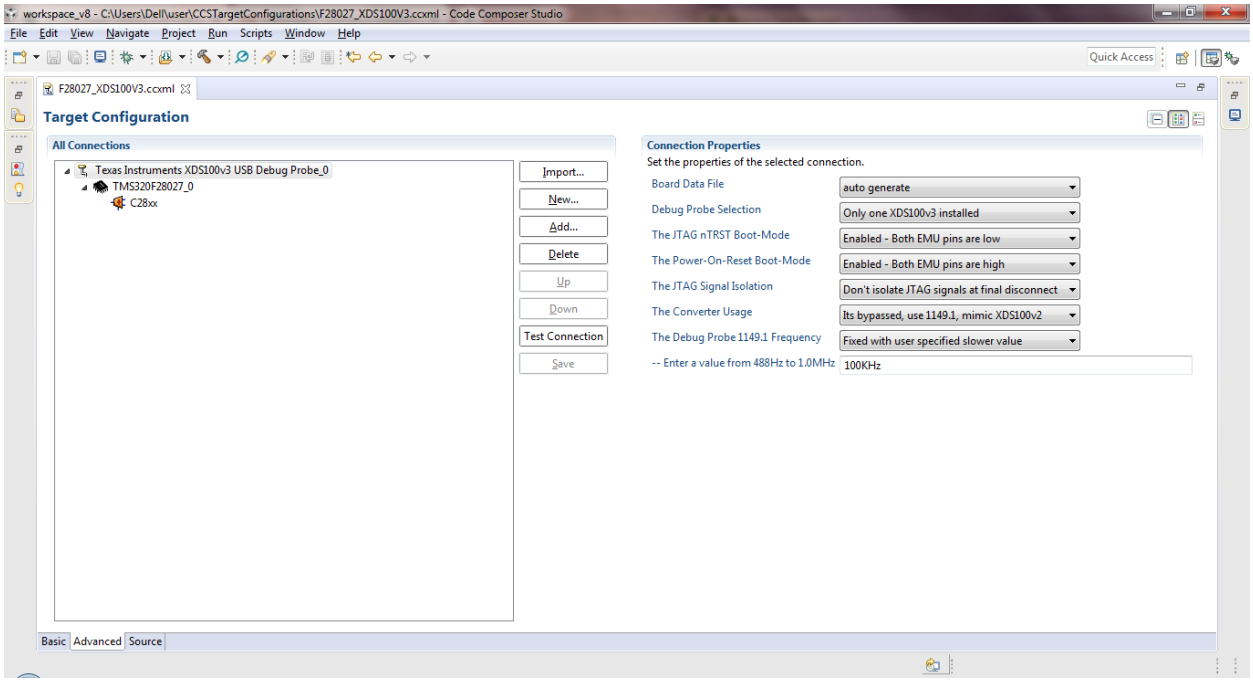
workspace_v8 - C:\Users\Dell\user\CCSTargetConfigurations\F28027_XDS100V3.ccxml - Code Composer Studio
File Edit View Navigate Project Run Scripts Window Help
Quick Access
F28027_XDS100V3.ccxml
Target Configuration
All Connections
Texas Instruments XDS100v3 USB Debug Probe_0
TMS320F28027_0
C28xx
Import...
New...
Add...
Delete
Up
Down
Test Connection
Save
Connection Properties
Set the properties of the selected connection.
Board Data File
auto generate
Debug Probe Selection
Only one XDS100v3 installed
The JTAG nTRST Boot-Mode
Enabled - Both EMU pins are low
The Power-On-Reset Boot-Mode
Enabled - Both EMU pins are high
The JTAG Signal Isolation
Don't isolate JTAG signals at final disconnect
The Converter Usage
Its bypassed, use 1149.1, mimic XDS100v2
The Debug Probe 1149.1 Frequency
Fixed with user specified slower value
-- Enter a value from 488Hz to 1.0MHz
100KHz
Basic
Advanced
Source