

## cC2802x MCU controller board pin-out description

V33D-ISO	1	51	V33D-ISO
ISO-RX-RS232	2	52	ISO-TX-RS232
spare	3	53	spare
spare	4	54	spare
spare	5	55	spare
GND_ISO	6	56	GND_ISO
ADCIN-B7 <b>**</b> (SW4.2)	7	57	ADCIN-A0/VREFHI
GND	8	58	GND
ADCIN-B1	9	59	ADCIN-A1
GND	10	60	GND
ADCIN-B2/COMP1B/AIO10	11	61	ADCIN-A2/COMP1A/AIO2
GND	12	62	GND
ADCIN-B3	13	63	ADCIN-A3
GND	14	64	GND
ADCIN-B4/COMP2B/AIO12	15	65	ADCIN-A4/COMP2A/AIO4
spare	16	66	spare
spare	17	67	spare
spare	18	68	spare
ADCIN-B6/AIO14	19	69	ADCIN-A6/AIO6
spare	20	70	spare
(can be chosen to be ADCIN-B7 via position 2 of SW4)	21	71	ADCIN-A7
spare	22	72	spare
GPIO-00 / EPWM-1A	23	73	GPIO-01 / EPWM-1B / EMU0 / COMP1O
GPIO-02 / EPWM-2A	24	74	GPIO-03 / EPWM-2B / COMP2O
GPIO-04 / EPWM-3A	25	75	GPIO-05 / EPWM-3B / ECAP-1
GPIO-06 / EPWM-4A / SYNCI / SYNCO	26	76	GPIO-07 / EPWM-4B / SCI-RX
GND	27	77	+5V in
spare	28	78	spare
spare	29	79	spare
spare	30	80	spare
spare	31	81	spare
spare	32	82	+5V in
GPIO-12 / TZ-1 / SCI-TX <b>**</b> (SW2.1)	33	83	(can be chosen to be GPIO-16 via position 2 of SW2)
(can be chosen to be GPIO-12 via position 1 of SW2)	34	84	(can be chosen to be GPIO-17 via position 1 of SW3)
(can be chosen to be GPIO-19 via position 2 of SW3)	35	85	spare
spare	36	86	spare
GND	37	87	+5V in
GPIO-16 / SPISIMO / TZ-2 <b>**</b> (SW2.2)	38	88	GPIO-17 / SPISOMI / TZ-3 <b>**</b> (SW3.1)
GPIO-18 / SPICLK / SCITX-B	39	89	GPIO-19/SPISTE/SCIRX/ECAP1/XCLKIN <b>**</b> (SW3.2)
spare	40	90	spare
spare	41	91	spare
spare	42	92	+5V in
GPIO-28 / SCIRX / I2CSDA / TZ-2 (*)	43	93	GPIO-29 / SCITX / I2CSCL / TZ-3
spare	44	94	spare
GPIO-32 / I2CSDA / SYNCI / ADCSOCA	45	95	GPIO-33 / I2CSCL / SYNCO / ADCSOCB
GPIO-34	46	96	+5V in
GND	47	97	TDI
TCK	48	98	TDO
TMS	49	99	TRSTn
EMU1	50	100	EMU0

(\*) GPIO-28 is held high by the SCI transceiver, remove resistor R10 on the controlCARD to enable other functionalities  
(\*\*X.Y) output of this functionality is controlled by SWX, position Y on the controlCARD, functionality is at this pin by default