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-- Concerto 144-pin package BSDL

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-- Notes :

-- Specific sequencing of EMU0, EMU1, XRS/ARS and TRST pins is required for
-- device to enter proper boundary scan. This sequencing requirement is outside the
-- scope of the BSDL file and therefore should be taken care by user on the board.

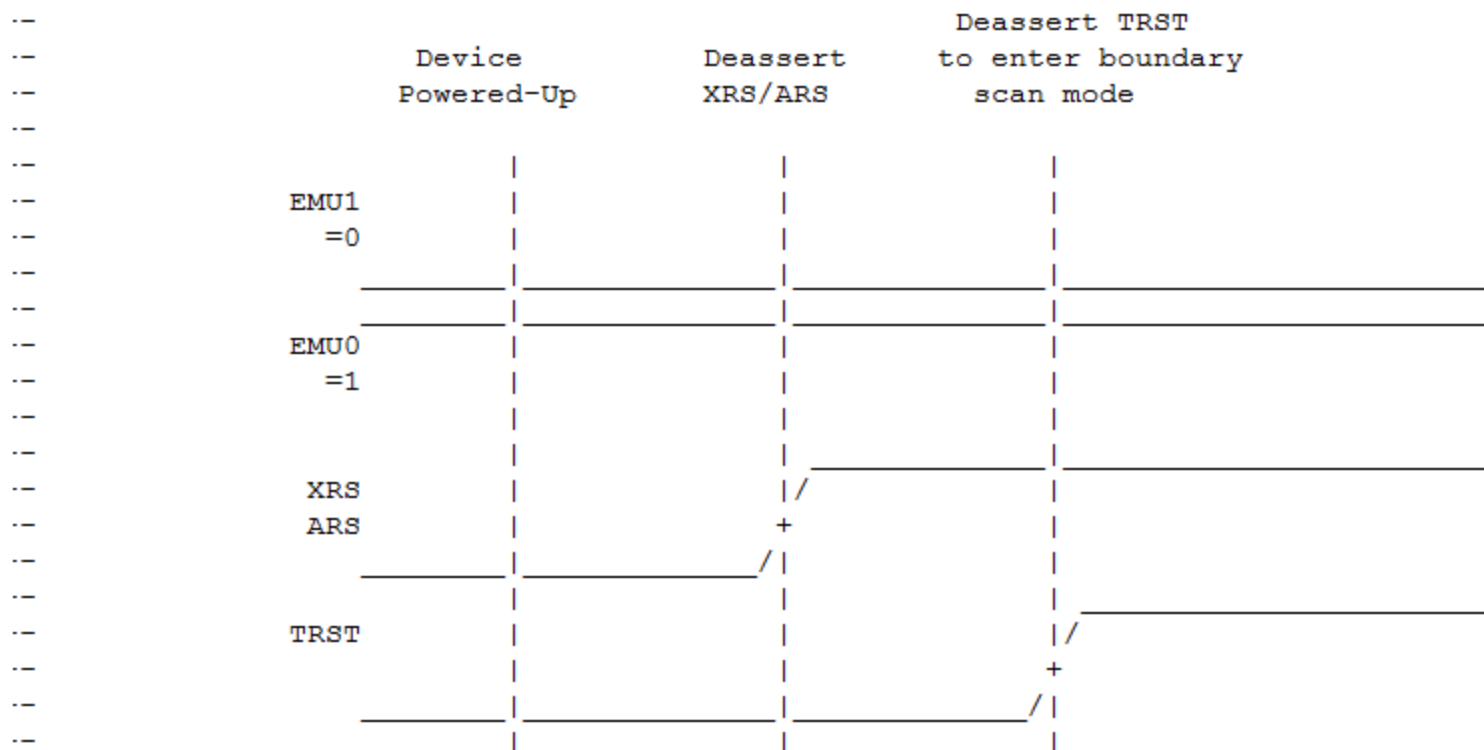
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-- The preamble sequence is given below. Here with EMU0 pin at a 'logic-1' state and
-- EMU1 at a 'logic-0' state, a rising edge on TRST pin would latch the device into
-- boundary scan (BSR) mode. Once the device has entered boundary scan mode, regular BSDL
-- based JTAG tests could be performed.

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-- BSR preamble sequence -

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