

Original project

- CCS version: Version: 10.1.1.00004, compiler 20.2.1.LTS
- Original project: HHC LLC development code 2.2
- The digital power SDK version is 1_03_00_00, with the old C2000WARE linked

Properties for HHC_F280049_demo

type filter text

Resource

Linked Resources

Resource Filters

General

Build

C2000 Compiler

Processor Option

Optimization

Include Options

Performance Advi

Prefdefined Symbs

Advanced Option

C2000 Linker

C2000 Hex Utility [D

Debug

Git

Project Natures

Linked Resources

Path Variables Linked Resources

Path variables specify locations in the file system, including other path variables with the syntax "\${VAR}".
The locations of linked resources may be specified relative to these path variables.
Defined path variables for resource 'HHC_F280049_demo':

Name	Value
BOARD_DRIVERS	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\solutions\tidm_1007\28004x\drivers
C2000WARE_DLL_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\driverlib\28004x\driverlib
CCS_BASE_ROOT	C:\ti\ccs1011\ccs\ccs_base
CCS_INSTALL_ROOT	C:\ti\ccs1011\ccs
CG_TOOL_ROOT	C:\ti\ccs1011\ccs\tools\compiler\ti-cgt-c2000_20.2.1.LTS
CLAMATH_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\libraries\math\CLAMath\c28
CLA_ROM_SYM_LIB	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\libraries\boot_rom\28004x\rev8\rom_symbol_libs\CLA_Data...
DCL_LIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\libraries\control\DCL\c28
ECLIPSE_HOME	C:\ti\ccs1011\ccs\edclipse
FASTRTSLIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\libraries\math\FPUfastRTS\c28
PARENT_LOC	C:\Users\ja0224194\workspace_v10\HHC_ILC_2021
POWER_MEAS_LIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\libraries\power_measurement
PROJECT_LOC	C:\Users\ja0224194\workspace_v10\HHC_ILC_2021\HHC_F280049_demo
SFRA_LIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\libraries\sfra
SPLL_LIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\libraries\spil
TI_PRODUCTS_DIR	C:\ti
TI_PRODUCTS_DIR_TIREX	C:\ti
UTILITIES_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\libraries\utilities
WORKSPACE_LOC	C:\Users\ja0224194\workspace_v10\HHC_ILC_2021

Resource Hitters

- General
- Build
- SysConfig
- C2000 Compiler
- C2000 Linker
- C2000 Hex Utility [D
- Debug
- Git
- Project Natures

Project

Device

Family: C2000

Variant: <select or type filter text> TMS320F280049M

Connection: Verify... (applies to whole project)

☐ Manage the project's target-configuration automatically

Project type and tool-chain

Compiler version: TI v20.2.1.LTS More...

Output type: RTSC Application (Executable)

Output format: legacy COFF

Device endianness: little

Linker command file: f28004x_flash_lnk.cmd Browse...

Runtime support library: <automatic> Browse...

To enable the CLB Tool to this project

- Follow all the steps mentioned here
- Add the latest C2000 link: c2000\C2000Ware_3_02_00_00, but keep all the original links unchanged.

6 Enabling CLB Tool In Existing DriverLib Projects

Use the following steps to add CLB support to an existing C2000WARE DriverLib Project:

Linked Resources	
Path variables specify locations in the file system, including other path variables with the syntax "\${VAR}". The locations of linked resources may be specified relative to these path variables.	
Defined path variables for resource 'HHC_F280049_demo':	
Name	Value
BOARD_DRIVERS	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\solutions\tdm_1007\f28004x\drivers
C2000WARE_DLIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\driverlib\f28004x\driverlib
C2000WARE_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_3_02_00_00
CCS_BASE_ROOT	C:\ti\ccs1011\ccs\ccs_base
CCS_INSTALL_ROOT	C:\ti\ccs1011\ccs
CG_TOOL_ROOT	C:\ti\ccs1011\ccs\tools\compiler\ti-cgt-c2000_20.2.1.LTS
CLAMATH_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\libraries\math\CLAMath\c28
CLA_ROM_SYM_LIB	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\libraries\boot_rom\rom_symbol...
CLB_SIM_COMPILER	C:\TDM-GCC-64\bin
CLB_SYSCFG_ROOT	\$(C2000WARE_ROOT)\utilities\clb_tool\clb_syscfg
DCL_LIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\libraries\control\DLL\c28
ECLIPSE_HOME	C:\ti\ccs1011\ccs\ eclipse\
FASTRTSLIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\c2000ware\libraries\math\FPUfastRTS\c28
PARENT_LOC	C:\Users\ao224194\workspace_v10\HHC_ILC_2021
POWER_MEAS_LIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\libraries\power_measurement
PROJECT_LOC	C:\Users\ao224194\workspace_v10\HHC_ILC_2021\HHC_F280049_demo
SFRALIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\libraries\sfra
SPLL_LIB_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\libraries\spil
SYSTEMC_INSTALL	\$(C2000WARE_ROOT)\utilities\clb_tool\clb_syscfg\systemc-2.3.3
TI_PRODUCTS_DIR	C:\ti
TI_PRODUCTS_DIR_TIREX	C:\ti
UTILITIES_ROOT	\$(TI_PRODUCTS_DIR_TIREX)\c2000\C2000Ware_DigitalPower_SDK_1_03_00_00\libraries\utilities
WORKSPACE_LOC	C:\Users\ao224194\workspace_v10\HHC_ILC_2021

Building errors reported

```
CDT Build Console [HHC_F280049_demo]
> _oo_oo/_c2000ware/ur1ver110/_c2000ware/ur1ver110/
--include_path="C:/Users/a0224194/workspace_v10/HHC_LLC_20
21/HHC_F280049_demo/libraries/DCL"
--include_path="C:/ti/ccs1011/ccs/tools/compiler/ti-cgt-c2
000_20.2.1.LTS/include" --advise:performance=all
--define=DEBUG --define=CLA_DEBUG=1 --define=F28x_DEVICE
--define=CPU1 --define=LARGE_MODEL --define=_FLASH -g
--diag_suppress=10063 --diag_suppress=173
--diag_warning=225 --diag_wrap=off --display_error_number
--quiet --abi=coffabi --cla_background_task=on -k
--asm_listing --preproc_with_compile
--preproc_dependency="libraries/DCL/DCL_clamp_C1.d_raw"
--include_path="C:/Users/a0224194/workspace_v10/HHC_LLC_20
21/HHC_F280049_demo/RELEASE/syscfg"
--obj_directory="libraries/DCL"
"../libraries/DCL/DCL_clamp_C1.asm"
Finished building: "../libraries/DCL/DCL_clamp_C1.asm"

gmake: Target 'all' not remade because of errors.

**** Build Finished ****
```

```
Problems 2 2 errors, 9 warnings, 0 others
Description
Errors (2 items)
gmake: *** [syscfg/clb_config.obj] Error 1
gmake: Target 'all' not remade because of errors.
Warnings (9 items)
```

```
CDT Build Console [HHC_F280049_demo]
--define=F28x_DEVICE --define=CPU1 --define=LARGE_MODEL --define=_FLASH -g
--diag_suppress=10063 --diag_suppress=173 --diag_warning=225
--diag_wrap=off --display_error_number --quiet --abi=coffabi
--cla_background_task=on -k --asm_listing --preproc_with_compile
--preproc_dependency="syscfg/clb_config.d_raw"
--include_path="C:/Users/a0224194/workspace_v10/HHC_LLC_2021/HHC_F280049_de
mo/RELEASE/syscfg" --obj_directory="syscfg" "syscfg/clb_config.c"

>> Compilation failure
subdir_rules.mk:22: recipe for target 'syscfg/clb_config.obj' failed
"syscfg/clb_config.c", line 9: fatal error #1965: cannot open source file
"clb.h"
1 catastrophic error detected in the compilation of "syscfg/clb_config.c".
Compilation terminated.
gmake: *** [syscfg/clb_config.obj] Error 1
Building file: "syscfg/clb_sim.cpp"
Invoking: C2000 Compiler
"C:/ti/ccs1011/ccs/tools/compiler/ti-cgt-c2000_20.2.1.LTS/bin/cl2000" -v28
-m1 -mt --cla_support=cla2 --float_support=fpu32 --tmu_support=tmu0
--vcu_support=vcu0 -O2 --opt_for_speed=5 --fp_mode=relaxed --fp_reassoc=on
--include_path="C:/Users/a0224194/workspace_v10/HHC_LLC_2021/HHC_F280049_de
mo/RELEASE/syscfg" --obj_directory="syscfg" "syscfg/clb_sim.cpp"
```