



I want to confirm how to get ADC at arrow timing.

EPwm1Regs.TBCTL.m_bit.CTRMODE = 2

←setting up-down count mode

Figure 15-148. ETSEL Register

15	14	13	12	11	10	9	8
SOCBEN		SOCBSEL		SOCAEN		SOCASEL	
R/W-0h		R/W-0h		R/W-0h		R/W-0h	
7	6	5	4	3	2	1	0
RESERVED	INTSELCMP	SOCBSELCMP	SOCASELCMP	INTEN	INTSEL		
R-0-0h	R/W-0h	R/W-0h	R/W-0h	R/W-0h	R/W-0h		

001: Enable event time-base counter equal to zero. (TBCTR = 0x00)

Figure 11-42. ADCSOC0CTL Register

31	30	29	28	27	26	25	24
RESERVED							TRIGSEL
R-0h							R/W-0h
23	22	21	20	19	18	17	16
TRIGSEL				RESERVED	CHSEL		
R/W-0h				R-0h	R/W-0h		
15	14	13	12	11	10	9	8
CHSEL	RESERVED						ACOPS
R/W-0h	R-0h						R/W-0h
7	6	5	4	3	2	1	0
ACQPS							
R/W-0h							

05h ADCTRIG5 - ePWM1, ADCSOCA