

Figure 3-2. Interrupt Propagation Path

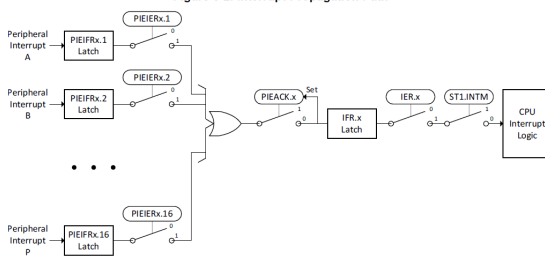


Table 2-12. Bit Fields of Status Register 1 (ST1)

15	13	12	11	10	9	8
ARP	XF	MOM1MAP	Reserved	OBJMODE	AMODE	
R/W-000	R/W-0	R/W-1	R/W-0	R/W-0	R/W-0	R/W-0
7	6	5	4	3	2	1
IDLESTAT	EALLOW	LOOP	SPA	VMAP	PAGE0	DBGM
R-0	R/W-0	R-0	R/W-0	R/W-1	R/W-0	R/W-1

LEGEND: R/W = Read/Write; R = Read only; -n = value after reset

15	14	13	12	11	10	9	8
RTOSINT	DLOGINT	INT14	INT13	INT12	INT11	INT10	INT9
R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0
7	6	5	4	3	2	1	0
INT8	INT7	INT6	INT5	INT4	INT3	INT2	INT1
R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0

Note: R = Read access; W = Write access; value following dash (-) is value after reset.

Figure 3-2. Interrupt Enable Register (IER)

```

2171 R PieCtrlRegs_PIECTRL 0x0000000F 0x0DE1
2172 R PieCtrlRegs_PIEACK 0x0000000F 0x0000
2173 R PieCtrlRegs_PIEIER1 0x0000000F 0x0041
2174 R PieCtrlRegs_PIEIFR1 0x0000000F 0x0000

```

```

1 521177 40
2 R ACC 0x0000000B 0x0000C010
3 R P 0x0000000B 0x44444460
4 R XT 0x0000000B 0x44440112
5 R XAR0 0x0000000B 0x00000007
6 R XAR1 0x0000000B 0x0000FFFF
7 R XAR2 0x0000000B 0x00000000
8 R XAR3 0x0000000B 0x00000000
9 R XAR4 0x0000000B 0x0000C00E
10 R XAR5 0x0000000B 0x0000C040
11 R XAR6 0x0000000B 0x0000C040
12 R XAR7 0x0000000B 0x0000C016
13 R PC 0x00000011 0x083D2F
14 R RPC 0x00000011 0x08269B
15 R ST0 0x0000000F 0x0090
16 R ST1 0x0000000F 0x8A08
17 R DP 0x0000000F 0x0300
18 R SP 0x0000000F 0x0408
19 R IER 0x0000000F 0x0409
20 R IFR 0x0000000F 0x0000
21 R DBGIER 0x0000000F 0x0000
22 R FPU_RB 0x0000000B 0x00000000
23 R FPU_STF 0x0000000B 0x00000200
24 R FPU_R0H 0x0000000B 0x443B8000
25 R FPU_R1H 0x0000000B 0x443B8000
26 R FPU_R2H 0x0000000B 0x00000000
27 R FPU_R3H 0x0000000B 0x00000000
28 R FPU_R4H 0x0000000B 0x00000000
29 R FPU_R5H 0x0000000B 0x00000000
30 R FPU_R6H 0x0000000B 0x00000000
31 R FPU_R7H 0x0000000B 0x00000000
32 R VCU_VR0 0x0000000B 0x00000000
33 R VCU_VR1 0x0000000B 0x00000000
34 R VCU_VR2 0x0000000B 0x00000000
35 R VCU_VR3 0x0000000B 0x00000000
36 R VCU_VR4 0x0000000B 0x00000000
37 R VCU_VR5 0x0000000B 0x00000000
38 R VCU_VR6 0x0000000B 0x00000000
39 R VCU_VR7 0x0000000B 0x00000000
40 R VCU_VR8 0x0000000B 0x00000000
41 R VCU_VT0 0x0000000B 0x00000000
42 R VCU_VT1 0x0000000B 0x00000000
43 R VCU_VSTATUS 0x0000000B 0x00000000
44 R VCU_VCRC 0x0000000B 0x00000000
45 R EnhancedDebugGlobalRegs_GLBL_EVENT_STAT 0x0000000F 0x0000
46 R EnhancedDebugGlobalRegs_GLBL_HALT_STAT 0x0000000F 0x0000
47 R EnhancedDebugGlobalRegs_GLBL_ENABLE 0x0000000F 0x0000
48 R EnhancedDebugGlobalRegs_CTM_GLBL_RESET 0x0000000F 0x0000
49 R EnhancedDebugGlobalRegs_GLBL_NMI_CTL 0x0000000F 0x0000
50 R EnhancedDebugGlobalRegs_GLBL_OWNER 0x0000000F 0x0002
51 R EnhancedDebugHWBPRegs_HWBP_MASK_LOW1 0x0000000F 0x0000
52 R EnhancedDebugHWBPRegs_HWBP_MASK_HIGH1 0x0000000F 0x0000
53 R EnhancedDebugHWBPRegs_HWBP_REF_LOW1 0x0000000F 0x4668
54 R EnhancedDebugHWBPRegs_HWBP_REF_HIGH1 0x0000000F 0x0008
55 R EnhancedDebugHWBPRegs_HWBP_CLEAR1 0x0000000F 0x0000
56 R EnhancedDebugHWBPRegs_HWBP_CNTL1 0x0000000F 0x0020
57 R EnhancedDebugHWBPRegs_HWBP_STATUS1 0x0000000F 0x0400
58 R EnhancedDebugHWBPRegs_HWBP_MASK_LOW2 0x0000000F 0x0000
59 R EnhancedDebugHWBPRegs_HWBP_MASK_HIGH2 0x0000000F 0x0000
60 R EnhancedDebugHWBPRegs_HWBP_REF_LOW2 0x0000000F 0x0000
61 R EnhancedDebugHWBPRegs_HWBP_REF_HIGH2 0x0000000F 0x0000
62 R EnhancedDebugHWBPRegs_HWBP_CLEAR2 0x0000000F 0x0000
63 R EnhancedDebugHWBPRegs_HWBP_CNTL2 0x0000000F 0x0000
64 R EnhancedDebugHWBPRegs_HWBP_STATUS2 0x0000000F 0x0400
65 R EnhancedDebugHWBPRegs_HWBP_MASK_LOW3 0x0000000F 0x0000

```

66 R EnhancedDebugHWBPRegs_HWBP_MASK_HIGH3 0x0000000F 0x0000
67 R EnhancedDebugHWBPRegs_HWBP_REF_LOW3 0x0000000F 0x0000
68 R EnhancedDebugHWBPRegs_HWBP_REF_HIGH3 0x0000000F 0x0000
69 R EnhancedDebugHWBPRegs_HWBP_CLEAR3 0x0000000F 0x0000
70 R EnhancedDebugHWBPRegs_HWBP_CNTL3 0x0000000F 0x0000
71 R EnhancedDebugHWBPRegs_HWBP_STATUS3 0x0000000F 0x0400
72 R EnhancedDebugHWBPRegs_HWBP_MASK_LOW4 0x0000000F 0x0000
73 R EnhancedDebugHWBPRegs_HWBP_MASK_HIGH4 0x0000000F 0x0000
74 R EnhancedDebugHWBPRegs_HWBP_REF_LOW4 0x0000000F 0x0000
75 R EnhancedDebugHWBPRegs_HWBP_REF_HIGH4 0x0000000F 0x0000
76 R EnhancedDebugHWBPRegs_HWBP_CLEAR4 0x0000000F 0x0000
77 R EnhancedDebugHWBPRegs_HWBP_CNTL4 0x0000000F 0x0000
78 R EnhancedDebugHWBPRegs_HWBP_STATUS4 0x0000000F 0x0400
79 R EnhancedDebugHWBPRegs_HWBP_MASK_LOW5 0x0000000F 0x0000
80 R EnhancedDebugHWBPRegs_HWBP_MASK_HIGH5 0x0000000F 0x0000
81 R EnhancedDebugHWBPRegs_HWBP_REF_LOW5 0x0000000F 0x0000
82 R EnhancedDebugHWBPRegs_HWBP_REF_HIGH5 0x0000000F 0x0000
83 R EnhancedDebugHWBPRegs_HWBP_CLEAR5 0x0000000F 0x0000
84 R EnhancedDebugHWBPRegs_HWBP_CNTL5 0x0000000F 0x0000
85 R EnhancedDebugHWBPRegs_HWBP_STATUS5 0x0000000F 0x0400
86 R EnhancedDebugHWBPRegs_HWBP_MASK_LOW6 0x0000000F 0x0000
87 R EnhancedDebugHWBPRegs_HWBP_MASK_HIGH6 0x0000000F 0x0000
88 R EnhancedDebugHWBPRegs_HWBP_REF_LOW6 0x0000000F 0x0000
89 R EnhancedDebugHWBPRegs_HWBP_REF_HIGH6 0x0000000F 0x0000
90 R EnhancedDebugHWBPRegs_HWBP_CLEAR6 0x0000000F 0x0000
91 R EnhancedDebugHWBPRegs_HWBP_CNTL6 0x0000000F 0x0000
92 R EnhancedDebugHWBPRegs_HWBP_STATUS6 0x0000000F 0x0400
93 R EnhancedDebugHWBPRegs_HWBP_MASK_LOW7 0x0000000F 0x0000
94 R EnhancedDebugHWBPRegs_HWBP_MASK_HIGH7 0x0000000F 0x0000
95 R EnhancedDebugHWBPRegs_HWBP_REF_LOW7 0x0000000F 0x0000
96 R EnhancedDebugHWBPRegs_HWBP_REF_HIGH7 0x0000000F 0x0000
97 R EnhancedDebugHWBPRegs_HWBP_CLEAR7 0x0000000F 0x0000
98 R EnhancedDebugHWBPRegs_HWBP_CNTL7 0x0000000F 0x0000
99 R EnhancedDebugHWBPRegs_HWBP_STATUS7 0x0000000F 0x0400
100 R EnhancedDebugHWBPRegs_HWBP_MASK_LOW8 0x0000000F 0x0000
101 R EnhancedDebugHWBPRegs_HWBP_MASK_HIGH8 0x0000000F 0x0000
102 R EnhancedDebugHWBPRegs_HWBP_REF_LOW8 0x0000000F 0x0000
103 R EnhancedDebugHWBPRegs_HWBP_REF_HIGH8 0x0000000F 0x0000
104 R EnhancedDebugHWBPRegs_HWBP_CLEAR8 0x0000000F 0x0000
105 R EnhancedDebugHWBPRegs_HWBP_CNTL8 0x0000000F 0x0000
106 R EnhancedDebugHWBPRegs_HWBP_STATUS8 0x0000000F 0x0400
107 R EnhancedDebugCounterRegs_CTM_CNTL1 0x0000000F 0x0000
108 R EnhancedDebugCounterRegs_CTM_STATUS1 0x0000000F 0x0010
109 R EnhancedDebugCounterRegs_CTM_REF_LOW1 0x0000000F 0x0000
110 R EnhancedDebugCounterRegs_CTM_REF_HIGH1 0x0000000F 0x0000
111 R EnhancedDebugCounterRegs_CTM_COUNT_LOW1 0x0000000F 0x0000
112 R EnhancedDebugCounterRegs_CTM_COUNT_HIGH1 0x0000000F 0x0000
113 R EnhancedDebugCounterRegs_CTM_MAX_COUNT_LOW1 0x0000000F 0x0000
114 R EnhancedDebugCounterRegs_CTM_MAX_COUNT_HIGH1 0x0000000F 0x0000
115 R EnhancedDebugCounterRegs_CTM_INPUT_SEL1 0x0000000F 0x0000
116 R EnhancedDebugCounterRegs_CTM_CLEAR1 0x0000000F 0x0000
117 R EnhancedDebugCounterRegs_CTM_INPUT_SEL_MSB1 0x0000000F 0x0000
118 R EnhancedDebugCounterRegs_CTM_CNTL2 0x0000000F 0x0000
119 R EnhancedDebugCounterRegs_CTM_STATUS2 0x0000000F 0x0010
120 R EnhancedDebugCounterRegs_CTM_REF_LOW2 0x0000000F 0x0000
121 R EnhancedDebugCounterRegs_CTM_REF_HIGH2 0x0000000F 0x0000
122 R EnhancedDebugCounterRegs_CTM_COUNT_LOW2 0x0000000F 0x0000
123 R EnhancedDebugCounterRegs_CTM_COUNT_HIGH2 0x0000000F 0x0000
124 R EnhancedDebugCounterRegs_CTM_MAX_COUNT_LOW2 0x0000000F 0x0000
125 R EnhancedDebugCounterRegs_CTM_MAX_COUNT_HIGH2 0x0000000F 0x0000
126 R EnhancedDebugCounterRegs_CTM_INPUT_SEL2 0x0000000F 0x0000
127 R EnhancedDebugCounterRegs_CTM_CLEAR2 0x0000000F 0x0000
128 R EnhancedDebugCounterRegs_CTM_INPUT_SEL_MSB2 0x0000000F 0x0000
129 R EnhancedDebugCounterRegs_CTM_CNTL3 0x0000000F 0x0000
130 R EnhancedDebugCounterRegs_CTM_STATUS3 0x0000000F 0x0010

131 R EnhancedDebugCounterRegs_CTM_REF_LOW3 0x0000000F 0x0000
132 R EnhancedDebugCounterRegs_CTM_REF_HIGH3 0x0000000F 0x0000
133 R EnhancedDebugCounterRegs_CTM_COUNT_LOW3 0x0000000F 0x0000
134 R EnhancedDebugCounterRegs_CTM_COUNT_HIGH3 0x0000000F 0x0000
135 R EnhancedDebugCounterRegs_CTM_MAX_COUNT_LOW3 0x0000000F 0x0000
136 R EnhancedDebugCounterRegs_CTM_MAX_COUNT_HIGH3 0x0000000F 0x0000
137 R EnhancedDebugCounterRegs_CTM_INPUT_SEL3 0x0000000F 0x0000
138 R EnhancedDebugCounterRegs_CTM_CLEAR3 0x0000000F 0x0000
139 R EnhancedDebugCounterRegs_CTM_INPUT_SEL_MSB3 0x0000000F 0x0000
140 R EnhancedDebugCounterRegs_CTM_CNTL4 0x0000000F 0x0000
141 R EnhancedDebugCounterRegs_CTM_STATUS4 0x0000000F 0x0010
142 R EnhancedDebugCounterRegs_CTM_REF_LOW4 0x0000000F 0x0000
143 R EnhancedDebugCounterRegs_CTM_REF_HIGH4 0x0000000F 0x0000
144 R EnhancedDebugCounterRegs_CTM_COUNT_LOW4 0x0000000F 0x0000
145 R EnhancedDebugCounterRegs_CTM_COUNT_HIGH4 0x0000000F 0x0000
146 R EnhancedDebugCounterRegs_CTM_MAX_COUNT_LOW4 0x0000000F 0x0000
147 R EnhancedDebugCounterRegs_CTM_MAX_COUNT_HIGH4 0x0000000F 0x0000
148 R EnhancedDebugCounterRegs_CTM_INPUT_SEL4 0x0000000F 0x0000
149 R EnhancedDebugCounterRegs_CTM_CLEAR4 0x0000000F 0x0000
150 R EnhancedDebugCounterRegs_CTM_INPUT_SEL_MSB4 0x0000000F 0x0000
151 R AccessProtectionRegs_NMAVFLG 0x0000000B 0x00000000
152 R AccessProtectionRegs_NMAVSET 0x0000000B 0x00000000
153 R AccessProtectionRegs_NMAVCLR 0x0000000B 0x00000000
154 R AccessProtectionRegs_NMAVINTEN 0x0000000B 0x00000000
155 R AccessProtectionRegs_NMCPURDAVADDR 0x0000000B 0x00000000
156 R AccessProtectionRegs_NMCPUWRAVADDR 0x0000000B 0x00000000
157 R AccessProtectionRegs_NMCPUFAVADDR 0x0000000B 0x00000000
158 R AccessProtectionRegs_NMDMAWRAVADDR 0x0000000B 0x00000000
159 R AccessProtectionRegs_NMCLA1RDVADDR 0x0000000B 0x00000000
160 R AccessProtectionRegs_NMCLA1WRAVADDR 0x0000000B 0x00000000
161 R AccessProtectionRegs_NMCLA1FAVADDR 0x0000000B 0x00000000
162 R AccessProtectionRegs_MAVFLG 0x0000000B 0x00000000
163 R AccessProtectionRegs_MAVSET 0x0000000B 0x00000000
164 R AccessProtectionRegs_MAVCLR 0x0000000B 0x00000000
165 R AccessProtectionRegs_MAVINTEN 0x0000000B 0x00000000
166 R AccessProtectionRegs_MCPUFAVADDR 0x0000000B 0x00000000
167 R AccessProtectionRegs_MCPUWRAVADDR 0x0000000B 0x00000000
168 R AccessProtectionRegs_MDMAWRAVADDR 0x0000000B 0x00000000
169 R AdcaResultRegs_ADCRESULT0 0x0000000F 0x0464
170 R AdcaResultRegs_ADCRESULT1 0x0000000F 0x07F7
171 R AdcaResultRegs_ADCRESULT2 0x0000000F 0x07EC
172 R AdcaResultRegs_ADCRESULT3 0x0000000F 0x0FED
173 R AdcaResultRegs_ADCRESULT4 0x0000000F 0x0000
174 R AdcaResultRegs_ADCRESULT5 0x0000000F 0x0000
175 R AdcaResultRegs_ADCRESULT6 0x0000000F 0x0000
176 R AdcaResultRegs_ADCRESULT7 0x0000000F 0x0000
177 R AdcaResultRegs_ADCRESULT8 0x0000000F 0x0000
178 R AdcaResultRegs_ADCRESULT9 0x0000000F 0x0000
179 R AdcaResultRegs_ADCRESULT10 0x0000000F 0x0000
180 R AdcaResultRegs_ADCRESULT11 0x0000000F 0x0000
181 R AdcaResultRegs_ADCRESULT12 0x0000000F 0x0000
182 R AdcaResultRegs_ADCRESULT13 0x0000000F 0x0000
183 R AdcaResultRegs_ADCRESULT14 0x0000000F 0x0000
184 R AdcaResultRegs_ADCRESULT15 0x0000000F 0x0000
185 R AdcaResultRegs_ADCPPB1RESULT 0x0000000B 0x00000466
186 R AdcaResultRegs_ADCPPB2RESULT 0x0000000B 0x00000466
187 R AdcaResultRegs_ADCPPB3RESULT 0x0000000B 0x00000466
188 R AdcaResultRegs_ADCPPB4RESULT 0x0000000B 0x00000466
189 R AdcbResultRegs_ADCRESULT0 0x0000000F 0x0FFF
190 R AdcbResultRegs_ADCRESULT1 0x0000000F 0x0157
191 R AdcbResultRegs_ADCRESULT2 0x0000000F 0x0000
192 R AdcbResultRegs_ADCRESULT3 0x0000000F 0x0000
193 R AdcbResultRegs_ADCRESULT4 0x0000000F 0x0000
194 R AdcbResultRegs_ADCRESULT5 0x0000000F 0x0000
195 R AdcbResultRegs_ADCRESULT6 0x0000000F 0x0000

196 R AdcbResultRegs_ADCRESULT7 0x0000000F 0x0000
197 R AdcbResultRegs_ADCRESULT8 0x0000000F 0x0000
198 R AdcbResultRegs_ADCRESULT9 0x0000000F 0x0000
199 R AdcbResultRegs_ADCRESULT10 0x0000000F 0x0000
200 R AdcbResultRegs_ADCRESULT11 0x0000000F 0x0000
201 R AdcbResultRegs_ADCRESULT12 0x0000000F 0x0000
202 R AdcbResultRegs_ADCRESULT13 0x0000000F 0x0000
203 R AdcbResultRegs_ADCRESULT14 0x0000000F 0x0000
204 R AdcbResultRegs_ADCRESULT15 0x0000000F 0x0000
205 R AdcbResultRegs_ADCPPB1RESULT 0x0000000B 0x0000FFF
206 R AdcbResultRegs_ADCPPB2RESULT 0x0000000B 0x0000FFF
207 R AdcbResultRegs_ADCPPB3RESULT 0x0000000B 0x0000FFF
208 R AdcbResultRegs_ADCPPB4RESULT 0x0000000B 0x0000FFF
209 R AdccResultRegs_ADCRESULT0 0x0000000F 0x045E
210 R AdccResultRegs_ADCRESULT1 0x0000000F 0x0451
211 R AdccResultRegs_ADCRESULT2 0x0000000F 0x07F5
212 R AdccResultRegs_ADCRESULT3 0x0000000F 0x07FE
213 R AdccResultRegs_ADCRESULT4 0x0000000F 0x0000
214 R AdccResultRegs_ADCRESULT5 0x0000000F 0x0000
215 R AdccResultRegs_ADCRESULT6 0x0000000F 0x0000
216 R AdccResultRegs_ADCRESULT7 0x0000000F 0x0000
217 R AdccResultRegs_ADCRESULT8 0x0000000F 0x0000
218 R AdccResultRegs_ADCRESULT9 0x0000000F 0x0000
219 R AdccResultRegs_ADCRESULT10 0x0000000F 0x0000
220 R AdccResultRegs_ADCRESULT11 0x0000000F 0x0000
221 R AdccResultRegs_ADCRESULT12 0x0000000F 0x0000
222 R AdccResultRegs_ADCRESULT13 0x0000000F 0x0000
223 R AdccResultRegs_ADCRESULT14 0x0000000F 0x0000
224 R AdccResultRegs_ADCRESULT15 0x0000000F 0x0000
225 R AdccResultRegs_ADCPPB1RESULT 0x0000000B 0x0000045E
226 R AdccResultRegs_ADCPPB2RESULT 0x0000000B 0x0000045E
227 R AdccResultRegs_ADCPPB3RESULT 0x0000000B 0x0000045E
228 R AdccResultRegs_ADCPPB4RESULT 0x0000000B 0x0000045E
229 R AdcaRegs_ADCCTL1 0x0000000F 0x0384
230 R AdcaRegs_ADCCTL2 0x0000000F 0x0006
231 R AdcaRegs_ADCBURSTCTL 0x0000000F 0x0000
232 R AdcaRegs_ADCINTFLG 0x0000000F 0x0001
233 R AdcaRegs_ADCINTFLGCLR 0x0000000F 0x0000
234 R AdcaRegs_ADCINTOVF 0x0000000F 0x0001
235 R AdcaRegs_ADCINTOVFCLR 0x0000000F 0x0000
236 R AdcaRegs_ADCINTSEL1N2 0x0000000F 0x0022
237 R AdcaRegs_ADCINTSEL3N4 0x0000000F 0x0000
238 R AdcaRegs_ADCSOCPRICTL 0x0000000F 0x0060
239 R AdcaRegs_ADCINTSOCSEL1 0x0000000F 0x0000
240 R AdcaRegs_ADCINTSOCSEL2 0x0000000F 0x0000
241 R AdcaRegs_ADCSOCFLG1 0x0000000F 0x0000
242 R AdcaRegs_ADCSOCFRC1 0x0000000F 0x0000
243 R AdcaRegs_ADCSOCOVF1 0x0000000F 0x0000
244 R AdcaRegs_ADCSOCOVFCLR1 0x0000000F 0x0000
245 R AdcaRegs_ADCSOC0CTL 0x0000000B 0x01150009
246 R AdcaRegs_ADCSOC1CTL 0x0000000B 0x01110009
247 R AdcaRegs_ADCSOC2CTL 0x0000000B 0x01100009
248 R AdcaRegs_ADCSOC3CTL 0x0000000B 0x01130009
249 R AdcaRegs_ADCSOC4CTL 0x0000000B 0x00000000
250 R AdcaRegs_ADCSOC5CTL 0x0000000B 0x00000000
251 R AdcaRegs_ADCSOC6CTL 0x0000000B 0x00000000
252 R AdcaRegs_ADCSOC7CTL 0x0000000B 0x00000000
253 R AdcaRegs_ADCSOC8CTL 0x0000000B 0x00000000
254 R AdcaRegs_ADCSOC9CTL 0x0000000B 0x00000000
255 R AdcaRegs_ADCSOC10CTL 0x0000000B 0x00000000
256 R AdcaRegs_ADCSOC11CTL 0x0000000B 0x00000000
257 R AdcaRegs_ADCSOC12CTL 0x0000000B 0x00000000
258 R AdcaRegs_ADCSOC13CTL 0x0000000B 0x00000000
259 R AdcaRegs_ADCSOC14CTL 0x0000000B 0x00000000
260 R AdcaRegs_ADCSOC15CTL 0x0000000B 0x00000000

261 R AdcaRegs_ADCEVTSTAT 0x0000000F 0x1111
262 R AdcaRegs_ADCEVTCLR 0x0000000F 0x0000
263 R AdcaRegs_ADCEVTSEL 0x0000000F 0x0000
264 R AdcaRegs_ADCEVTINTSEL 0x0000000F 0x0000
265 R AdcaRegs_ADCCOUNTER 0x0000000F 0x0BC9
266 R AdcaRegs_ADCREV 0x0000000F 0x0005
267 R AdcaRegs_ADCOFFTRIM 0x0000000F 0x00F0
268 R AdcaRegs_ADCPPB1CONFIG 0x0000000F 0x0000
269 R AdcaRegs_ADCPPB1STAMP 0x0000000F 0x0002
270 R AdcaRegs_ADCPPB1OFFCAL 0x0000000F 0x0000
271 R AdcaRegs_ADCPPB1OFFREF 0x0000000F 0x0000
272 R AdcaRegs_ADCPPB1TRIPHI 0x0000000B 0x00000000
273 R AdcaRegs_ADCPPB1TRIPL0 0x0000000B 0xF6E00000
274 R AdcaRegs_ADCPPB2CONFIG 0x0000000F 0x0000
275 R AdcaRegs_ADCPPB2STAMP 0x0000000F 0x0002
276 R AdcaRegs_ADCPPB2OFFCAL 0x0000000F 0x0000
277 R AdcaRegs_ADCPPB2OFFREF 0x0000000F 0x0000
278 R AdcaRegs_ADCPPB2TRIPHI 0x0000000B 0x00000000
279 R AdcaRegs_ADCPPB2TRIPL0 0x0000000B 0xFBE00000
280 R AdcaRegs_ADCPPB3CONFIG 0x0000000F 0x0000
281 R AdcaRegs_ADCPPB3STAMP 0x0000000F 0x0002
282 R AdcaRegs_ADCPPB3OFFCAL 0x0000000F 0x0000
283 R AdcaRegs_ADCPPB3OFFREF 0x0000000F 0x0000
284 R AdcaRegs_ADCPPB3TRIPHI 0x0000000B 0x00000000
285 R AdcaRegs_ADCPPB3TRIPL0 0x0000000B 0x12E00000
286 R AdcaRegs_ADCPPB4CONFIG 0x0000000F 0x0000
287 R AdcaRegs_ADCPPB4STAMP 0x0000000F 0x0002
288 R AdcaRegs_ADCPPB4OFFCAL 0x0000000F 0x0000
289 R AdcaRegs_ADCPPB4OFFREF 0x0000000F 0x0000
290 R AdcaRegs_ADCPPB4TRIPHI 0x0000000B 0x00000000
291 R AdcaRegs_ADCPPB4TRIPL0 0x0000000B 0x3BE00000
292 R AdcaRegs_ADCINTCYCLE 0x0000000F 0x0000
293 R AdcbRegs_ADCCTL1 0x0000000F 0x0384
294 R AdcbRegs_ADCCTL2 0x0000000F 0x0006
295 R AdcbRegs_ADCBURSTCTL 0x0000000F 0x0000
296 R AdcbRegs_ADCINTFLG 0x0000000F 0x0000
297 R AdcbRegs_ADCINTFLGCLR 0x0000000F 0x0000
298 R AdcbRegs_ADCINTOVF 0x0000000F 0x0000
299 R AdcbRegs_ADCINTOVFCLR 0x0000000F 0x0000
300 R AdcbRegs_ADCINTSEL1N2 0x0000000F 0x0000
301 R AdcbRegs_ADCINTSEL3N4 0x0000000F 0x0000
302 R AdcbRegs_ADCSOCPRICTL 0x0000000F 0x0060
303 R AdcbRegs_ADCINTSOCSEL1 0x0000000F 0x0000
304 R AdcbRegs_ADCINTSOCSEL2 0x0000000F 0x0000
305 R AdcbRegs_ADCSOCFLG1 0x0000000F 0x0000
306 R AdcbRegs_ADCSOCFRC1 0x0000000F 0x0000
307 R AdcbRegs_ADCSOCOVF1 0x0000000F 0x0000
308 R AdcbRegs_ADCSOCOVFCLR1 0x0000000F 0x0000
309 R AdcbRegs_ADCSOC0CTL 0x0000000B 0x00740009
310 R AdcbRegs_ADCSOC1CTL 0x0000000B 0x00720009
311 R AdcbRegs_ADCSOC2CTL 0x0000000B 0x00718009
312 R AdcbRegs_ADCSOC3CTL 0x0000000B 0x00710009
313 R AdcbRegs_ADCSOC4CTL 0x0000000B 0x00000000
314 R AdcbRegs_ADCSOC5CTL 0x0000000B 0x00000000
315 R AdcbRegs_ADCSOC6CTL 0x0000000B 0x00000000
316 R AdcbRegs_ADCSOC7CTL 0x0000000B 0x00000000
317 R AdcbRegs_ADCSOC8CTL 0x0000000B 0x00000000
318 R AdcbRegs_ADCSOC9CTL 0x0000000B 0x00000000
319 R AdcbRegs_ADCSOC10CTL 0x0000000B 0x00000000
320 R AdcbRegs_ADCSOC11CTL 0x0000000B 0x00000000
321 R AdcbRegs_ADCSOC12CTL 0x0000000B 0x00000000
322 R AdcbRegs_ADCSOC13CTL 0x0000000B 0x00000000
323 R AdcbRegs_ADCSOC14CTL 0x0000000B 0x00000000
324 R AdcbRegs_ADCSOC15CTL 0x0000000B 0x00000000
325 R AdcbRegs_ADCEVTSTAT 0x0000000F 0x1111

326 R AdcbRegs_ADCEVTCLR 0x0000000F 0x0000
327 R AdcbRegs_ADCEVTSEL 0x0000000F 0x0000
328 R AdcbRegs_ADCEVTINTSEL 0x0000000F 0x0000
329 R AdcbRegs_ADCCOUNTER 0x0000000F 0x0FC6
330 R AdcbRegs_ADCREV 0x0000000F 0x0005
331 R AdcbRegs_ADCOFFTRIM 0x0000000F 0x00F0
332 R AdcbRegs_ADCPPB1CONFIG 0x0000000F 0x0000
333 R AdcbRegs_ADCPPB1STAMP 0x0000000F 0x0002
334 R AdcbRegs_ADCPPB1OFFCAL 0x0000000F 0x0000
335 R AdcbRegs_ADCPPB1OFFREF 0x0000000F 0x0000
336 R AdcbRegs_ADCPPB1TRIPHI 0x0000000B 0x00000000
337 R AdcbRegs_ADCPPB1TRIPL0 0x0000000B 0x2B300000
338 R AdcbRegs_ADCPPB2CONFIG 0x0000000F 0x0000
339 R AdcbRegs_ADCPPB2STAMP 0x0000000F 0x0002
340 R AdcbRegs_ADCPPB2OFFCAL 0x0000000F 0x0000
341 R AdcbRegs_ADCPPB2OFFREF 0x0000000F 0x0000
342 R AdcbRegs_ADCPPB2TRIPHI 0x0000000B 0x00000000
343 R AdcbRegs_ADCPPB2TRIPL0 0x0000000B 0x3EB00000
344 R AdcbRegs_ADCPPB3CONFIG 0x0000000F 0x0000
345 R AdcbRegs_ADCPPB3STAMP 0x0000000F 0x0002
346 R AdcbRegs_ADCPPB3OFFCAL 0x0000000F 0x0000
347 R AdcbRegs_ADCPPB3OFFREF 0x0000000F 0x0000
348 R AdcbRegs_ADCPPB3TRIPHI 0x0000000B 0x00000000
349 R AdcbRegs_ADCPPB3TRIPL0 0x0000000B 0x55B00000
350 R AdcbRegs_ADCPPB4CONFIG 0x0000000F 0x0000
351 R AdcbRegs_ADCPPB4STAMP 0x0000000F 0x0002
352 R AdcbRegs_ADCPPB4OFFCAL 0x0000000F 0x0000
353 R AdcbRegs_ADCPPB4OFFREF 0x0000000F 0x0000
354 R AdcbRegs_ADCPPB4TRIPHI 0x0000000B 0x00000000
355 R AdcbRegs_ADCPPB4TRIPL0 0x0000000B 0x9F300000
356 R AdcbRegs_ADCINTCYCLE 0x0000000F 0x0000
357 R AdccRegs_ADCCTL1 0x0000000F 0x0384
358 R AdccRegs_ADCCTL2 0x0000000F 0x0006
359 R AdccRegs_ADCBURSTCTL 0x0000000F 0x0000
360 R AdccRegs_ADCINTFLG 0x0000000F 0x0000
361 R AdccRegs_ADCINTFLGCLR 0x0000000F 0x0000
362 R AdccRegs_ADCINTOVF 0x0000000F 0x0000
363 R AdccRegs_ADCINTOVFCLR 0x0000000F 0x0000
364 R AdccRegs_ADCINTSEL1N2 0x0000000F 0x0000
365 R AdccRegs_ADCINTSEL3N4 0x0000000F 0x0000
366 R AdccRegs_ADCSOCPRICTL 0x0000000F 0x0060
367 R AdccRegs_ADCINTSOCSEL1 0x0000000F 0x0000
368 R AdccRegs_ADCINTSOCSEL2 0x0000000F 0x0000
369 R AdccRegs_ADCSOCFLG1 0x0000000F 0x0000
370 R AdccRegs_ADCSOCFRC1 0x0000000F 0x0000
371 R AdccRegs_ADCSOCOVF1 0x0000000F 0x0000
372 R AdccRegs_ADCSOCOVFCLR1 0x0000000F 0x0000
373 R AdccRegs_ADCSOC0CTL 0x0000000B 0x01100009
374 R AdccRegs_ADCSOC1CTL 0x0000000B 0x01120009
375 R AdccRegs_ADCSOC2CTL 0x0000000B 0x01110009
376 R AdccRegs_ADCSOC3CTL 0x0000000B 0x01108009
377 R AdccRegs_ADCSOC4CTL 0x0000000B 0x00000000
378 R AdccRegs_ADCSOC5CTL 0x0000000B 0x00000000
379 R AdccRegs_ADCSOC6CTL 0x0000000B 0x00000000
380 R AdccRegs_ADCSOC7CTL 0x0000000B 0x00000000
381 R AdccRegs_ADCSOC8CTL 0x0000000B 0x00000000
382 R AdccRegs_ADCSOC9CTL 0x0000000B 0x00000000
383 R AdccRegs_ADCSOC10CTL 0x0000000B 0x00000000
384 R AdccRegs_ADCSOC11CTL 0x0000000B 0x00000000
385 R AdccRegs_ADCSOC12CTL 0x0000000B 0x00000000
386 R AdccRegs_ADCSOC13CTL 0x0000000B 0x00000000
387 R AdccRegs_ADCSOC14CTL 0x0000000B 0x00000000
388 R AdccRegs_ADCSOC15CTL 0x0000000B 0x00000000
389 R AdccRegs_ADCEVTSTAT 0x0000000F 0x1111
390 R AdccRegs_ADCEVTCLR 0x0000000F 0x0000

391 R AdccRegs_ADCEVTSEL 0x0000000F 0x0000
392 R AdccRegs_ADCEVTINTSEL 0x0000000F 0x0000
393 R AdccRegs_ADCCOUNTER 0x0000000F 0x04AF
394 R AdccRegs_ADCREV 0x0000000F 0x0005
395 R AdccRegs_ADCOFFTRIM 0x0000000F 0x00F0
396 R AdccRegs_ADCPPB1CONFIG 0x0000000F 0x0000
397 R AdccRegs_ADCPPB1STAMP 0x0000000F 0x0002
398 R AdccRegs_ADCPPB1OFFCAL 0x0000000F 0x0000
399 R AdccRegs_ADCPPB1OFFREF 0x0000000F 0x0000
400 R AdccRegs_ADCPPB1TRIPHI 0x0000000B 0x00000000
401 R AdccRegs_ADCPPB1TRIPL0 0x0000000B 0xAA000000
402 R AdccRegs_ADCPPB2CONFIG 0x0000000F 0x0000
403 R AdccRegs_ADCPPB2STAMP 0x0000000F 0x0002
404 R AdccRegs_ADCPPB2OFFCAL 0x0000000F 0x0000
405 R AdccRegs_ADCPPB2OFFREF 0x0000000F 0x0000
406 R AdccRegs_ADCPPB2TRIPHI 0x0000000B 0x00000000
407 R AdccRegs_ADCPPB2TRIPL0 0x0000000B 0x3E000000
408 R AdccRegs_ADCPPB3CONFIG 0x0000000F 0x0000
409 R AdccRegs_ADCPPB3STAMP 0x0000000F 0x0002
410 R AdccRegs_ADCPPB3OFFCAL 0x0000000F 0x0000
411 R AdccRegs_ADCPPB3OFFREF 0x0000000F 0x0000
412 R AdccRegs_ADCPPB3TRIPHI 0x0000000B 0x00000000
413 R AdccRegs_ADCPPB3TRIPL0 0x0000000B 0x55000000
414 R AdccRegs_ADCPPB4CONFIG 0x0000000F 0x0000
415 R AdccRegs_ADCPPB4STAMP 0x0000000F 0x0002
416 R AdccRegs_ADCPPB4OFFCAL 0x0000000F 0x0000
417 R AdccRegs_ADCPPB4OFFREF 0x0000000F 0x0000
418 R AdccRegs_ADCPPB4TRIPHI 0x0000000B 0x00000000
419 R AdccRegs_ADCPPB4TRIPL0 0x0000000B 0xEF000000
420 R AdccRegs_ADCINTCYCLE 0x0000000F 0x0000
421 R AnalogSubsysRegs_ANAREFPP_1 0x0000000F 0x0001
422 R AnalogSubsysRegs_TSNSCTL_1 0x0000000F 0x0000
423 R AnalogSubsysRegs_ANAREFCTL_1 0x0000000F 0x0007
424 R AnalogSubsysRegs_DCDCCTL_1 0x0000000B 0x00000000
425 R AnalogSubsysRegs_DCDCSTS_1 0x0000000F 0x0000
426 R AnalogSubsysRegs_CMPHPMXSEL_1 0x0000000B 0x00000000
427 R AnalogSubsysRegs_CMPLPMXSEL_1 0x0000000B 0x00000000
428 R AnalogSubsysRegs_CMPHNMXSEL_1 0x0000000F 0x0000
429 R AnalogSubsysRegs_CMPLNMXSEL_1 0x0000000F 0x0000
430 R AnalogSubsysRegs_LOCK_1 0x0000000B 0x00000000
431 R CanaRegs_CAN_CTL 0x0000000B 0x00001401
432 R CanaRegs_CAN_ES 0x0000000B 0x00000007
433 R CanaRegs_CAN_ERRC 0x0000000B 0x00000000
434 R CanaRegs_CAN_BTR 0x0000000B 0x00002301
435 R CanaRegs_CAN_INT 0x0000000B 0x00000000
436 R CanaRegs_CAN_TEST 0x0000000B 0x00000080
437 R CanaRegs_CAN_PERR 0x0000000B 0x00000110
438 R CanaRegs_CAN_RAM_INIT 0x0000000B 0x00000005
439 R CanaRegs_CAN_GLB_INT_EN 0x0000000B 0x00000000
440 R CanaRegs_CAN_GLB_INT_FLG 0x0000000B 0x00000000
441 R CanaRegs_CAN_GLB_INT_CLR 0x0000000B 0x00000000
442 R CanaRegs_CAN_ABOTR 0x0000000B 0x00000000
443 R CanaRegs_CAN_TXRQ_X 0x0000000B 0x00000000
444 R CanaRegs_CAN_TXRQ_21 0x0000000B 0x00000000
445 R CanaRegs_CAN_NDAT_X 0x0000000B 0x00000000
446 R CanaRegs_CAN_NDAT_21 0x0000000B 0x00000000
447 R CanaRegs_CAN_IPEN_X 0x0000000B 0x00000000
448 R CanaRegs_CAN_IPEN_21 0x0000000B 0x00000000
449 R CanaRegs_CAN_MVAL_X 0x0000000B 0x00000000
450 R CanaRegs_CAN_MVAL_21 0x0000000B 0x00000000
451 R CanaRegs_CAN_IP_MUX21 0x0000000B 0x00000000
452 R CanaRegs_CAN_IF1CMD 0x0000000B 0x00000001
453 R CanaRegs_CAN_IF1MSK 0x0000000B 0xFFFFFFFF
454 R CanaRegs_CAN_IF1ARB 0x0000000B 0x00000000
455 R CanaRegs_CAN_IF1MCTL 0x0000000B 0x00000000

456 R CanaRegs_CAN_IF1DATA 0x0000000B 0x00000000
457 R CanaRegs_CAN_IF1DATB 0x0000000B 0x00000000
458 R CanaRegs_CAN_IF2CMD 0x0000000B 0x00000001
459 R CanaRegs_CAN_IF2MSK 0x0000000B 0xFFFFFFFF
460 R CanaRegs_CAN_IF2ARB 0x0000000B 0x00000000
461 R CanaRegs_CAN_IF2MCTL 0x0000000B 0x00000000
462 R CanaRegs_CAN_IF2DATA 0x0000000B 0x00000000
463 R CanaRegs_CAN_IF2DATB 0x0000000B 0x00000000
464 R CanaRegs_CAN_IF3OBS 0x0000000B 0x00000000
465 R CanaRegs_CAN_IF3MSK 0x0000000B 0xFFFFFFFF
466 R CanaRegs_CAN_IF3ARB 0x0000000B 0x00000000
467 R CanaRegs_CAN_IF3MCTL 0x0000000B 0x00000000
468 R CanaRegs_CAN_IF3DATA 0x0000000B 0x00000000
469 R CanaRegs_CAN_IF3DATB 0x0000000B 0x00000000
470 R CanaRegs_CAN_IF3UPD 0x0000000B 0x00000000
471 R CanbRegs_CAN_CTL 0x0000000B 0x00001401
472 R CanbRegs_CAN_ES 0x0000000B 0x00000007
473 R CanbRegs_CAN_ERRC 0x0000000B 0x00000000
474 R CanbRegs_CAN_BTR 0x0000000B 0x00002301
475 R CanbRegs_CAN_INT 0x0000000B 0x00000000
476 R CanbRegs_CAN_TEST 0x0000000B 0x00000080
477 R CanbRegs_CAN_PERR 0x0000000B 0x00000633
478 R CanbRegs_CAN_RAM_INIT 0x0000000B 0x00000005
479 R CanbRegs_CAN_GLB_INT_EN 0x0000000B 0x00000000
480 R CanbRegs_CAN_GLB_INT_FLG 0x0000000B 0x00000000
481 R CanbRegs_CAN_GLB_INT_CLR 0x0000000B 0x00000000
482 R CanbRegs_CAN_ABOTR 0x0000000B 0x00000000
483 R CanbRegs_CAN_TXRQ_X 0x0000000B 0x00000000
484 R CanbRegs_CAN_TXRQ_21 0x0000000B 0x00000000
485 R CanbRegs_CAN_NDAT_X 0x0000000B 0x00000000
486 R CanbRegs_CAN_NDAT_21 0x0000000B 0x00000000
487 R CanbRegs_CAN_IPEN_X 0x0000000B 0x00000000
488 R CanbRegs_CAN_IPEN_21 0x0000000B 0x00000000
489 R CanbRegs_CAN_MVAL_X 0x0000000B 0x00000000
490 R CanbRegs_CAN_MVAL_21 0x0000000B 0x00000000
491 R CanbRegs_CAN_IP_MUX21 0x0000000B 0x00000000
492 R CanbRegs_CAN_IF1CMD 0x0000000B 0x00000001
493 R CanbRegs_CAN_IF1MSK 0x0000000B 0xFFFFFFFF
494 R CanbRegs_CAN_IF1ARB 0x0000000B 0x00000000
495 R CanbRegs_CAN_IF1MCTL 0x0000000B 0x00000000
496 R CanbRegs_CAN_IF1DATA 0x0000000B 0x00000000
497 R CanbRegs_CAN_IF1DATB 0x0000000B 0x00000000
498 R CanbRegs_CAN_IF2CMD 0x0000000B 0x00000001
499 R CanbRegs_CAN_IF2MSK 0x0000000B 0xFFFFFFFF
500 R CanbRegs_CAN_IF2ARB 0x0000000B 0x00000000
501 R CanbRegs_CAN_IF2MCTL 0x0000000B 0x00000000
502 R CanbRegs_CAN_IF2DATA 0x0000000B 0x00000000
503 R CanbRegs_CAN_IF2DATB 0x0000000B 0x00000000
504 R CanbRegs_CAN_IF3OBS 0x0000000B 0x00000000
505 R CanbRegs_CAN_IF3MSK 0x0000000B 0xFFFFFFFF
506 R CanbRegs_CAN_IF3ARB 0x0000000B 0x00000000
507 R CanbRegs_CAN_IF3MCTL 0x0000000B 0x00000000
508 R CanbRegs_CAN_IF3DATA 0x0000000B 0x00000000
509 R CanbRegs_CAN_IF3DATB 0x0000000B 0x00000000
510 R CanbRegs_CAN_IF3UPD 0x0000000B 0x00000000
511 R Cla1Regs_MVECT1 0x0000000F 0x8000
512 R Cla1Regs_MVECT2 0x0000000F 0x0000
513 R Cla1Regs_MVECT3 0x0000000F 0x0000
514 R Cla1Regs_MVECT4 0x0000000F 0x0000
515 R Cla1Regs_MVECT5 0x0000000F 0x0000
516 R Cla1Regs_MVECT6 0x0000000F 0x0000
517 R Cla1Regs_MVECT7 0x0000000F 0x0000
518 R Cla1Regs_MVECT8 0x0000000F 0x0000
519 R Cla1Regs_MCTL 0x0000000F 0x0004
520 R Cla1Regs__MVECTBGRNDACTIVE 0x0000000F 0x8202

521 R Cla1Regs_SOFTINTEN 0x0000000F 0x0000
522 R Cla1Regs_MSTSBGRND 0x0000000F 0x0000
523 R Cla1Regs_MCTLBGRND 0x0000000F 0x8000
524 R Cla1Regs_MVECTBGRND 0x0000000F 0x8202
525 R Cla1Regs_MIFR 0x0000000F 0x0000
526 R Cla1Regs_MIOVF 0x0000000F 0x0000
527 R Cla1Regs_MIFRC 0x0000000F 0x0000
528 R Cla1Regs_MICLR 0x0000000F 0x0000
529 R Cla1Regs_MICLROVF 0x0000000F 0x0000
530 R Cla1Regs_MIER 0x0000000F 0x0001
531 R Cla1Regs_MIRUN 0x0000000F 0x0000
532 R Cla1Regs_MPC 0x0000000F 0x8200
533 R Cla1Regs_MAR0 0x0000000F 0x0000
534 R Cla1Regs_MAR1 0x0000000F 0x0000
535 R Cla1Regs_MSTF 0x0000000B 0x00000000
536 R Cla1Regs_MR0 0x0000000B 0x00000000
537 R Cla1Regs_MR1 0x0000000B 0x00000001
538 R Cla1Regs_MR2 0x0000000B 0x00000000
539 R Cla1Regs_MR3 0x0000000B 0x00000000
540 R Cla1Regs_MPSACTL 0x0000000F 0x0000
541 R Cla1Regs_MPSA1 0x0000000B 0x00000000
542 R Cla1Regs_MPSA2 0x0000000B 0x00000000
543 R ClaPROMCRC32_CRC32-CONTROLREG 0x0000000B 0x00000000
544 R ClaPROMCRC32_CRC32-STARTADDRESS 0x0000000B 0x00000000
545 R ClaPROMCRC32_CRC32-SEED 0x0000000B 0x00000000
546 R ClaPROMCRC32_CRC32-STATUSREG 0x0000000B 0x00000000
547 R ClaPROMCRC32_CRC32-CRCRESULT 0x0000000B 0x00000000
548 R ClaPROMCRC32_CRC32-GOLDENCRC 0x0000000B 0x00000000
549 R ClaPROMCRC32_CRC32-INTEN 0x0000000B 0x00000000
550 R ClaPROMCRC32_CRC32-FLG 0x0000000B 0x00000000
551 R ClaPROMCRC32_CRC32-CLR 0x0000000B 0x00000000
552 R ClaPROMCRC32_CRC32-FRC 0x0000000B 0x00000000
553 R ClkCfgRegs_CLKCFGLOCK1 0x0000000B 0x00000000
554 R ClkCfgRegs_CLKSRCCTL1 0x0000000B 0x00000001
555 R ClkCfgRegs_CLKSRCCTL2 0x0000000B 0x00000000
556 R ClkCfgRegs_CLKSRCCTL3 0x0000000B 0x00000000
557 R ClkCfgRegs_SYSPLLCTL1 0x0000000B 0x00000003
558 R ClkCfgRegs_SYSPLLMULT 0x0000000B 0x0000000A
559 R ClkCfgRegs_SYSPLLSTS 0x0000000B 0x00000001
560 R ClkCfgRegs_SYSCLKDIVSEL 0x0000000B 0x00000001
561 R ClkCfgRegs_XCLKOUTDIVSEL 0x0000000B 0x00000003
562 R ClkCfgRegs_LOSPCP 0x0000000B 0x00000002
563 R ClkCfgRegs_MCDRC 0x0000000B 0x00000000
564 R ClkCfgRegs_X1CNT 0x0000000B 0x000003FF
565 R ClkCfgRegs_XTALCR 0x0000000B 0x00000004
566 R Cmpss1Regs_COMPCTL 0x0000000F 0x0000
567 R Cmpss1Regs_COMPHYCTL 0x0000000F 0x0000
568 R Cmpss1Regs_COMPSTS 0x0000000F 0x0000
569 R Cmpss1Regs_COMPSTSCLR 0x0000000F 0x0000
570 R Cmpss1Regs_COMPDACCTL 0x0000000F 0x0000
571 R Cmpss1Regs_DACHVALS 0x0000000F 0x0000
572 R Cmpss1Regs_DACHVALA 0x0000000F 0x0000
573 R Cmpss1Regs_RAMPMAXREFA 0x0000000F 0x0000
574 R Cmpss1Regs_RAMPMAXREFS 0x0000000F 0x0000
575 R Cmpss1Regs_RAMPDECVALA 0x0000000F 0x0000
576 R Cmpss1Regs_RAMPDECVALS 0x0000000F 0x0000
577 R Cmpss1Regs_RAMPSTS 0x0000000F 0x0000
578 R Cmpss1Regs_DACLVALS 0x0000000F 0x0000
579 R Cmpss1Regs_DACLVALA 0x0000000F 0x0000
580 R Cmpss1Regs_RAMPDLYA 0x0000000F 0x0000
581 R Cmpss1Regs_RAMPDLYS 0x0000000F 0x0000
582 R Cmpss1Regs_CTRIPLFILCTL 0x0000000F 0x0000
583 R Cmpss1Regs_CTRIPLFILCLKCTL 0x0000000F 0x0000
584 R Cmpss1Regs_CTRIPHFILCTL 0x0000000F 0x0000
585 R Cmpss1Regs_CTRIPHFILCLKCTL 0x0000000F 0x0000

586 R Cmpss1Regs_COMPLOCK 0x0000000F 0x0000
587 R Cmpss2Regs_COMPCTL 0x0000000F 0x0000
588 R Cmpss2Regs_COMPHYSCTL 0x0000000F 0x0000
589 R Cmpss2Regs_COMPSTS 0x0000000F 0x0000
590 R Cmpss2Regs_COMPSTSLR 0x0000000F 0x0000
591 R Cmpss2Regs_COMPDACCTL 0x0000000F 0x0000
592 R Cmpss2Regs_DACHVALS 0x0000000F 0x0000
593 R Cmpss2Regs_DACHVALA 0x0000000F 0x0000
594 R Cmpss2Regs_RAMPMAXREFA 0x0000000F 0x0000
595 R Cmpss2Regs_RAMPMAXREFS 0x0000000F 0x0000
596 R Cmpss2Regs_RAMPDECVALA 0x0000000F 0x0000
597 R Cmpss2Regs_RAMPDECVALS 0x0000000F 0x0000
598 R Cmpss2Regs_RAMPSTS 0x0000000F 0x0000
599 R Cmpss2Regs_DACLVALS 0x0000000F 0x0000
600 R Cmpss2Regs_DACLVALA 0x0000000F 0x0000
601 R Cmpss2Regs_RAMPDLYA 0x0000000F 0x0000
602 R Cmpss2Regs_RAMPDLYS 0x0000000F 0x0000
603 R Cmpss2Regs_CTRIPLFILCTL 0x0000000F 0x0000
604 R Cmpss2Regs_CTRIPLFILCLKCTL 0x0000000F 0x0000
605 R Cmpss2Regs_CTRIPHFILCTL 0x0000000F 0x0000
606 R Cmpss2Regs_CTRIPHFILCLKCTL 0x0000000F 0x0000
607 R Cmpss2Regs_COMPLOCK 0x0000000F 0x0000
608 R Cmpss3Regs_COMPCTL 0x0000000F 0x0000
609 R Cmpss3Regs_COMPHYSCTL 0x0000000F 0x0000
610 R Cmpss3Regs_COMPSTS 0x0000000F 0x0000
611 R Cmpss3Regs_COMPSTSLR 0x0000000F 0x0000
612 R Cmpss3Regs_COMPDACCTL 0x0000000F 0x0000
613 R Cmpss3Regs_DACHVALS 0x0000000F 0x0000
614 R Cmpss3Regs_DACHVALA 0x0000000F 0x0000
615 R Cmpss3Regs_RAMPMAXREFA 0x0000000F 0x0000
616 R Cmpss3Regs_RAMPMAXREFS 0x0000000F 0x0000
617 R Cmpss3Regs_RAMPDECVALA 0x0000000F 0x0000
618 R Cmpss3Regs_RAMPDECVALS 0x0000000F 0x0000
619 R Cmpss3Regs_RAMPSTS 0x0000000F 0x0000
620 R Cmpss3Regs_DACLVALS 0x0000000F 0x0000
621 R Cmpss3Regs_DACLVALA 0x0000000F 0x0000
622 R Cmpss3Regs_RAMPDLYA 0x0000000F 0x0000
623 R Cmpss3Regs_RAMPDLYS 0x0000000F 0x0000
624 R Cmpss3Regs_CTRIPLFILCTL 0x0000000F 0x0000
625 R Cmpss3Regs_CTRIPLFILCLKCTL 0x0000000F 0x0000
626 R Cmpss3Regs_CTRIPHFILCTL 0x0000000F 0x0000
627 R Cmpss3Regs_CTRIPHFILCLKCTL 0x0000000F 0x0000
628 R Cmpss3Regs_COMPLOCK 0x0000000F 0x0000
629 R Cmpss4Regs_COMPCTL 0x0000000F 0x0000
630 R Cmpss4Regs_COMPHYSCTL 0x0000000F 0x0000
631 R Cmpss4Regs_COMPSTS 0x0000000F 0x0000
632 R Cmpss4Regs_COMPSTSLR 0x0000000F 0x0000
633 R Cmpss4Regs_COMPDACCTL 0x0000000F 0x0000
634 R Cmpss4Regs_DACHVALS 0x0000000F 0x0000
635 R Cmpss4Regs_DACHVALA 0x0000000F 0x0000
636 R Cmpss4Regs_RAMPMAXREFA 0x0000000F 0x0000
637 R Cmpss4Regs_RAMPMAXREFS 0x0000000F 0x0000
638 R Cmpss4Regs_RAMPDECVALA 0x0000000F 0x0000
639 R Cmpss4Regs_RAMPDECVALS 0x0000000F 0x0000
640 R Cmpss4Regs_RAMPSTS 0x0000000F 0x0000
641 R Cmpss4Regs_DACLVALS 0x0000000F 0x0000
642 R Cmpss4Regs_DACLVALA 0x0000000F 0x0000
643 R Cmpss4Regs_RAMPDLYA 0x0000000F 0x0000
644 R Cmpss4Regs_RAMPDLYS 0x0000000F 0x0000
645 R Cmpss4Regs_CTRIPLFILCTL 0x0000000F 0x0000
646 R Cmpss4Regs_CTRIPLFILCLKCTL 0x0000000F 0x0000
647 R Cmpss4Regs_CTRIPHFILCTL 0x0000000F 0x0000
648 R Cmpss4Regs_CTRIPHFILCLKCTL 0x0000000F 0x0000
649 R Cmpss4Regs_COMPLOCK 0x0000000F 0x0000
650 R Cmpss5Regs_COMPCTL 0x0000000F 0x0000

651 R Cmpss5Regs_COMPHYCTL 0x0000000F 0x0000
652 R Cmpss5Regs_COMPSTS 0x0000000F 0x0000
653 R Cmpss5Regs_COMPSTSLR 0x0000000F 0x0000
654 R Cmpss5Regs_COMPDACCTL 0x0000000F 0x0000
655 R Cmpss5Regs_DACHVALS 0x0000000F 0x0000
656 R Cmpss5Regs_DACHVALA 0x0000000F 0x0000
657 R Cmpss5Regs_RAMPMAXREFA 0x0000000F 0x0000
658 R Cmpss5Regs_RAMPMAXREFS 0x0000000F 0x0000
659 R Cmpss5Regs_RAMPDECVALA 0x0000000F 0x0000
660 R Cmpss5Regs_RAMPDECVALS 0x0000000F 0x0000
661 R Cmpss5Regs_RAMPSTS 0x0000000F 0x0000
662 R Cmpss5Regs_DACLVALS 0x0000000F 0x0000
663 R Cmpss5Regs_DACLVALA 0x0000000F 0x0000
664 R Cmpss5Regs_RAMPDLYA 0x0000000F 0x0000
665 R Cmpss5Regs_RAMPDLYS 0x0000000F 0x0000
666 R Cmpss5Regs_CTRIPLFILCTL 0x0000000F 0x0000
667 R Cmpss5Regs_CTRIPLFILCLKCTL 0x0000000F 0x0000
668 R Cmpss5Regs_CTRIPHFILCTL 0x0000000F 0x0000
669 R Cmpss5Regs_CTRIPHFILCLKCTL 0x0000000F 0x0000
670 R Cmpss5Regs_COMPLOCK 0x0000000F 0x0000
671 R Cmpss6Regs_COMPCTL 0x0000000F 0x0000
672 R Cmpss6Regs_COMPHYCTL 0x0000000F 0x0000
673 R Cmpss6Regs_COMPSTS 0x0000000F 0x0000
674 R Cmpss6Regs_COMPSTSLR 0x0000000F 0x0000
675 R Cmpss6Regs_COMPDACCTL 0x0000000F 0x0000
676 R Cmpss6Regs_DACHVALS 0x0000000F 0x0000
677 R Cmpss6Regs_DACHVALA 0x0000000F 0x0000
678 R Cmpss6Regs_RAMPMAXREFA 0x0000000F 0x0000
679 R Cmpss6Regs_RAMPMAXREFS 0x0000000F 0x0000
680 R Cmpss6Regs_RAMPDECVALA 0x0000000F 0x0000
681 R Cmpss6Regs_RAMPDECVALS 0x0000000F 0x0000
682 R Cmpss6Regs_RAMPSTS 0x0000000F 0x0000
683 R Cmpss6Regs_DACLVALS 0x0000000F 0x0000
684 R Cmpss6Regs_DACLVALA 0x0000000F 0x0000
685 R Cmpss6Regs_RAMPDLYA 0x0000000F 0x0000
686 R Cmpss6Regs_RAMPDLYS 0x0000000F 0x0000
687 R Cmpss6Regs_CTRIPLFILCTL 0x0000000F 0x0000
688 R Cmpss6Regs_CTRIPLFILCLKCTL 0x0000000F 0x0000
689 R Cmpss6Regs_CTRIPHFILCTL 0x0000000F 0x0000
690 R Cmpss6Regs_CTRIPHFILCLKCTL 0x0000000F 0x0000
691 R Cmpss6Regs_COMPLOCK 0x0000000F 0x0000
692 R Cmpss7Regs_COMPCTL 0x0000000F 0x0000
693 R Cmpss7Regs_COMPHYCTL 0x0000000F 0x0000
694 R Cmpss7Regs_COMPSTS 0x0000000F 0x0000
695 R Cmpss7Regs_COMPSTSLR 0x0000000F 0x0000
696 R Cmpss7Regs_COMPDACCTL 0x0000000F 0x0000
697 R Cmpss7Regs_DACHVALS 0x0000000F 0x0000
698 R Cmpss7Regs_DACHVALA 0x0000000F 0x0000
699 R Cmpss7Regs_RAMPMAXREFA 0x0000000F 0x0000
700 R Cmpss7Regs_RAMPMAXREFS 0x0000000F 0x0000
701 R Cmpss7Regs_RAMPDECVALA 0x0000000F 0x0000
702 R Cmpss7Regs_RAMPDECVALS 0x0000000F 0x0000
703 R Cmpss7Regs_RAMPSTS 0x0000000F 0x0000
704 R Cmpss7Regs_DACLVALS 0x0000000F 0x0000
705 R Cmpss7Regs_DACLVALA 0x0000000F 0x0000
706 R Cmpss7Regs_RAMPDLYA 0x0000000F 0x0000
707 R Cmpss7Regs_RAMPDLYS 0x0000000F 0x0000
708 R Cmpss7Regs_CTRIPLFILCTL 0x0000000F 0x0000
709 R Cmpss7Regs_CTRIPLFILCLKCTL 0x0000000F 0x0000
710 R Cmpss7Regs_CTRIPHFILCTL 0x0000000F 0x0000
711 R Cmpss7Regs_CTRIPHFILCLKCTL 0x0000000F 0x0000
712 R Cmpss7Regs_COMPLOCK 0x0000000F 0x0000
713 R CpuSysRegs_CPUSYSLOCK1 0x0000000B 0x00000000
714 R CpuSysRegs_PIEVERRADDR 0x0000000B 0x003FFFFF
715 R CpuSysRegs_PCLKCR0 0x0000000B 0x0005003D

716 R CpuSysRegs_PCLKCR2 0x0000000B 0x000000FF
717 R CpuSysRegs_PCLKCR3 0x0000000B 0x0000007F
718 R CpuSysRegs_PCLKCR4 0x0000000B 0x00000003
719 R CpuSysRegs_PCLKCR6 0x0000000B 0x00000001
720 R CpuSysRegs_PCLKCR7 0x0000000B 0x00000003
721 R CpuSysRegs_PCLKCR8 0x0000000B 0x00000003
722 R CpuSysRegs_PCLKCR9 0x0000000B 0x00000001
723 R CpuSysRegs_PCLKCR10 0x0000000B 0x00000003
724 R CpuSysRegs_PCLKCR13 0x0000000B 0x00000007
725 R CpuSysRegs_PCLKCR14 0x0000000B 0x0000007F
726 R CpuSysRegs_PCLKCR15 0x0000000B 0x0000007F
727 R CpuSysRegs_PCLKCR16 0x0000000B 0x00030000
728 R CpuSysRegs_PCLKCR17 0x0000000B 0x00000000
729 R CpuSysRegs_PCLKCR18 0x0000000B 0x00000000
730 R CpuSysRegs_PCLKCR19 0x0000000B 0x00000001
731 R CpuSysRegs_PCLKCR20 0x0000000B 0x00000001
732 R CpuSysRegs_PCLKCR21 0x0000000B 0x00000001
733 R CpuSysRegs_LPMCR 0x0000000B 0x000000FC
734 R CpuSysRegs_GPIOLPMSEL0 0x0000000B 0x00000000
735 R CpuSysRegs_GPIOLPMSEL1 0x0000000B 0x00000000
736 R CpuSysRegs_TMR2CLKCTL 0x0000000B 0x00000000
737 R CpuSysRegs_RESCCLR 0x0000000B 0x00000000
738 R CpuSysRegs_RESC 0x0000000B 0xC0000000
739 R CpuTimer0Regs_TIM 0x0000000B 0x00001A4B
740 R CpuTimer0Regs_PRD 0x0000000B 0x00002710
741 R CpuTimer0Regs_TCR 0x0000000F 0xC001
742 R CpuTimer0Regs_TPR 0x0000000F 0x0000
743 R CpuTimer0Regs_TPRH 0x0000000F 0x0000
744 R CpuTimer1Regs_TIM 0x0000000B 0x00006AE4
745 R CpuTimer1Regs_PRD 0x0000000B 0x0000FFFF
746 R CpuTimer1Regs_TCR 0x0000000F 0x8001
747 R CpuTimer1Regs_TPR 0x0000000F 0x0000
748 R CpuTimer1Regs_TPRH 0x0000000F 0x0000
749 R CpuTimer2Regs_TIM 0x0000000B 0x00005D15
750 R CpuTimer2Regs_PRD 0x0000000B 0x0000FFFF
751 R CpuTimer2Regs_TCR 0x0000000F 0x8001
752 R CpuTimer2Regs_TPR 0x0000000F 0x0000
753 R CpuTimer2Regs_TPRH 0x0000000F 0x0000
754 R DacRegs_DACREV 0x0000000F 0x0000
755 R DacRegs_DACCTL 0x0000000F 0x0000
756 R DacRegs_DACVALA 0x0000000F 0x0000
757 R DacRegs_DACVALS 0x0000000F 0x0000
758 R DacRegs_DACOUTEN 0x0000000F 0x0000
759 R DacRegs_DACLOCK 0x0000000F 0x0000
760 R DacRegs_DACTRIM 0x0000000F 0x0000
761 R DacbRegs_DACREV 0x0000000F 0x0000
762 R DacbRegs_DACCTL 0x0000000F 0x0000
763 R DacbRegs_DACVALA 0x0000000F 0x0000
764 R DacbRegs_DACVALS 0x0000000F 0x0000
765 R DacbRegs_DACOUTEN 0x0000000F 0x0000
766 R DacbRegs_DACLOCK 0x0000000F 0x0000
767 R DacbRegs_DACTRIM 0x0000000F 0x0000
768 R Dcc0Regs_DCCGCTRL 0x0000000B 0x0000AAA5
769 R Dcc0Regs_DCCREV 0x0000000B 0x0000AAA5
770 R Dcc0Regs_DCCNTSEED0 0x0000000B 0x4000204
771 R Dcc0Regs_DCCVALIDSEED0 0x0000000B 0x4000204
772 R Dcc0Regs_DCCNTSEED1 0x0000000B 0x00000062
773 R Dcc0Regs_DCCSTATUS 0x0000000B 0x00000062
774 R Dcc0Regs_DCCNT0 0x0000000B 0x00000004
775 R Dcc0Regs_DCCVALID0 0x0000000B 0x00000004
776 R Dcc0Regs_DCCNT1 0x0000000B 0x000003E8
777 R Dcc0Regs_DCCCLKSRC1 0x0000000B 0x000003E8
778 R Dcc0Regs_DCCCLKSRC0 0x0000000B 0x00000002
779 R DcsmCommonRegs_FLSEM_1 0x0000000B 0x00000000
780 R DcsmCommonRegs_B0_SECTSTAT_1 0x0000000B 0xFFFFFFFF

781 R DcsmCommonRegs_RAMSTAT_1 0x0000000B 0x0000FFFF
782 R DcsmCommonRegs_B1_SECTSTAT_1 0x0000000B 0xFFFFFFFF
783 R DcsmCommonRegs_SECERRSTAT 0x0000000B 0x00000000
784 R DcsmCommonRegs_SECERRCLR 0x0000000B 0x00000000
785 R DcsmCommonRegs_SECERRFRC 0x0000000B 0x00000000
786 R DcsmBank0Z1Regs_B0_Z1_LINKPOINTER_1 0x0000000B 0xFFFFFFFF
787 R DcsmBank0Z1Regs_Z1_OTPSECLOCK_1 0x0000000B 0x00000FFF
788 R DcsmBank0Z1Regs_Z1_BOOTDEF_HIGH_1 0x0000000B 0xFFFFFFFF
789 R DcsmBank0Z1Regs_B0_Z1_LINKPOINTERERR_1 0x0000000B 0x00000000
790 R DcsmBank0Z1Regs_Z1_BOOTPIN_CONFIG_1 0x0000000B 0xFFFFFFFF
791 R DcsmBank0Z1Regs_Z1_GPREG2_1 0x0000000B 0xFFFFFFFF
792 R DcsmBank0Z1Regs_Z1_BOOTDEF_LOW_1 0x0000000B 0xFFFFFFFF
793 R DcsmBank0Z1Regs_Z1_CSMKEY0_1 0x0000000B 0xFFFFFFFF
794 R DcsmBank0Z1Regs_Z1_CSMKEY1_1 0x0000000B 0x47FFFFFF
795 R DcsmBank0Z1Regs_Z1_CSMKEY2_1 0x0000000B 0xFFFFFFFF
796 R DcsmBank0Z1Regs_Z1_CSMKEY3_1 0x0000000B 0xFFFFFFFF
797 R DcsmBank0Z1Regs_Z1_CR_1 0x0000000F 0x0060
798 R DcsmBank0Z1Regs_B0_Z1_GRABSECTR_1 0x0000000B 0xFFFFFFFF
799 R DcsmBank0Z1Regs_Z1_GRABRAMR_1 0x0000000B 0x0000FFFF
800 R DcsmBank0Z1Regs_B0_Z1_EXEONLYSECTR_1 0x0000000B 0x0000FFFF
801 R DcsmBank0Z1Regs_Z1_EXEONLYRAMR_1 0x0000000B 0x000000FF
802 R DcsmBank0Z2Regs_B0_Z2_LINKPOINTER_1 0x0000000B 0xFFFFFFFF
803 R DcsmBank0Z2Regs_Z2_OTPSECLOCK_1 0x0000000B 0x00000FFF
804 R DcsmBank0Z2Regs_B0_Z2_LINKPOINTERERR_1 0x0000000B 0x00000000
805 R DcsmBank0Z2Regs_Z2_CSMKEY0_1 0x0000000B 0xFFFFFFFF
806 R DcsmBank0Z2Regs_Z2_CSMKEY1_1 0x0000000B 0xE3FFFFFF
807 R DcsmBank0Z2Regs_Z2_CSMKEY2_1 0x0000000B 0xFFFFFFFF
808 R DcsmBank0Z2Regs_Z2_CSMKEY3_1 0x0000000B 0xFFFFFFFF
809 R DcsmBank0Z2Regs_Z2_CR_1 0x0000000F 0x0060
810 R DcsmBank0Z2Regs_B0_Z2_GRABSECTR_1 0x0000000B 0xFFFFFFFF
811 R DcsmBank0Z2Regs_Z2_GRABRAMR_1 0x0000000B 0x0000FFFF
812 R DcsmBank0Z2Regs_B0_Z2_EXEONLYSECTR_1 0x0000000B 0x0000FFFF
813 R DcsmBank0Z2Regs_Z2_EXEONLYRAMR_1 0x0000000B 0x000000FF
814 R DcsmBank1Z1Regs_B1_Z1_LINKPOINTER_1 0x0000000B 0xFFFFFFFF
815 R DcsmBank1Z1Regs_B1_Z1_LINKPOINTERERR_1 0x0000000B 0x00000000
816 R DcsmBank1Z1Regs_B1_Z1_GRABSECTR_1 0x0000000B 0xFFFFFFFF
817 R DcsmBank1Z1Regs_B1_Z1_EXEONLYSECTR_1 0x0000000B 0x0000FFFF
818 R DcsmBank1Z2Regs_B1_Z2_LINKPOINTER_1 0x0000000B 0xFFFFFFFF
819 R DcsmBank1Z2Regs_B1_Z2_LINKPOINTERERR_1 0x0000000B 0x00000000
820 R DcsmBank1Z2Regs_B1_Z2_GRABSECTR_1 0x0000000B 0xFFFFFFFF
821 R DcsmBank1Z2Regs_B1_Z2_EXEONLYSECTR_1 0x0000000B 0x0000FFFF
822 R DevCfgRegs_PARTIDL_1 0x0000000B 0x20064280
823 R DevCfgRegs_PARTIDH_1 0x0000000B 0x01FF0500
824 R DevCfgRegs_REVID 0x0000000B 0x00000002
825 R DevCfgRegs_FUSEERR 0x0000000B 0x00000000
826 R DevCfgRegs_SOFTPRES0 0x0000000B 0x00000000
827 R DevCfgRegs_SOFTPRES2 0x0000000B 0x00000000
828 R DevCfgRegs_SOFTPRES3 0x0000000B 0x00000000
829 R DevCfgRegs_SOFTPRES4 0x0000000B 0x00000000
830 R DevCfgRegs_SOFTPRES6 0x0000000B 0x00000000
831 R DevCfgRegs_SOFTPRES7 0x0000000B 0x00000000
832 R DevCfgRegs_SOFTPRES8 0x0000000B 0x00000000
833 R DevCfgRegs_SOFTPRES9 0x0000000B 0x00000000
834 R DevCfgRegs_SOFTPRES10 0x0000000B 0x00000000
835 R DevCfgRegs_SOFTPRES13 0x0000000B 0x00000000
836 R DevCfgRegs_SOFTPRES14 0x0000000B 0x00000000
837 R DevCfgRegs_SOFTPRES15 0x0000000B 0x00000000
838 R DevCfgRegs_SOFTPRES16 0x0000000B 0x00000000
839 R DevCfgRegs_SOFTPRES17_1 0x0000000B 0x00000000
840 R DevCfgRegs_SOFTPRES18_1 0x0000000B 0x00000000
841 R DevCfgRegs_SOFTPRES19 0x0000000B 0x00000000
842 R DevCfgRegs_SOFTPRES20_1 0x0000000B 0x00000000
843 R DevCfgRegs_SOFTPRES21 0x0000000B 0x00000000
844 R DevCfgRegs_TAP_STATUS_1 0x0000000B 0x80000002
845 R DmaClaSrcSelRegs_CLA1TASKSRCSELLOCK 0x0000000B 0x00000000

846 R DmaClaSrcSelRegs_DMACHSRCSELLOCK 0x0000000B 0x00000000
847 R DmaClaSrcSelRegs_CLA1TASKSRCSEL1 0x0000000B 0x00000025
848 R DmaClaSrcSelRegs_CLA1TASKSRCSEL2 0x0000000B 0x00000000
849 R DmaClaSrcSelRegs_DMACHSRCSEL1 0x0000000B 0x00000000
850 R DmaClaSrcSelRegs_DMACHSRCSEL2 0x0000000B 0x00000000
851 R DmaRegs_DMACTRL 0x0000000F 0x0000
852 R DmaRegs_DEBUGCTRL 0x0000000F 0x0000
853 R DmaRegs_PRIORITYCTRL1 0x0000000F 0x0000
854 R DmaRegs_PRIORITYSTAT 0x0000000F 0x0000
855 R DmaCh1Regs_MODE 0x0000000F 0x0000
856 R DmaCh1Regs_CONTROL 0x0000000F 0x0000
857 R DmaCh1Regs_BURST_SIZE 0x0000000F 0x0000
858 R DmaCh1Regs_BURST_COUNT 0x0000000F 0x0000
859 R DmaCh1Regs_SRC_BURST_STEP 0x0000000F 0x0000
860 R DmaCh1Regs_DST_BURST_STEP 0x0000000F 0x0000
861 R DmaCh1Regs_TRANSFER_SIZE 0x0000000F 0x0000
862 R DmaCh1Regs_TRANSFER_COUNT 0x0000000F 0x0000
863 R DmaCh1Regs_SRC_TRANSFER_STEP 0x0000000F 0x0000
864 R DmaCh1Regs_DST_TRANSFER_STEP 0x0000000F 0x0000
865 R DmaCh1Regs_SRC_WRAP_SIZE 0x0000000F 0xFFFF
866 R DmaCh1Regs_SRC_WRAP_COUNT 0x0000000F 0x0000
867 R DmaCh1Regs_SRC_WRAP_STEP 0x0000000F 0x0000
868 R DmaCh1Regs_DST_WRAP_SIZE 0x0000000F 0xFFFF
869 R DmaCh1Regs_DST_WRAP_COUNT 0x0000000F 0x0000
870 R DmaCh1Regs_DST_WRAP_STEP 0x0000000F 0x0000
871 R DmaCh1Regs_SRC_BEG_ADDR_SHADOW 0x0000000B 0x00000000
872 R DmaCh1Regs_SRC_ADDR_SHADOW 0x0000000B 0x00000000
873 R DmaCh1Regs_SRC_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
874 R DmaCh1Regs_SRC_ADDR_ACTIVE 0x0000000B 0x00000000
875 R DmaCh1Regs_DST_BEG_ADDR_SHADOW 0x0000000B 0x00000000
876 R DmaCh1Regs_DST_ADDR_SHADOW 0x0000000B 0x00000000
877 R DmaCh1Regs_DST_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
878 R DmaCh1Regs_DST_ADDR_ACTIVE 0x0000000B 0x00000000
879 R DmaCh2Regs_MODE 0x0000000F 0x0000
880 R DmaCh2Regs_CONTROL 0x0000000F 0x0000
881 R DmaCh2Regs_BURST_SIZE 0x0000000F 0x0000
882 R DmaCh2Regs_BURST_COUNT 0x0000000F 0x0000
883 R DmaCh2Regs_SRC_BURST_STEP 0x0000000F 0x0000
884 R DmaCh2Regs_DST_BURST_STEP 0x0000000F 0x0000
885 R DmaCh2Regs_TRANSFER_SIZE 0x0000000F 0x0000
886 R DmaCh2Regs_TRANSFER_COUNT 0x0000000F 0x0000
887 R DmaCh2Regs_SRC_TRANSFER_STEP 0x0000000F 0x0000
888 R DmaCh2Regs_DST_TRANSFER_STEP 0x0000000F 0x0000
889 R DmaCh2Regs_SRC_WRAP_SIZE 0x0000000F 0xFFFF
890 R DmaCh2Regs_SRC_WRAP_COUNT 0x0000000F 0x0000
891 R DmaCh2Regs_SRC_WRAP_STEP 0x0000000F 0x0000
892 R DmaCh2Regs_DST_WRAP_SIZE 0x0000000F 0xFFFF
893 R DmaCh2Regs_DST_WRAP_COUNT 0x0000000F 0x0000
894 R DmaCh2Regs_DST_WRAP_STEP 0x0000000F 0x0000
895 R DmaCh2Regs_SRC_BEG_ADDR_SHADOW 0x0000000B 0x00000000
896 R DmaCh2Regs_SRC_ADDR_SHADOW 0x0000000B 0x00000000
897 R DmaCh2Regs_SRC_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
898 R DmaCh2Regs_SRC_ADDR_ACTIVE 0x0000000B 0x00000000
899 R DmaCh2Regs_DST_BEG_ADDR_SHADOW 0x0000000B 0x00000000
900 R DmaCh2Regs_DST_ADDR_SHADOW 0x0000000B 0x00000000
901 R DmaCh2Regs_DST_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
902 R DmaCh2Regs_DST_ADDR_ACTIVE 0x0000000B 0x00000000
903 R DmaCh3Regs_MODE 0x0000000F 0x0000
904 R DmaCh3Regs_CONTROL 0x0000000F 0x0000
905 R DmaCh3Regs_BURST_SIZE 0x0000000F 0x0000
906 R DmaCh3Regs_BURST_COUNT 0x0000000F 0x0000
907 R DmaCh3Regs_SRC_BURST_STEP 0x0000000F 0x0000
908 R DmaCh3Regs_DST_BURST_STEP 0x0000000F 0x0000
909 R DmaCh3Regs_TRANSFER_SIZE 0x0000000F 0x0000
910 R DmaCh3Regs_TRANSFER_COUNT 0x0000000F 0x0000

911 R DmaCh3Regs_SRC_TRANSFER_STEP 0x0000000F 0x0000
912 R DmaCh3Regs_DST_TRANSFER_STEP 0x0000000F 0x0000
913 R DmaCh3Regs_SRC_WRAP_SIZE 0x0000000F 0xFFFF
914 R DmaCh3Regs_SRC_WRAP_COUNT 0x0000000F 0x0000
915 R DmaCh3Regs_SRC_WRAP_STEP 0x0000000F 0x0000
916 R DmaCh3Regs_DST_WRAP_SIZE 0x0000000F 0xFFFF
917 R DmaCh3Regs_DST_WRAP_COUNT 0x0000000F 0x0000
918 R DmaCh3Regs_DST_WRAP_STEP 0x0000000F 0x0000
919 R DmaCh3Regs_SRC_BEG_ADDR_SHADOW 0x0000000B 0x00000000
920 R DmaCh3Regs_SRC_ADDR_SHADOW 0x0000000B 0x00000000
921 R DmaCh3Regs_SRC_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
922 R DmaCh3Regs_SRC_ADDR_ACTIVE 0x0000000B 0x00000000
923 R DmaCh3Regs_DST_BEG_ADDR_SHADOW 0x0000000B 0x00000000
924 R DmaCh3Regs_DST_ADDR_SHADOW 0x0000000B 0x00000000
925 R DmaCh3Regs_DST_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
926 R DmaCh3Regs_DST_ADDR_ACTIVE 0x0000000B 0x00000000
927 R DmaCh4Regs_MODE 0x0000000F 0x0000
928 R DmaCh4Regs_CONTROL 0x0000000F 0x0000
929 R DmaCh4Regs_BURST_SIZE 0x0000000F 0x0000
930 R DmaCh4Regs_BURST_COUNT 0x0000000F 0x0000
931 R DmaCh4Regs_SRC_BURST_STEP 0x0000000F 0x0000
932 R DmaCh4Regs_DST_BURST_STEP 0x0000000F 0x0000
933 R DmaCh4Regs_TRANSFER_SIZE 0x0000000F 0x0000
934 R DmaCh4Regs_TRANSFER_COUNT 0x0000000F 0x0000
935 R DmaCh4Regs_SRC_TRANSFER_STEP 0x0000000F 0x0000
936 R DmaCh4Regs_DST_TRANSFER_STEP 0x0000000F 0x0000
937 R DmaCh4Regs_SRC_WRAP_SIZE 0x0000000F 0xFFFF
938 R DmaCh4Regs_SRC_WRAP_COUNT 0x0000000F 0x0000
939 R DmaCh4Regs_SRC_WRAP_STEP 0x0000000F 0x0000
940 R DmaCh4Regs_DST_WRAP_SIZE 0x0000000F 0xFFFF
941 R DmaCh4Regs_DST_WRAP_COUNT 0x0000000F 0x0000
942 R DmaCh4Regs_DST_WRAP_STEP 0x0000000F 0x0000
943 R DmaCh4Regs_SRC_BEG_ADDR_SHADOW 0x0000000B 0x00000000
944 R DmaCh4Regs_SRC_ADDR_SHADOW 0x0000000B 0x00000000
945 R DmaCh4Regs_SRC_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
946 R DmaCh4Regs_SRC_ADDR_ACTIVE 0x0000000B 0x00000000
947 R DmaCh4Regs_DST_BEG_ADDR_SHADOW 0x0000000B 0x00000000
948 R DmaCh4Regs_DST_ADDR_SHADOW 0x0000000B 0x00000000
949 R DmaCh4Regs_DST_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
950 R DmaCh4Regs_DST_ADDR_ACTIVE 0x0000000B 0x00000000
951 R DmaCh5Regs_MODE 0x0000000F 0x0000
952 R DmaCh5Regs_CONTROL 0x0000000F 0x0000
953 R DmaCh5Regs_BURST_SIZE 0x0000000F 0x0000
954 R DmaCh5Regs_BURST_COUNT 0x0000000F 0x0000
955 R DmaCh5Regs_SRC_BURST_STEP 0x0000000F 0x0000
956 R DmaCh5Regs_DST_BURST_STEP 0x0000000F 0x0000
957 R DmaCh5Regs_TRANSFER_SIZE 0x0000000F 0x0000
958 R DmaCh5Regs_TRANSFER_COUNT 0x0000000F 0x0000
959 R DmaCh5Regs_SRC_TRANSFER_STEP 0x0000000F 0x0000
960 R DmaCh5Regs_DST_TRANSFER_STEP 0x0000000F 0x0000
961 R DmaCh5Regs_SRC_WRAP_SIZE 0x0000000F 0xFFFF
962 R DmaCh5Regs_SRC_WRAP_COUNT 0x0000000F 0x0000
963 R DmaCh5Regs_SRC_WRAP_STEP 0x0000000F 0x0000
964 R DmaCh5Regs_DST_WRAP_SIZE 0x0000000F 0xFFFF
965 R DmaCh5Regs_DST_WRAP_COUNT 0x0000000F 0x0000
966 R DmaCh5Regs_DST_WRAP_STEP 0x0000000F 0x0000
967 R DmaCh5Regs_SRC_BEG_ADDR_SHADOW 0x0000000B 0x00000000
968 R DmaCh5Regs_SRC_ADDR_SHADOW 0x0000000B 0x00000000
969 R DmaCh5Regs_SRC_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
970 R DmaCh5Regs_SRC_ADDR_ACTIVE 0x0000000B 0x00000000
971 R DmaCh5Regs_DST_BEG_ADDR_SHADOW 0x0000000B 0x00000000
972 R DmaCh5Regs_DST_ADDR_SHADOW 0x0000000B 0x00000000
973 R DmaCh5Regs_DST_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
974 R DmaCh5Regs_DST_ADDR_ACTIVE 0x0000000B 0x00000000
975 R DmaCh6Regs_MODE 0x0000000F 0x0000

976 R DmaCh6Regs_CONTROL 0x0000000F 0x0000
977 R DmaCh6Regs_BURST_SIZE 0x0000000F 0x0000
978 R DmaCh6Regs_BURST_COUNT 0x0000000F 0x0000
979 R DmaCh6Regs_SRC_BURST_STEP 0x0000000F 0x0000
980 R DmaCh6Regs_DST_BURST_STEP 0x0000000F 0x0000
981 R DmaCh6Regs_TRANSFER_SIZE 0x0000000F 0x0000
982 R DmaCh6Regs_TRANSFER_COUNT 0x0000000F 0x0000
983 R DmaCh6Regs_SRC_TRANSFER_STEP 0x0000000F 0x0000
984 R DmaCh6Regs_DST_TRANSFER_STEP 0x0000000F 0x0000
985 R DmaCh6Regs_SRC_WRAP_SIZE 0x0000000F 0xFFFF
986 R DmaCh6Regs_SRC_WRAP_COUNT 0x0000000F 0x0000
987 R DmaCh6Regs_SRC_WRAP_STEP 0x0000000F 0x0000
988 R DmaCh6Regs_DST_WRAP_SIZE 0x0000000F 0xFFFF
989 R DmaCh6Regs_DST_WRAP_COUNT 0x0000000F 0x0000
990 R DmaCh6Regs_DST_WRAP_STEP 0x0000000F 0x0000
991 R DmaCh6Regs_SRC_BEG_ADDR_SHADOW 0x0000000B 0x00000000
992 R DmaCh6Regs_SRC_ADDR_SHADOW 0x0000000B 0x00000000
993 R DmaCh6Regs_SRC_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
994 R DmaCh6Regs_SRC_ADDR_ACTIVE 0x0000000B 0x00000000
995 R DmaCh6Regs_DST_BEG_ADDR_SHADOW 0x0000000B 0x00000000
996 R DmaCh6Regs_DST_ADDR_SHADOW 0x0000000B 0x00000000
997 R DmaCh6Regs_DST_BEG_ADDR_ACTIVE 0x0000000B 0x00000000
998 R DmaCh6Regs_DST_ADDR_ACTIVE 0x0000000B 0x00000000
999 R ECap1Regs_TSCTR 0x0000000B 0x000636C7
1000 R ECap1Regs_CTRPHS 0x0000000B 0x00000000
1001 R ECap1Regs_CAP1 0x0000000B 0x00196E80
1002 R ECap1Regs_CAP2 0x0000000B 0x00000000
1003 R ECap1Regs_CAP3 0x0000000B 0x00000000
1004 R ECap1Regs_CAP4 0x0000000B 0x00000000
1005 R ECap1Regs_ECCTL0 0x0000000B 0x0000000E
1006 R ECap1Regs_ECCTL1 0x0000000F 0x01BB
1007 R ECap1Regs_ECCTL2 0x0000000F 0x0030
1008 R ECap1Regs_ECEINT 0x0000000F 0x0002
1009 R ECap1Regs_ECFLG 0x0000000F 0x0000
1010 R ECap1Regs_ECCLR 0x0000000F 0x0000
1011 R ECap1Regs_ECFRC 0x0000000F 0x0000
1012 R ECap2Regs_TSCTR 0x0000000B 0x00000000
1013 R ECap2Regs_CTRPHS 0x0000000B 0x00000000
1014 R ECap2Regs_CAP1 0x0000000B 0x00000000
1015 R ECap2Regs_CAP2 0x0000000B 0x00000000
1016 R ECap2Regs_CAP3 0x0000000B 0x00000000
1017 R ECap2Regs_CAP4 0x0000000B 0x00000000
1018 R ECap2Regs_ECCTL0 0x0000000B 0x00000000
1019 R ECap2Regs_ECCTL1 0x0000000F 0x0000
1020 R ECap2Regs_ECCTL2 0x0000000F 0x0006
1021 R ECap2Regs_ECEINT 0x0000000F 0x0000
1022 R ECap2Regs_ECFLG 0x0000000F 0x0000
1023 R ECap2Regs_ECCLR 0x0000000F 0x0000
1024 R ECap2Regs_ECFRC 0x0000000F 0x0000
1025 R ECap3Regs_TSCTR 0x0000000B 0x00000000
1026 R ECap3Regs_CTRPHS 0x0000000B 0x00000000
1027 R ECap3Regs_CAP1 0x0000000B 0x00000000
1028 R ECap3Regs_CAP2 0x0000000B 0x00000000
1029 R ECap3Regs_CAP3 0x0000000B 0x00000000
1030 R ECap3Regs_CAP4 0x0000000B 0x00000000
1031 R ECap3Regs_ECCTL0 0x0000000B 0x00000000
1032 R ECap3Regs_ECCTL1 0x0000000F 0x0000
1033 R ECap3Regs_ECCTL2 0x0000000F 0x0006
1034 R ECap3Regs_ECEINT 0x0000000F 0x0000
1035 R ECap3Regs_ECFLG 0x0000000F 0x0000
1036 R ECap3Regs_ECCLR 0x0000000F 0x0000
1037 R ECap3Regs_ECFRC 0x0000000F 0x0000
1038 R ECap4Regs_TSCTR 0x0000000B 0x00000000
1039 R ECap4Regs_CTRPHS 0x0000000B 0x00000000
1040 R ECap4Regs_CAP1 0x0000000B 0x00000000

1041 R ECap4Regs_CAP2 0x0000000B 0x00000000
1042 R ECap4Regs_CAP3 0x0000000B 0x00000000
1043 R ECap4Regs_CAP4 0x0000000B 0x00000000
1044 R ECap4Regs_ECCTL0 0x0000000B 0x00000000
1045 R ECap4Regs_ECCTL1 0x0000000F 0x0000
1046 R ECap4Regs_ECCTL2 0x0000000F 0x0006
1047 R ECap4Regs_ECEINT 0x0000000F 0x0000
1048 R ECap4Regs_ECFLG 0x0000000F 0x0000
1049 R ECap4Regs_ECCLR 0x0000000F 0x0000
1050 R ECap4Regs_ECFRC 0x0000000F 0x0000
1051 R ECap5Regs_TSCTR 0x0000000B 0x00000000
1052 R ECap5Regs_CTRPHS 0x0000000B 0x00000000
1053 R ECap5Regs_CAP1 0x0000000B 0x00000000
1054 R ECap5Regs_CAP2 0x0000000B 0x00000000
1055 R ECap5Regs_CAP3 0x0000000B 0x00000000
1056 R ECap5Regs_CAP4 0x0000000B 0x00000000
1057 R ECap5Regs_ECCTL0 0x0000000B 0x00000000
1058 R ECap5Regs_ECCTL1 0x0000000F 0x0000
1059 R ECap5Regs_ECCTL2 0x0000000F 0x0006
1060 R ECap5Regs_ECEINT 0x0000000F 0x0000
1061 R ECap5Regs_ECFLG 0x0000000F 0x0000
1062 R ECap5Regs_ECCLR 0x0000000F 0x0000
1063 R ECap5Regs_ECFRC 0x0000000F 0x0000
1064 R ECap6Regs_TSCTR 0x0000000B 0x00000000
1065 R ECap6Regs_CTRPHS 0x0000000B 0x00000000
1066 R ECap6Regs_CAP1 0x0000000B 0x00000000
1067 R ECap6Regs_CAP2 0x0000000B 0x00000000
1068 R ECap6Regs_CAP3 0x0000000B 0x00000000
1069 R ECap6Regs_CAP4 0x0000000B 0x00000000
1070 R ECap6Regs_ECCTL0 0x0000000B 0x00000000
1071 R ECap6Regs_ECCTL1 0x0000000F 0x0000
1072 R ECap6Regs_ECCTL2 0x0000000F 0x0006
1073 R ECap6Regs_ECEINT 0x0000000F 0x0000
1074 R ECap6Regs_ECFLG 0x0000000F 0x0000
1075 R ECap6Regs_ECCLR 0x0000000F 0x0000
1076 R ECap6Regs_ECFRC 0x0000000F 0x0000
1077 R ECap7Regs_TSCTR 0x0000000B 0x00000000
1078 R ECap7Regs_CTRPHS 0x0000000B 0x00000000
1079 R ECap7Regs_CAP1 0x0000000B 0x00000000
1080 R ECap7Regs_CAP2 0x0000000B 0x00000000
1081 R ECap7Regs_CAP3 0x0000000B 0x00000000
1082 R ECap7Regs_CAP4 0x0000000B 0x00000000
1083 R ECap7Regs_ECCTL0 0x0000000B 0x00000000
1084 R ECap7Regs_ECCTL1 0x0000000F 0x0000
1085 R ECap7Regs_ECCTL2 0x0000000F 0x0006
1086 R ECap7Regs_ECEINT 0x0000000F 0x0000
1087 R ECap7Regs_ECFLG 0x0000000F 0x0000
1088 R ECap7Regs_ECCLR 0x0000000F 0x0000
1089 R ECap7Regs_ECFRC 0x0000000F 0x0000
1090 R EPwm1Regs_TBCTL 0x0000000F 0x0083
1091 R EPwm1Regs_TBCTL2 0x0000000F 0x0000
1092 R EPwm1Regs_TBCTR 0x0000000F 0x0000
1093 R EPwm1Regs_TBSTS 0x0000000F 0x0000
1094 R EPwm1Regs_CMPCTL 0x0000000F 0x0000
1095 R EPwm1Regs_CMPCTL2 0x0000000F 0x0000
1096 R EPwm1Regs_DBCTL 0x0000000F 0x0000
1097 R EPwm1Regs_DBCTL2 0x0000000F 0x0000
1098 R EPwm1Regs_AQCTL 0x0000000F 0x0000
1099 R EPwm1Regs_AQSRCSEL 0x0000000F 0x0000
1100 R EPwm1Regs_PCCTL 0x0000000F 0x0000
1101 R EPwm1Regs_HRCNFG 0x0000000F 0x0000
1102 R EPwm1Regs_HRPWR 0x0000000F 0x0000
1103 R EPwm1Regs_HRMSTEP 0x0000000F 0x0000
1104 R EPwm1Regs_HRCNFG2 0x0000000F 0x0000
1105 R EPwm1Regs_HRPCTL 0x0000000F 0x0000

1106 R EPwm1Regs_TRREM 0x0000000F 0x0000
1107 R EPwm1Regs_GLDCTL 0x0000000F 0x0000
1108 R EPwm1Regs_GLDCFG 0x0000000F 0x0000
1109 R EPwm1Regs_EPWMXLINK 0x0000000B 0x00000000
1110 R EPwm1Regs_AQCTLA 0x0000000F 0x0000
1111 R EPwm1Regs_AQCTLA2 0x0000000F 0x0000
1112 R EPwm1Regs_AQCTLB 0x0000000F 0x0000
1113 R EPwm1Regs_AQCTLB2 0x0000000F 0x0000
1114 R EPwm1Regs_AQSFRC 0x0000000F 0x0000
1115 R EPwm1Regs_AQCSFRC 0x0000000F 0x0000
1116 R EPwm1Regs_DBREDHR 0x0000000F 0x0000
1117 R EPwm1Regs_DBRED 0x0000000F 0x0000
1118 R EPwm1Regs_DBFEDHR 0x0000000F 0x0000
1119 R EPwm1Regs_DBFED 0x0000000F 0x0000
1120 R EPwm1Regs_TBPHS 0x0000000B 0x00000000
1121 R EPwm1Regs_TBPRDHR 0x0000000F 0x0000
1122 R EPwm1Regs_TBPRD 0x0000000F 0x0000
1123 R EPwm1Regs_CMPA 0x0000000B 0x00000000
1124 R EPwm1Regs_CMPB 0x0000000B 0x00000000
1125 R EPwm1Regs_CMPC 0x0000000F 0x0000
1126 R EPwm1Regs_CMPD 0x0000000F 0x0000
1127 R EPwm1Regs_GLDCTL2 0x0000000F 0x0000
1128 R EPwm1Regs_TZSEL 0x0000000F 0x0000
1129 R EPwm1Regs_TZDCSEL 0x0000000F 0x0000
1130 R EPwm1Regs_TZCTL 0x0000000F 0x0000
1131 R EPwm1Regs_TZCTL2 0x0000000F 0x0000
1132 R EPwm1Regs_TZCTLDCA 0x0000000F 0x0000
1133 R EPwm1Regs_TZCTLDCB 0x0000000F 0x0000
1134 R EPwm1Regs_TZEINT 0x0000000F 0x0000
1135 R EPwm1Regs_TZFLG 0x0000000F 0x0000
1136 R EPwm1Regs_TZCBCFLG 0x0000000F 0x0000
1137 R EPwm1Regs_TZOSTFLG 0x0000000F 0x0000
1138 R EPwm1Regs_TZCLR 0x0000000F 0x0000
1139 R EPwm1Regs_TZCBCCLR 0x0000000F 0x0000
1140 R EPwm1Regs_TZOSTCLR 0x0000000F 0x0000
1141 R EPwm1Regs_TZFRC 0x0000000F 0x0000
1142 R EPwm1Regs_ETSEL 0x0000000F 0x0000
1143 R EPwm1Regs_ETPS 0x0000000F 0x0000
1144 R EPwm1Regs_ETFLG 0x0000000F 0x0000
1145 R EPwm1Regs_ETCLR 0x0000000F 0x0000
1146 R EPwm1Regs_ETFRC 0x0000000F 0x0000
1147 R EPwm1Regs_ETINTPS 0x0000000F 0x0000
1148 R EPwm1Regs_ETSOCPS 0x0000000F 0x0000
1149 R EPwm1Regs_ETCNTINITCTL 0x0000000F 0x0000
1150 R EPwm1Regs_ETCNTINIT 0x0000000F 0x0000
1151 R EPwm1Regs_DCTRIPSEL 0x0000000F 0x0000
1152 R EPwm1Regs_DCACTL 0x0000000F 0x0000
1153 R EPwm1Regs_DCBCTL 0x0000000F 0x0000
1154 R EPwm1Regs_DCFCTL 0x0000000F 0x0000
1155 R EPwm1Regs_DCCAPCTL 0x0000000F 0x0000
1156 R EPwm1Regs_DCFOFFSET 0x0000000F 0x0000
1157 R EPwm1Regs_DCFOFFSETCNT 0x0000000F 0x0000
1158 R EPwm1Regs_DCFWINDOW 0x0000000F 0x0000
1159 R EPwm1Regs_DCFWINDOWCNT 0x0000000F 0x0000
1160 R EPwm1Regs_DCCAP 0x0000000F 0x0000
1161 R EPwm1Regs_DCAHTRIPSEL 0x0000000F 0x0000
1162 R EPwm1Regs_DCALTRIPSEL 0x0000000F 0x0000
1163 R EPwm1Regs_DCBHTRIPSEL 0x0000000F 0x0000
1164 R EPwm1Regs_DCBLTRIPSEL 0x0000000F 0x0000
1165 R EPwm1Regs_EPWMLOCK 0x0000000B 0x00000000
1166 R EPwm2Regs_TBCTL 0x0000000F 0x0032
1167 R EPwm2Regs_TBCTL2 0x0000000F 0x0000
1168 R EPwm2Regs_TBCTR 0x0000000F 0x011A
1169 R EPwm2Regs_TBSTS 0x0000000F 0x0000
1170 R EPwm2Regs_CMPCTL 0x0000000F 0x0000

1171 R EPwm2Regs_CMPCTL2 0x0000000F 0x0000
1172 R EPwm2Regs_DBCTL 0x0000000F 0x0000
1173 R EPwm2Regs_DBCTL2 0x0000000F 0x0000
1174 R EPwm2Regs_AQCTL 0x0000000F 0x0000
1175 R EPwm2Regs_AQTSRCSEL 0x0000000F 0x0000
1176 R EPwm2Regs_PCCTL 0x0000000F 0x0000
1177 R EPwm2Regs_HRCNFG 0x0000000F 0x0000
1178 R EPwm2Regs_HRPWR 0x0000000F 0x0000
1179 R EPwm2Regs_HRMSTEP 0x0000000F 0x0000
1180 R EPwm2Regs_HRCNFG2 0x0000000F 0x0000
1181 R EPwm2Regs_HRPCTL 0x0000000F 0x0000
1182 R EPwm2Regs_TRREM 0x0000000F 0x0000
1183 R EPwm2Regs_GLDCTL 0x0000000F 0x0000
1184 R EPwm2Regs_GLDCFG 0x0000000F 0x0000
1185 R EPwm2Regs_EPWMXLINK 0x0000000B 0x10011111
1186 R EPwm2Regs_AQCTLA 0x0000000F 0x0090
1187 R EPwm2Regs_AQCTLA2 0x0000000F 0x0000
1188 R EPwm2Regs_AQCTLB 0x0000000F 0x0900
1189 R EPwm2Regs_AQCTLB2 0x0000000F 0x0000
1190 R EPwm2Regs_AQSFRC 0x0000000F 0x0000
1191 R EPwm2Regs_AQCSFRC 0x0000000F 0x0005
1192 R EPwm2Regs_DBREDHR 0x0000000F 0x0000
1193 R EPwm2Regs_DBRED 0x0000000F 0x0000
1194 R EPwm2Regs_DBFEDHR 0x0000000F 0x0000
1195 R EPwm2Regs_DBFED 0x0000000F 0x0000
1196 R EPwm2Regs_TBPHS 0x0000000B 0x00000000
1197 R EPwm2Regs_TBPRDHR 0x0000000F 0x0000
1198 R EPwm2Regs_TBPRD 0x0000000F 0x01F4
1199 R EPwm2Regs_CMPA 0x0000000B 0x00000000
1200 R EPwm2Regs_CMPB 0x0000000B 0x01F40000
1201 R EPwm2Regs_CMPC 0x0000000F 0x0000
1202 R EPwm2Regs_CMPD 0x0000000F 0x0000
1203 R EPwm2Regs_GLDCTL2 0x0000000F 0x0000
1204 R EPwm2Regs_TZSEL 0x0000000F 0x0000
1205 R EPwm2Regs_TZDCSEL 0x0000000F 0x0000
1206 R EPwm2Regs_TZCTL 0x0000000F 0x0000
1207 R EPwm2Regs_TZCTL2 0x0000000F 0x0000
1208 R EPwm2Regs_TZCTLDCA 0x0000000F 0x0000
1209 R EPwm2Regs_TZCTLDCB 0x0000000F 0x0000
1210 R EPwm2Regs_TZEINT 0x0000000F 0x0000
1211 R EPwm2Regs_TZFLG 0x0000000F 0x0000
1212 R EPwm2Regs_TZCBCFLG 0x0000000F 0x0000
1213 R EPwm2Regs_TZOSTFLG 0x0000000F 0x0000
1214 R EPwm2Regs_TZCLR 0x0000000F 0x0000
1215 R EPwm2Regs_TZCBCCLR 0x0000000F 0x0000
1216 R EPwm2Regs_TZOSTCLR 0x0000000F 0x0000
1217 R EPwm2Regs_TZFRC 0x0000000F 0x0000
1218 R EPwm2Regs_ETSEL 0x0000000F 0x0A09
1219 R EPwm2Regs_ETPS 0x0000000F 0x0131
1220 R EPwm2Regs_ETFLG 0x0000000F 0x0004
1221 R EPwm2Regs_ETCLR 0x0000000F 0x0000
1222 R EPwm2Regs_ETFRC 0x0000000F 0x0000
1223 R EPwm2Regs_ETINTPS 0x0000000F 0x0001
1224 R EPwm2Regs_ETSOCPS 0x0000000F 0x0001
1225 R EPwm2Regs_ETCNTINITCTL 0x0000000F 0x0000
1226 R EPwm2Regs_ETCNTINIT 0x0000000F 0x0000
1227 R EPwm2Regs_DCTRIPSEL 0x0000000F 0x0000
1228 R EPwm2Regs_DCACTL 0x0000000F 0x0000
1229 R EPwm2Regs_DCBCTL 0x0000000F 0x0000
1230 R EPwm2Regs_DCFCTL 0x0000000F 0x0000
1231 R EPwm2Regs_DCCAPCTL 0x0000000F 0x0000
1232 R EPwm2Regs_DCFOFFSET 0x0000000F 0x0000
1233 R EPwm2Regs_DCFOFFSETCNT 0x0000000F 0x0000
1234 R EPwm2Regs_DCFWINDOW 0x0000000F 0x0000
1235 R EPwm2Regs_DCFWINDOWCNT 0x0000000F 0x0000

1236 R EPwm2Regs_DCCAP 0x0000000F 0x0000
1237 R EPwm2Regs_DCAHTRIPSEL 0x0000000F 0x0000
1238 R EPwm2Regs_DCALTRIPSEL 0x0000000F 0x0000
1239 R EPwm2Regs_DCBHTRIPSEL 0x0000000F 0x0000
1240 R EPwm2Regs_DCBLTRIPSEL 0x0000000F 0x0000
1241 R EPwm2Regs_EPWMLOCK 0x0000000B 0x00000000
1242 R EPwm3Regs_TBCTL 0x0000000F 0x0083
1243 R EPwm3Regs_TBCTL2 0x0000000F 0x0000
1244 R EPwm3Regs_TBCTR 0x0000000F 0x0000
1245 R EPwm3Regs_TBSTS 0x0000000F 0x0000
1246 R EPwm3Regs_CMPCTL 0x0000000F 0x0000
1247 R EPwm3Regs_CMPCTL2 0x0000000F 0x0000
1248 R EPwm3Regs_DBCTL 0x0000000F 0x0000
1249 R EPwm3Regs_DBCTL2 0x0000000F 0x0000
1250 R EPwm3Regs_AQCTL 0x0000000F 0x0000
1251 R EPwm3Regs_AQTSRCSEL 0x0000000F 0x0000
1252 R EPwm3Regs_PCCTL 0x0000000F 0x0000
1253 R EPwm3Regs_HRCNFG 0x0000000F 0x0000
1254 R EPwm3Regs_HRPWR 0x0000000F 0x0000
1255 R EPwm3Regs_HRMSTEP 0x0000000F 0x0000
1256 R EPwm3Regs_HRCNFG2 0x0000000F 0x0000
1257 R EPwm3Regs_HRPCTL 0x0000000F 0x0000
1258 R EPwm3Regs_TRREM 0x0000000F 0x0000
1259 R EPwm3Regs_GLDCTL 0x0000000F 0x0000
1260 R EPwm3Regs_GLDCFG 0x0000000F 0x0000
1261 R EPwm3Regs_EPWMXLINK 0x0000000B 0x20022222
1262 R EPwm3Regs_AQCTLA 0x0000000F 0x0000
1263 R EPwm3Regs_AQCTLA2 0x0000000F 0x0000
1264 R EPwm3Regs_AQCTLB 0x0000000F 0x0000
1265 R EPwm3Regs_AQCTLB2 0x0000000F 0x0000
1266 R EPwm3Regs_AQSFRC 0x0000000F 0x0000
1267 R EPwm3Regs_AQCSFRC 0x0000000F 0x0000
1268 R EPwm3Regs_DBREDHR 0x0000000F 0x0000
1269 R EPwm3Regs_DBRED 0x0000000F 0x0000
1270 R EPwm3Regs_DBFEDHR 0x0000000F 0x0000
1271 R EPwm3Regs_DBFED 0x0000000F 0x0000
1272 R EPwm3Regs_TBPHS 0x0000000B 0x00000000
1273 R EPwm3Regs_TBPRDHR 0x0000000F 0x0000
1274 R EPwm3Regs_TBPRD 0x0000000F 0x0000
1275 R EPwm3Regs_CMPA 0x0000000B 0x00000000
1276 R EPwm3Regs_CMPB 0x0000000B 0x00000000
1277 R EPwm3Regs_CMPC 0x0000000F 0x0000
1278 R EPwm3Regs_CMPD 0x0000000F 0x0000
1279 R EPwm3Regs_GLDCTL2 0x0000000F 0x0000
1280 R EPwm3Regs_TZSEL 0x0000000F 0x0000
1281 R EPwm3Regs_TZDCSEL 0x0000000F 0x0000
1282 R EPwm3Regs_TZCTL 0x0000000F 0x0000
1283 R EPwm3Regs_TZCTL2 0x0000000F 0x0000
1284 R EPwm3Regs_TZCTLDCA 0x0000000F 0x0000
1285 R EPwm3Regs_TZCTLDCB 0x0000000F 0x0000
1286 R EPwm3Regs_TZEINT 0x0000000F 0x0000
1287 R EPwm3Regs_TZFLG 0x0000000F 0x0000
1288 R EPwm3Regs_TZCBCFLG 0x0000000F 0x0000
1289 R EPwm3Regs_TZOSTFLG 0x0000000F 0x0000
1290 R EPwm3Regs_TZCLR 0x0000000F 0x0000
1291 R EPwm3Regs_TZCBCCLR 0x0000000F 0x0000
1292 R EPwm3Regs_TZOSTCLR 0x0000000F 0x0000
1293 R EPwm3Regs_TZFRC 0x0000000F 0x0000
1294 R EPwm3Regs_ETSEL 0x0000000F 0x0000
1295 R EPwm3Regs_ETPS 0x0000000F 0x0000
1296 R EPwm3Regs_ETFLG 0x0000000F 0x0000
1297 R EPwm3Regs_ETCLR 0x0000000F 0x0000
1298 R EPwm3Regs_ETFRC 0x0000000F 0x0000
1299 R EPwm3Regs_ETINTPS 0x0000000F 0x0000
1300 R EPwm3Regs_ETSOCPS 0x0000000F 0x0000

1301 R EPwm3Regs_ETCNTINITCTL 0x0000000F 0x0000
1302 R EPwm3Regs_ETCNTINIT 0x0000000F 0x0000
1303 R EPwm3Regs_DCTRIPSEL 0x0000000F 0x0000
1304 R EPwm3Regs_DCACTL 0x0000000F 0x0000
1305 R EPwm3Regs_DCBCTL 0x0000000F 0x0000
1306 R EPwm3Regs_DCFCTL 0x0000000F 0x0000
1307 R EPwm3Regs_DCCAPCTL 0x0000000F 0x0000
1308 R EPwm3Regs_DCOFFSET 0x0000000F 0x0000
1309 R EPwm3Regs_DCOFFSETCNT 0x0000000F 0x0000
1310 R EPwm3Regs_DCFWINDOW 0x0000000F 0x0000
1311 R EPwm3Regs_DCFWINDOWCNT 0x0000000F 0x0000
1312 R EPwm3Regs_DCCAP 0x0000000F 0x0000
1313 R EPwm3Regs_DCAHTRIPSEL 0x0000000F 0x0000
1314 R EPwm3Regs_DCALTRIPSEL 0x0000000F 0x0000
1315 R EPwm3Regs_DCBHTRIPSEL 0x0000000F 0x0000
1316 R EPwm3Regs_DCBLTRIPSEL 0x0000000F 0x0000
1317 R EPwm3Regs_EPWMLOCK 0x0000000B 0x00000000
1318 R EPwm4Regs_TBCTL 0x0000000F 0x0083
1319 R EPwm4Regs_TBCTL2 0x0000000F 0x0000
1320 R EPwm4Regs_TBCTR 0x0000000F 0x0000
1321 R EPwm4Regs_TBSTS 0x0000000F 0x0000
1322 R EPwm4Regs_CMPCTL 0x0000000F 0x0000
1323 R EPwm4Regs_CMPCTL2 0x0000000F 0x0000
1324 R EPwm4Regs_DBCTL 0x0000000F 0x0000
1325 R EPwm4Regs_DBCTL2 0x0000000F 0x0000
1326 R EPwm4Regs_AQCTL 0x0000000F 0x0000
1327 R EPwm4Regs_AQTSRCSEL 0x0000000F 0x0000
1328 R EPwm4Regs_PCCTL 0x0000000F 0x0000
1329 R EPwm4Regs_HRCNFG 0x0000000F 0x0000
1330 R EPwm4Regs_HRPWR 0x0000000F 0x0000
1331 R EPwm4Regs_HRMSTEP 0x0000000F 0x0000
1332 R EPwm4Regs_HRCNFG2 0x0000000F 0x0000
1333 R EPwm4Regs_HRPCTL 0x0000000F 0x0000
1334 R EPwm4Regs_TRREM 0x0000000F 0x0000
1335 R EPwm4Regs_GLDCTL 0x0000000F 0x0000
1336 R EPwm4Regs_GLDCFG 0x0000000F 0x0000
1337 R EPwm4Regs_EPWMXLINK 0x0000000B 0x30033333
1338 R EPwm4Regs_AQCTLA 0x0000000F 0x0000
1339 R EPwm4Regs_AQCTLA2 0x0000000F 0x0000
1340 R EPwm4Regs_AQCTLB 0x0000000F 0x0000
1341 R EPwm4Regs_AQCTLB2 0x0000000F 0x0000
1342 R EPwm4Regs_AQSFRC 0x0000000F 0x0000
1343 R EPwm4Regs_AQCSFRC 0x0000000F 0x0000
1344 R EPwm4Regs_DBREDHR 0x0000000F 0x0000
1345 R EPwm4Regs_DBRED 0x0000000F 0x0000
1346 R EPwm4Regs_DBFEDHR 0x0000000F 0x0000
1347 R EPwm4Regs_DBFED 0x0000000F 0x0000
1348 R EPwm4Regs_TBPHS 0x0000000B 0x00000000
1349 R EPwm4Regs_TBPRDHR 0x0000000F 0x0000
1350 R EPwm4Regs_TBPRD 0x0000000F 0x0000
1351 R EPwm4Regs_CMPA 0x0000000B 0x00000000
1352 R EPwm4Regs_CMPB 0x0000000B 0x00000000
1353 R EPwm4Regs_CMPC 0x0000000F 0x0000
1354 R EPwm4Regs_CMPD 0x0000000F 0x0000
1355 R EPwm4Regs_GLDCTL2 0x0000000F 0x0000
1356 R EPwm4Regs_TZSEL 0x0000000F 0x0000
1357 R EPwm4Regs_TZDCSEL 0x0000000F 0x0000
1358 R EPwm4Regs_TZCTL 0x0000000F 0x0000
1359 R EPwm4Regs_TZCTL2 0x0000000F 0x0000
1360 R EPwm4Regs_TZCTLDCA 0x0000000F 0x0000
1361 R EPwm4Regs_TZCTLDCB 0x0000000F 0x0000
1362 R EPwm4Regs_TZEINT 0x0000000F 0x0000
1363 R EPwm4Regs_TZFLG 0x0000000F 0x0000
1364 R EPwm4Regs_TZCBCFLG 0x0000000F 0x0000
1365 R EPwm4Regs_TZOSTFLG 0x0000000F 0x0000

1366 R EPwm4Regs_TZCLR 0x0000000F 0x0000
1367 R EPwm4Regs_TZCBCCLR 0x0000000F 0x0000
1368 R EPwm4Regs_TZOSTCLR 0x0000000F 0x0000
1369 R EPwm4Regs_TZFRC 0x0000000F 0x0000
1370 R EPwm4Regs_ETSEL 0x0000000F 0x0000
1371 R EPwm4Regs_ETPS 0x0000000F 0x0000
1372 R EPwm4Regs_ETFLG 0x0000000F 0x0000
1373 R EPwm4Regs_ETCLR 0x0000000F 0x0000
1374 R EPwm4Regs_ETFRC 0x0000000F 0x0000
1375 R EPwm4Regs_ETINTPS 0x0000000F 0x0000
1376 R EPwm4Regs_ETSOCPS 0x0000000F 0x0000
1377 R EPwm4Regs_ETCNTINITCTL 0x0000000F 0x0000
1378 R EPwm4Regs_ETCNTINIT 0x0000000F 0x0000
1379 R EPwm4Regs_DCTRIPSEL 0x0000000F 0x0000
1380 R EPwm4Regs_DCACTL 0x0000000F 0x0000
1381 R EPwm4Regs_DCBCTL 0x0000000F 0x0000
1382 R EPwm4Regs_DCFCTL 0x0000000F 0x0000
1383 R EPwm4Regs_DCCAPCTL 0x0000000F 0x0000
1384 R EPwm4Regs_DCFOFFSET 0x0000000F 0x0000
1385 R EPwm4Regs_DCFOFFSETCNT 0x0000000F 0x0000
1386 R EPwm4Regs_DCFWINDOW 0x0000000F 0x0000
1387 R EPwm4Regs_DCFWINDOWCNT 0x0000000F 0x0000
1388 R EPwm4Regs_DCCAP 0x0000000F 0x0000
1389 R EPwm4Regs_DCAHTRIPSEL 0x0000000F 0x0000
1390 R EPwm4Regs_DCALTRIPSEL 0x0000000F 0x0000
1391 R EPwm4Regs_DCBHTRIPSEL 0x0000000F 0x0000
1392 R EPwm4Regs_DCBLTRIPSEL 0x0000000F 0x0000
1393 R EPwm4Regs_EPWMLOCK 0x0000000B 0x00000000
1394 R EPwm5Regs_TBCTL 0x0000000F 0x0083
1395 R EPwm5Regs_TBCTL2 0x0000000F 0x0000
1396 R EPwm5Regs_TBCTR 0x0000000F 0x0000
1397 R EPwm5Regs_TBSTS 0x0000000F 0x0000
1398 R EPwm5Regs_CMPCTL 0x0000000F 0x0000
1399 R EPwm5Regs_CMPCTL2 0x0000000F 0x0000
1400 R EPwm5Regs_DBCTL 0x0000000F 0x0000
1401 R EPwm5Regs_DBCTL2 0x0000000F 0x0000
1402 R EPwm5Regs_AQCTL 0x0000000F 0x0000
1403 R EPwm5Regs_AQTSRCSEL 0x0000000F 0x0000
1404 R EPwm5Regs_PCCTL 0x0000000F 0x0000
1405 R EPwm5Regs_HRCNFG 0x0000000F 0x0000
1406 R EPwm5Regs_HRPWR 0x0000000F 0x0000
1407 R EPwm5Regs_HRMSTEP 0x0000000F 0x0000
1408 R EPwm5Regs_HRCNFG2 0x0000000F 0x0000
1409 R EPwm5Regs_HRPCTL 0x0000000F 0x0000
1410 R EPwm5Regs_TRREM 0x0000000F 0x0000
1411 R EPwm5Regs_GLDCTL 0x0000000F 0x0000
1412 R EPwm5Regs_GLDCFG 0x0000000F 0x0000
1413 R EPwm5Regs_EPWMXLINK 0x0000000B 0x40044444
1414 R EPwm5Regs_AQCTLA 0x0000000F 0x0000
1415 R EPwm5Regs_AQCTLA2 0x0000000F 0x0000
1416 R EPwm5Regs_AQCTLB 0x0000000F 0x0000
1417 R EPwm5Regs_AQCTLB2 0x0000000F 0x0000
1418 R EPwm5Regs_AQSFRC 0x0000000F 0x0000
1419 R EPwm5Regs_AQCSFRC 0x0000000F 0x0000
1420 R EPwm5Regs_DBREDHR 0x0000000F 0x0000
1421 R EPwm5Regs_DBRED 0x0000000F 0x0000
1422 R EPwm5Regs_DBFEDHR 0x0000000F 0x0000
1423 R EPwm5Regs_DBFED 0x0000000F 0x0000
1424 R EPwm5Regs_TBPHS 0x0000000B 0x00000000
1425 R EPwm5Regs_TBPRDHR 0x0000000F 0x0000
1426 R EPwm5Regs_TBPRD 0x0000000F 0x0000
1427 R EPwm5Regs_CMPA 0x0000000B 0x00000000
1428 R EPwm5Regs_CMPB 0x0000000B 0x00000000
1429 R EPwm5Regs_CMPC 0x0000000F 0x0000
1430 R EPwm5Regs_CMPD 0x0000000F 0x0000

1431 R EPwm5Regs_GLDCTL2 0x0000000F 0x0000
1432 R EPwm5Regs_TZSEL 0x0000000F 0x0000
1433 R EPwm5Regs_TZDCSEL 0x0000000F 0x0000
1434 R EPwm5Regs_TZCTL 0x0000000F 0x0000
1435 R EPwm5Regs_TZCTL2 0x0000000F 0x0000
1436 R EPwm5Regs_TZCTLDCA 0x0000000F 0x0000
1437 R EPwm5Regs_TZCTLDCB 0x0000000F 0x0000
1438 R EPwm5Regs_TZEINT 0x0000000F 0x0000
1439 R EPwm5Regs_TZFLG 0x0000000F 0x0000
1440 R EPwm5Regs_TZCBCFLG 0x0000000F 0x0000
1441 R EPwm5Regs_TZOSTFLG 0x0000000F 0x0000
1442 R EPwm5Regs_TZCLR 0x0000000F 0x0000
1443 R EPwm5Regs_TZCBCCLR 0x0000000F 0x0000
1444 R EPwm5Regs_TZOSTCLR 0x0000000F 0x0000
1445 R EPwm5Regs_TZFRC 0x0000000F 0x0000
1446 R EPwm5Regs_ETSEL 0x0000000F 0x0000
1447 R EPwm5Regs_ETPS 0x0000000F 0x0000
1448 R EPwm5Regs_ETFLG 0x0000000F 0x0000
1449 R EPwm5Regs_ETCLR 0x0000000F 0x0000
1450 R EPwm5Regs_ETFRC 0x0000000F 0x0000
1451 R EPwm5Regs_ETINTPS 0x0000000F 0x0000
1452 R EPwm5Regs_ETSOCPS 0x0000000F 0x0000
1453 R EPwm5Regs_ETCNTINITCTL 0x0000000F 0x0000
1454 R EPwm5Regs_ETCNTINIT 0x0000000F 0x0000
1455 R EPwm5Regs_DCTRIPSEL 0x0000000F 0x0000
1456 R EPwm5Regs_DCACTL 0x0000000F 0x0000
1457 R EPwm5Regs_DCBCTL 0x0000000F 0x0000
1458 R EPwm5Regs_DCFCTL 0x0000000F 0x0000
1459 R EPwm5Regs_DCCAPCTL 0x0000000F 0x0000
1460 R EPwm5Regs_DCFOFFSET 0x0000000F 0x0000
1461 R EPwm5Regs_DCFOFFSETCNT 0x0000000F 0x0000
1462 R EPwm5Regs_DCFWINDOW 0x0000000F 0x0000
1463 R EPwm5Regs_DCFWINDOWCNT 0x0000000F 0x0000
1464 R EPwm5Regs_DCCAP 0x0000000F 0x0000
1465 R EPwm5Regs_DCAHTRIPSEL 0x0000000F 0x0000
1466 R EPwm5Regs_DCALTRIPSEL 0x0000000F 0x0000
1467 R EPwm5Regs_DCBHTRIPSEL 0x0000000F 0x0000
1468 R EPwm5Regs_DCBLTRIPSEL 0x0000000F 0x0000
1469 R EPwm5Regs_EPWMLOCK 0x0000000B 0x00000000
1470 R EPwm6Regs_TBCTL 0x0000000F 0x0083
1471 R EPwm6Regs_TBCTL2 0x0000000F 0x0000
1472 R EPwm6Regs_TBCTR 0x0000000F 0x0000
1473 R EPwm6Regs_TBSTS 0x0000000F 0x0000
1474 R EPwm6Regs_CMPCTL 0x0000000F 0x0000
1475 R EPwm6Regs_CMPCTL2 0x0000000F 0x0000
1476 R EPwm6Regs_DBCTL 0x0000000F 0x0000
1477 R EPwm6Regs_DBCTL2 0x0000000F 0x0000
1478 R EPwm6Regs_AQCTL 0x0000000F 0x0000
1479 R EPwm6Regs_AQSRCSEL 0x0000000F 0x0000
1480 R EPwm6Regs_PCCTL 0x0000000F 0x0000
1481 R EPwm6Regs_HRCNFG 0x0000000F 0x0000
1482 R EPwm6Regs_HRPWR 0x0000000F 0x0000
1483 R EPwm6Regs_HRMSTEP 0x0000000F 0x0000
1484 R EPwm6Regs_HRCNFG2 0x0000000F 0x0000
1485 R EPwm6Regs_HRPCTL 0x0000000F 0x0000
1486 R EPwm6Regs_TRREM 0x0000000F 0x0000
1487 R EPwm6Regs_GLDCTL 0x0000000F 0x0000
1488 R EPwm6Regs_GLDCFG 0x0000000F 0x0000
1489 R EPwm6Regs_EPWMXLINK 0x0000000B 0x50055555
1490 R EPwm6Regs_AQCTLA 0x0000000F 0x0000
1491 R EPwm6Regs_AQCTLA2 0x0000000F 0x0000
1492 R EPwm6Regs_AQCTLB 0x0000000F 0x0000
1493 R EPwm6Regs_AQCTLB2 0x0000000F 0x0000
1494 R EPwm6Regs_AQSFRC 0x0000000F 0x0000
1495 R EPwm6Regs_AQCSFRC 0x0000000F 0x0000

1496 R EPwm6Regs_DBREDHR 0x0000000F 0x0000
1497 R EPwm6Regs_DBRED 0x0000000F 0x0000
1498 R EPwm6Regs_DBFEDHR 0x0000000F 0x0000
1499 R EPwm6Regs_DBFED 0x0000000F 0x0000
1500 R EPwm6Regs_TBPHS 0x0000000B 0x00000000
1501 R EPwm6Regs_TBPRDHR 0x0000000F 0x0000
1502 R EPwm6Regs_TBPRD 0x0000000F 0x0000
1503 R EPwm6Regs_CMPA 0x0000000B 0x00000000
1504 R EPwm6Regs_CMPB 0x0000000B 0x00000000
1505 R EPwm6Regs_CMPC 0x0000000F 0x0000
1506 R EPwm6Regs_CMPD 0x0000000F 0x0000
1507 R EPwm6Regs_GLDCTL2 0x0000000F 0x0000
1508 R EPwm6Regs_TZSEL 0x0000000F 0x0000
1509 R EPwm6Regs_TZDCSEL 0x0000000F 0x0000
1510 R EPwm6Regs_TZCTL 0x0000000F 0x0000
1511 R EPwm6Regs_TZCTL2 0x0000000F 0x0000
1512 R EPwm6Regs_TZCTLDCA 0x0000000F 0x0000
1513 R EPwm6Regs_TZCTLDCB 0x0000000F 0x0000
1514 R EPwm6Regs_TZEINT 0x0000000F 0x0000
1515 R EPwm6Regs_TZFLG 0x0000000F 0x0000
1516 R EPwm6Regs_TZCBCFLG 0x0000000F 0x0000
1517 R EPwm6Regs_TZOSTFLG 0x0000000F 0x0000
1518 R EPwm6Regs_TZCLR 0x0000000F 0x0000
1519 R EPwm6Regs_TZCBCCLR 0x0000000F 0x0000
1520 R EPwm6Regs_TZOSTCLR 0x0000000F 0x0000
1521 R EPwm6Regs_TZFRC 0x0000000F 0x0000
1522 R EPwm6Regs_ETSEL 0x0000000F 0x0000
1523 R EPwm6Regs_ETPS 0x0000000F 0x0000
1524 R EPwm6Regs_ETFLG 0x0000000F 0x0000
1525 R EPwm6Regs_ETCLR 0x0000000F 0x0000
1526 R EPwm6Regs_ETFRC 0x0000000F 0x0000
1527 R EPwm6Regs_ETINTPS 0x0000000F 0x0000
1528 R EPwm6Regs_ETSOCPS 0x0000000F 0x0000
1529 R EPwm6Regs_ETCNTINITCTL 0x0000000F 0x0000
1530 R EPwm6Regs_ETCNTINIT 0x0000000F 0x0000
1531 R EPwm6Regs_DCTRIPSEL 0x0000000F 0x0000
1532 R EPwm6Regs_DCACTL 0x0000000F 0x0000
1533 R EPwm6Regs_DCBCTL 0x0000000F 0x0000
1534 R EPwm6Regs_DCFCTL 0x0000000F 0x0000
1535 R EPwm6Regs_DCCAPCTL 0x0000000F 0x0000
1536 R EPwm6Regs_DCFOFFSET 0x0000000F 0x0000
1537 R EPwm6Regs_DCFOFFSETCNT 0x0000000F 0x0000
1538 R EPwm6Regs_DCFWINDOW 0x0000000F 0x0000
1539 R EPwm6Regs_DCFWINDOWCNT 0x0000000F 0x0000
1540 R EPwm6Regs_DCCAP 0x0000000F 0x0000
1541 R EPwm6Regs_DCAHTRIPSEL 0x0000000F 0x0000
1542 R EPwm6Regs_DCALTRIPSEL 0x0000000F 0x0000
1543 R EPwm6Regs_DCBHTRIPSEL 0x0000000F 0x0000
1544 R EPwm6Regs_DCBLTRIPSEL 0x0000000F 0x0000
1545 R EPwm6Regs_EPWMLOCK 0x0000000B 0x00000000
1546 R EPwm7Regs_TBCTL 0x0000000F 0x0002
1547 R EPwm7Regs_TBCTL2 0x0000000F 0x0000
1548 R EPwm7Regs_TBCTR 0x0000000F 0x014C
1549 R EPwm7Regs_TBSTS 0x0000000F 0x0000
1550 R EPwm7Regs_CMPCTL 0x0000000F 0x0000
1551 R EPwm7Regs_CMPCTL2 0x0000000F 0x0000
1552 R EPwm7Regs_DBCTL 0x0000000F 0x0000
1553 R EPwm7Regs_DBCTL2 0x0000000F 0x0000
1554 R EPwm7Regs_AQCTL 0x0000000F 0x0000
1555 R EPwm7Regs_AQTSRCSEL 0x0000000F 0x0000
1556 R EPwm7Regs_PCCTL 0x0000000F 0x0000
1557 R EPwm7Regs_HRCNFG 0x0000000F 0x0000
1558 R EPwm7Regs_HRPWR 0x0000000F 0x0000
1559 R EPwm7Regs_HRMSTEP 0x0000000F 0x0000
1560 R EPwm7Regs_HRCNFG2 0x0000000F 0x0000

1561 R EPwm7Regs_HRPCTL 0x0000000F 0x0000
1562 R EPwm7Regs_TRREM 0x0000000F 0x0000
1563 R EPwm7Regs_GLDCTL 0x0000000F 0x0000
1564 R EPwm7Regs_GLDCFG 0x0000000F 0x0000
1565 R EPwm7Regs_EPWMXLINK 0x0000000B 0x60066666
1566 R EPwm7Regs_AQCTLA 0x0000000F 0x0090
1567 R EPwm7Regs_AQCTLA2 0x0000000F 0x0000
1568 R EPwm7Regs_AQCTLB 0x0000000F 0x0060
1569 R EPwm7Regs_AQCTLB2 0x0000000F 0x0000
1570 R EPwm7Regs_AQSFRC 0x0000000F 0x0000
1571 R EPwm7Regs_AQCSFRC 0x0000000F 0x0005
1572 R EPwm7Regs_DBREDHR 0x0000000F 0x0000
1573 R EPwm7Regs_DBRED 0x0000000F 0x0000
1574 R EPwm7Regs_DBFEDHR 0x0000000F 0x0000
1575 R EPwm7Regs_DBFED 0x0000000F 0x0000
1576 R EPwm7Regs_TBPHS 0x0000000B 0x00000000
1577 R EPwm7Regs_TBPRDHR 0x0000000F 0x0000
1578 R EPwm7Regs_TBPRD 0x0000000F 0x03E8
1579 R EPwm7Regs_CMPA 0x0000000B 0x00000000
1580 R EPwm7Regs_CMPB 0x0000000B 0x00000000
1581 R EPwm7Regs_CMPC 0x0000000F 0x0000
1582 R EPwm7Regs_CMPD 0x0000000F 0x0000
1583 R EPwm7Regs_GLDCTL2 0x0000000F 0x0000
1584 R EPwm7Regs_TZSEL 0x0000000F 0x0000
1585 R EPwm7Regs_TZDCSEL 0x0000000F 0x0000
1586 R EPwm7Regs_TZCTL 0x0000000F 0x0000
1587 R EPwm7Regs_TZCTL2 0x0000000F 0x0000
1588 R EPwm7Regs_TZCTLDCA 0x0000000F 0x0000
1589 R EPwm7Regs_TZCTLDCB 0x0000000F 0x0000
1590 R EPwm7Regs_TZEINT 0x0000000F 0x0000
1591 R EPwm7Regs_TZFLG 0x0000000F 0x0000
1592 R EPwm7Regs_TZCBCFLG 0x0000000F 0x0000
1593 R EPwm7Regs_TZOSTFLG 0x0000000F 0x0000
1594 R EPwm7Regs_TZCLR 0x0000000F 0x0000
1595 R EPwm7Regs_TZCBCCLR 0x0000000F 0x0000
1596 R EPwm7Regs_TZOSTCLR 0x0000000F 0x0000
1597 R EPwm7Regs_TZFRC 0x0000000F 0x0000
1598 R EPwm7Regs_ETSEL 0x0000000F 0x0909
1599 R EPwm7Regs_ETPS 0x0000000F 0x0034
1600 R EPwm7Regs_ETFLG 0x0000000F 0x0005
1601 R EPwm7Regs_ETCLR 0x0000000F 0x0000
1602 R EPwm7Regs_ETFRC 0x0000000F 0x0000
1603 R EPwm7Regs_ETINTPS 0x0000000F 0x0011
1604 R EPwm7Regs_ETSOCPS 0x0000000F 0x0001
1605 R EPwm7Regs_ETCNTINITCTL 0x0000000F 0x0000
1606 R EPwm7Regs_ETCNTINIT 0x0000000F 0x0000
1607 R EPwm7Regs_DCTRIPSEL 0x0000000F 0x0000
1608 R EPwm7Regs_DCACTL 0x0000000F 0x0000
1609 R EPwm7Regs_DCBCTL 0x0000000F 0x0000
1610 R EPwm7Regs_DCFCTL 0x0000000F 0x0000
1611 R EPwm7Regs_DCCAPCTL 0x0000000F 0x0000
1612 R EPwm7Regs_DCFOFFSET 0x0000000F 0x0000
1613 R EPwm7Regs_DCFOFFSETCNT 0x0000000F 0x0000
1614 R EPwm7Regs_DCFWINDOW 0x0000000F 0x0000
1615 R EPwm7Regs_DCFWINDOWCNT 0x0000000F 0x0000
1616 R EPwm7Regs_DCCAP 0x0000000F 0x0000
1617 R EPwm7Regs_DCAHTRIPSEL 0x0000000F 0x0000
1618 R EPwm7Regs_DCALTRIPSEL 0x0000000F 0x0000
1619 R EPwm7Regs_DCBHTRIPSEL 0x0000000F 0x0000
1620 R EPwm7Regs_DCBLTRIPSEL 0x0000000F 0x0000
1621 R EPwm7Regs_EPWMLOCK 0x0000000B 0x00000000
1622 R EPwm8Regs_TBCTL 0x0000000F 0x0083
1623 R EPwm8Regs_TBCTL2 0x0000000F 0x0000
1624 R EPwm8Regs_TBCTR 0x0000000F 0x0000
1625 R EPwm8Regs_TBSTS 0x0000000F 0x0000

1626 R EPwm8Regs_CMPCTL 0x0000000F 0x0000
1627 R EPwm8Regs_CMPCTL2 0x0000000F 0x0000
1628 R EPwm8Regs_DBCTL 0x0000000F 0x0000
1629 R EPwm8Regs_DBCTL2 0x0000000F 0x0000
1630 R EPwm8Regs_AQCTL 0x0000000F 0x0000
1631 R EPwm8Regs_AQTSRCSEL 0x0000000F 0x0000
1632 R EPwm8Regs_PCCTL 0x0000000F 0x0000
1633 R EPwm8Regs_HRCNFG 0x0000000F 0x0000
1634 R EPwm8Regs_HRPWR 0x0000000F 0x0000
1635 R EPwm8Regs_HRMSTEP 0x0000000F 0x0000
1636 R EPwm8Regs_HRCNFG2 0x0000000F 0x0000
1637 R EPwm8Regs_HRPCTL 0x0000000F 0x0000
1638 R EPwm8Regs_TRREM 0x0000000F 0x0000
1639 R EPwm8Regs_GLDCTL 0x0000000F 0x0000
1640 R EPwm8Regs_GLDCFG 0x0000000F 0x0000
1641 R EPwm8Regs_EPWMXLINK 0x0000000B 0x70077777
1642 R EPwm8Regs_AQCTLA 0x0000000F 0x0000
1643 R EPwm8Regs_AQCTLA2 0x0000000F 0x0000
1644 R EPwm8Regs_AQCTLB 0x0000000F 0x0000
1645 R EPwm8Regs_AQCTLB2 0x0000000F 0x0000
1646 R EPwm8Regs_AQSFRC 0x0000000F 0x0000
1647 R EPwm8Regs_AQCSFRC 0x0000000F 0x0000
1648 R EPwm8Regs_DBREDHR 0x0000000F 0x0000
1649 R EPwm8Regs_DBRED 0x0000000F 0x0000
1650 R EPwm8Regs_DBFEDHR 0x0000000F 0x0000
1651 R EPwm8Regs_DBFED 0x0000000F 0x0000
1652 R EPwm8Regs_TBPHS 0x0000000B 0x00000000
1653 R EPwm8Regs_TBPRDHR 0x0000000F 0x0000
1654 R EPwm8Regs_TBPRD 0x0000000F 0x0000
1655 R EPwm8Regs_CMPA 0x0000000B 0x00000000
1656 R EPwm8Regs_CMPB 0x0000000B 0x00000000
1657 R EPwm8Regs_CMPC 0x0000000F 0x0000
1658 R EPwm8Regs_CMPD 0x0000000F 0x0000
1659 R EPwm8Regs_GLDCTL2 0x0000000F 0x0000
1660 R EPwm8Regs_TZSEL 0x0000000F 0x0000
1661 R EPwm8Regs_TZDCSEL 0x0000000F 0x0000
1662 R EPwm8Regs_TZCTL 0x0000000F 0x0000
1663 R EPwm8Regs_TZCTL2 0x0000000F 0x0000
1664 R EPwm8Regs_TZCTLDCA 0x0000000F 0x0000
1665 R EPwm8Regs_TZCTLDCB 0x0000000F 0x0000
1666 R EPwm8Regs_TZEINT 0x0000000F 0x0000
1667 R EPwm8Regs_TZFLG 0x0000000F 0x0000
1668 R EPwm8Regs_TZCBCFLG 0x0000000F 0x0000
1669 R EPwm8Regs_TZOSTFLG 0x0000000F 0x0000
1670 R EPwm8Regs_TZCLR 0x0000000F 0x0000
1671 R EPwm8Regs_TZCBCCLR 0x0000000F 0x0000
1672 R EPwm8Regs_TZOSTCLR 0x0000000F 0x0000
1673 R EPwm8Regs_TZFRC 0x0000000F 0x0000
1674 R EPwm8Regs_ETSEL 0x0000000F 0x0000
1675 R EPwm8Regs_ETPS 0x0000000F 0x0000
1676 R EPwm8Regs_ETFLG 0x0000000F 0x0000
1677 R EPwm8Regs_ETCLR 0x0000000F 0x0000
1678 R EPwm8Regs_ETFRC 0x0000000F 0x0000
1679 R EPwm8Regs_ETINTPS 0x0000000F 0x0000
1680 R EPwm8Regs_ETSOCPS 0x0000000F 0x0000
1681 R EPwm8Regs_ETCNTINITCTL 0x0000000F 0x0000
1682 R EPwm8Regs_ETCNTINIT 0x0000000F 0x0000
1683 R EPwm8Regs_DCTRIPSEL 0x0000000F 0x0000
1684 R EPwm8Regs_DCACTL 0x0000000F 0x0000
1685 R EPwm8Regs_DCBCTL 0x0000000F 0x0000
1686 R EPwm8Regs_DCFCTL 0x0000000F 0x0000
1687 R EPwm8Regs_DCCAPCTL 0x0000000F 0x0000
1688 R EPwm8Regs_DCFOFFSET 0x0000000F 0x0000
1689 R EPwm8Regs_DCFOFFSETCNT 0x0000000F 0x0000
1690 R EPwm8Regs_DCFWINDOW 0x0000000F 0x0000

1691 R EPwm8Regs_DCFWINDOWCNT 0x0000000F 0x0000
1692 R EPwm8Regs_DCCAP 0x0000000F 0x0000
1693 R EPwm8Regs_DCAHTRIPSEL 0x0000000F 0x0000
1694 R EPwm8Regs_DCALTRIPSEL 0x0000000F 0x0000
1695 R EPwm8Regs_DCBHTRIPSEL 0x0000000F 0x0000
1696 R EPwm8Regs_DCBLTRIPSEL 0x0000000F 0x0000
1697 R EPwm8Regs_EPWMLOCK 0x0000000B 0x00000000
1698 R EPwmXbarRegs_TRIP4MUX0T015CFG 0x0000000B 0x00000000
1699 R EPwmXbarRegs_TRIP4MUX16T031CFG 0x0000000B 0x00000000
1700 R EPwmXbarRegs_TRIP5MUX0T015CFG 0x0000000B 0x00000000
1701 R EPwmXbarRegs_TRIP5MUX16T031CFG 0x0000000B 0x00000000
1702 R EPwmXbarRegs_TRIP7MUX0T015CFG 0x0000000B 0x00000000
1703 R EPwmXbarRegs_TRIP7MUX16T031CFG 0x0000000B 0x00000000
1704 R EPwmXbarRegs_TRIP8MUX0T015CFG 0x0000000B 0x00000000
1705 R EPwmXbarRegs_TRIP8MUX16T031CFG 0x0000000B 0x00000000
1706 R EPwmXbarRegs_TRIP9MUX0T015CFG 0x0000000B 0x00000000
1707 R EPwmXbarRegs_TRIP9MUX16T031CFG 0x0000000B 0x00000000
1708 R EPwmXbarRegs_TRIP10MUX0T015CFG 0x0000000B 0x00000000
1709 R EPwmXbarRegs_TRIP10MUX16T031CFG 0x0000000B 0x00000000
1710 R EPwmXbarRegs_TRIP11MUX0T015CFG 0x0000000B 0x00000000
1711 R EPwmXbarRegs_TRIP11MUX16T031CFG 0x0000000B 0x00000000
1712 R EPwmXbarRegs_TRIP12MUX0T015CFG 0x0000000B 0x00000000
1713 R EPwmXbarRegs_TRIP12MUX16T031CFG 0x0000000B 0x00000000
1714 R EPwmXbarRegs_TRIP4MUXENABLE 0x0000000B 0x00000000
1715 R EPwmXbarRegs_TRIP5MUXENABLE 0x0000000B 0x00000000
1716 R EPwmXbarRegs_TRIP7MUXENABLE 0x0000000B 0x00000000
1717 R EPwmXbarRegs_TRIP8MUXENABLE 0x0000000B 0x00000000
1718 R EPwmXbarRegs_TRIP9MUXENABLE 0x0000000B 0x00000000
1719 R EPwmXbarRegs_TRIP10MUXENABLE 0x0000000B 0x00000000
1720 R EPwmXbarRegs_TRIP11MUXENABLE 0x0000000B 0x00000000
1721 R EPwmXbarRegs_TRIP12MUXENABLE 0x0000000B 0x00000000
1722 R EPwmXbarRegs_TRIPOUTINV 0x0000000B 0x00000000
1723 R EPwmXbarRegs_TRIPLOCK 0x0000000B 0x00000000
1724 R EQep1Regs_QPOSCNT 0x0000000B 0x00000000
1725 R EQep1Regs_QPOSINIT 0x0000000B 0x00000000
1726 R EQep1Regs_QPOSMAX 0x0000000B 0x00000000
1727 R EQep1Regs_QPOSCMP 0x0000000B 0x00000000
1728 R EQep1Regs_QPOSILAT 0x0000000B 0x00000000
1729 R EQep1Regs_QPOSSLAT 0x0000000B 0x00000000
1730 R EQep1Regs_QPOSLAT 0x0000000B 0x00000000
1731 R EQep1Regs_QUTMR 0x0000000B 0x00000000
1732 R EQep1Regs_QUPRD 0x0000000B 0x00000000
1733 R EQep1Regs_QWDTMR 0x0000000F 0x0000
1734 R EQep1Regs_QWDPRD 0x0000000F 0x0000
1735 R EQep1Regs_QDECCTL 0x0000000F 0x0000
1736 R EQep1Regs_QEPCTL 0x0000000F 0x0000
1737 R EQep1Regs_QCAPCTL 0x0000000F 0x0000
1738 R EQep1Regs_QPOSCTL 0x0000000F 0x0000
1739 R EQep1Regs_QEINT 0x0000000F 0x0000
1740 R EQep1Regs_QFLG 0x0000000F 0x0000
1741 R EQep1Regs_QCLR 0x0000000F 0x0000
1742 R EQep1Regs_QFRC 0x0000000F 0x0000
1743 R EQep1Regs_QEPSTS 0x0000000F 0x0000
1744 R EQep1Regs_QCTMR 0x0000000F 0x0000
1745 R EQep1Regs_QCPRD 0x0000000F 0x0000
1746 R EQep1Regs_QCTMRLAT 0x0000000F 0x0000
1747 R EQep1Regs_QCPRDLAT 0x0000000F 0x0000
1748 R EQep1Regs_REV 0x0000000B 0x00000001
1749 R EQep1Regs_QEPSTROBESEL 0x0000000B 0x00000000
1750 R EQep1Regs_QMACTRL 0x0000000B 0x00000000
1751 R EQep2Regs_QPOSCNT 0x0000000B 0x00000000
1752 R EQep2Regs_QPOSINIT 0x0000000B 0x00000000
1753 R EQep2Regs_QPOSMAX 0x0000000B 0x00000000
1754 R EQep2Regs_QPOSCMP 0x0000000B 0x00000000
1755 R EQep2Regs_QPOSILAT 0x0000000B 0x00000000

1756 R EQep2Regs_QPOSSLAT 0x0000000B 0x00000000
1757 R EQep2Regs_QPOSLAT 0x0000000B 0x00000000
1758 R EQep2Regs_QUTMR 0x0000000B 0x00000000
1759 R EQep2Regs_QUPRD 0x0000000B 0x00000000
1760 R EQep2Regs_QWDTMR 0x0000000F 0x0000
1761 R EQep2Regs_QWDPRD 0x0000000F 0x0000
1762 R EQep2Regs_QDECCTL 0x0000000F 0x0000
1763 R EQep2Regs_QEPCTL 0x0000000F 0x0000
1764 R EQep2Regs_QCAPCTL 0x0000000F 0x0000
1765 R EQep2Regs_QPOSCTL 0x0000000F 0x0000
1766 R EQep2Regs_QEINT 0x0000000F 0x0000
1767 R EQep2Regs_QFLG 0x0000000F 0x0000
1768 R EQep2Regs_QCLR 0x0000000F 0x0000
1769 R EQep2Regs_QFRC 0x0000000F 0x0000
1770 R EQep2Regs_QEPSTS 0x0000000F 0x0000
1771 R EQep2Regs_QCTMR 0x0000000F 0x0000
1772 R EQep2Regs_QCPRD 0x0000000F 0x0000
1773 R EQep2Regs_QCTMRLAT 0x0000000F 0x0000
1774 R EQep2Regs_QCPRDLAT 0x0000000F 0x0000
1775 R EQep2Regs_REV 0x0000000B 0x00000001
1776 R EQep2Regs_QEPSTROBESEL 0x0000000B 0x00000000
1777 R EQep2Regs_QMACTRL 0x0000000B 0x00000000
1778 R FsiTxARegs_TX_MASTER_CTRL 0x0000000F 0x0000
1779 R FsiTxARegs_TX_CLK_CTRL 0x0000000F 0x0000
1780 R FsiTxARegs_TX_OPER_CTRL_LO 0x0000000F 0x0000
1781 R FsiTxARegs_TX_OPER_CTRL_HI_1 0x0000000F 0x0000
1782 R FsiTxARegs_TX_FRAME_CTRL 0x0000000F 0x0000
1783 R FsiTxARegs_TX_FRAME_TAG_UDATA 0x0000000F 0x0000
1784 R FsiTxARegs_TX_BUF_PTR_LOAD 0x0000000F 0x0000
1785 R FsiTxARegs_TX_BUF_PTR_STS 0x0000000F 0x0000
1786 R FsiTxARegs_TX_PING_CTRL 0x0000000F 0x0000
1787 R FsiTxARegs_TX_PING_TAG 0x0000000F 0x0000
1788 R FsiTxARegs_TX_PING_TO_REF 0x0000000B 0x00000000
1789 R FsiTxARegs_TX_PING_TO_CNT 0x0000000B 0x00000000
1790 R FsiTxARegs_TX_INT_CTRL 0x0000000F 0x0000
1791 R FsiTxARegs_TX_DMA_CTRL_1 0x0000000F 0x0000
1792 R FsiTxARegs_TX_LOCK_CTRL 0x0000000F 0x0000
1793 R FsiTxARegs_TX_EVT_STS 0x0000000F 0x0000
1794 R FsiTxARegs_TX_EVT_CLR 0x0000000F 0x0000
1795 R FsiTxARegs_TX_EVT_FRC 0x0000000F 0x0000
1796 R FsiTxARegs_TX_USER_CRC_1 0x0000000F 0x0000
1797 R FsiTxARegs_TX_ECC_DATA 0x0000000B 0x00000000
1798 R FsiTxARegs_TX_ECC_VAL 0x0000000F 0x0000
1799 R FsiTxARegs_TX_BUF_BASE 0x0000000F 0x0000
1800 R FsiRxARegs_RX_MASTER_CTRL 0x0000000F 0x0000
1801 R FsiRxARegs_RX_OPER_CTRL 0x0000000F 0x0000
1802 R FsiRxARegs_RX_FRAME_INFO 0x0000000F 0x0000
1803 R FsiRxARegs_RX_FRAME_TAG_UDATA 0x0000000F 0x0000
1804 R FsiRxARegs_RX_DMA_CTRL 0x0000000F 0x0000
1805 R FsiRxARegs_RX_EVT_STS 0x0000000F 0x0000
1806 R FsiRxARegs_RX_CRC_INFO 0x0000000F 0x0000
1807 R FsiRxARegs_RX_EVT_CLR 0x0000000F 0x0000
1808 R FsiRxARegs_RX_EVT_FRC 0x0000000F 0x0000
1809 R FsiRxARegs_RX_BUF_PTR_LOAD 0x0000000F 0x0000
1810 R FsiRxARegs_RX_BUF_PTR_STS 0x0000000F 0x0000
1811 R FsiRxARegs_RX_FRAME_WD_CTRL 0x0000000F 0x0000
1812 R FsiRxARegs_RX_FRAME_WD_REF 0x0000000B 0x00000000
1813 R FsiRxARegs_RX_FRAME_WD_CNT 0x0000000B 0x00000000
1814 R FsiRxARegs_RX_PING_WD_CTRL 0x0000000F 0x0000
1815 R FsiRxARegs_RX_PING_TAG 0x0000000F 0x0000
1816 R FsiRxARegs_RX_PING_WD_REF 0x0000000B 0x00000000
1817 R FsiRxARegs_RX_PING_WD_CNT 0x0000000B 0x00000000
1818 R FsiRxARegs_RX_INT1_CTRL 0x0000000F 0x0000
1819 R FsiRxARegs_RX_INT2_CTRL 0x0000000F 0x0000
1820 R FsiRxARegs_RX_LOCK_CTRL 0x0000000F 0x0000

1821 R FsiRxARegs_RX_ECC_DATA 0x0000000B 0x00000000
1822 R FsiRxARegs_RX_ECC_VAL 0x0000000F 0x0000
1823 R FsiRxARegs_RX_ECC_SEC_DATA 0x0000000B 0x00000000
1824 R FsiRxARegs_RX_ECC_LOG 0x0000000F 0x0000
1825 R FsiRxARegs_RX_DLYLINE_CTRL 0x0000000F 0x0000
1826 R FsiRxARegs_RX_VIS_1 0x0000000B 0x00000000
1827 R FsiRxARegs_RX_BUF_BASE 0x0000000F 0x0000
1828 R FlashCtrlRegs_FRDCNTL 0x0000000B 0x00000400
1829 R FlashCtrlRegs_FBAC 0x0000000B 0x0000000F
1830 R FlashCtrlRegs_FBFALLBACK 0x0000000B 0x0000000F
1831 R FlashCtrlRegs_FBPRDY 0x0000000B 0x00008003
1832 R FlashCtrlRegs_FPAC1 0x0000000B 0x03E80001
1833 R FlashCtrlRegs_FPAC2 0x0000000B 0x00000000
1834 R FlashCtrlRegs_FMSTAT 0x0000000B 0x00000000
1835 R FlashCtrlRegs_FRD_INTF_CTRL 0x0000000B 0x00000003
1836 R FlashEccRegs_ECC_ENABLE 0x0000000B 0x0000000A
1837 R FlashEccRegs_SINGLE_ERR_ADDR_LOW 0x0000000B 0x00000000
1838 R FlashEccRegs_SINGLE_ERR_ADDR_HIGH 0x0000000B 0x00000000
1839 R FlashEccRegs_UNC_ERR_ADDR_LOW 0x0000000B 0x00000000
1840 R FlashEccRegs_UNC_ERR_ADDR_HIGH 0x0000000B 0x00000000
1841 R FlashEccRegs_ERR_STATUS 0x0000000B 0x00000000
1842 R FlashEccRegs_ERR_POS 0x0000000B 0x00000000
1843 R FlashEccRegs_ERR_STATUS_CLR 0x0000000B 0x00000000
1844 R FlashEccRegs_ERR_CNT 0x0000000B 0x00000000
1845 R FlashEccRegs_ERR_THRESHOLD 0x0000000B 0x00000000
1846 R FlashEccRegs_ERR_INTFLG 0x0000000B 0x00000000
1847 R FlashEccRegs_ERR_INTCLR 0x0000000B 0x00000000
1848 R FlashEccRegs_FDATAH_TEST 0x0000000B 0x00000000
1849 R FlashEccRegs_FDATAH_TEST 0x0000000B 0x00000000
1850 R FlashEccRegs_FADDR_TEST 0x0000000B 0x00000000
1851 R FlashEccRegs_FECC_TEST 0x0000000B 0x00000000
1852 R FlashEccRegs_FECC_CTRL 0x0000000B 0x00000000
1853 R FlashEccRegs_FOUTH_TEST 0x0000000B 0x00000000
1854 R FlashEccRegs_FOUTL_TEST 0x0000000B 0x00000000
1855 R FlashEccRegs_FECC_STATUS 0x0000000B 0x00000000
1856 R GpioCtrlRegs_GPACTRL 0x0000000B 0x00000000
1857 R GpioCtrlRegs_GPAQSEL1 0x0000000B 0x00000000
1858 R GpioCtrlRegs_GPAQSEL2 0x0000000B 0x00000000
1859 R GpioCtrlRegs_GPAMUX1 0x0000000B 0x000000A0
1860 R GpioCtrlRegs_GPAMUX2 0x0000000B 0x0F000000
1861 R GpioCtrlRegs_GPADIR 0x0000000B 0x00C010F0
1862 R GpioCtrlRegs_GPAPUD 0x0000000B 0xFFFFFFFF
1863 R GpioCtrlRegs_GPAINV 0x0000000B 0x00000000
1864 R GpioCtrlRegs_GPAODR 0x0000000B 0x00000000
1865 R GpioCtrlRegs_GPAAMSEL 0x0000000B 0x00000000
1866 R GpioCtrlRegs_GPAGMUX1 0x0000000B 0x00000050
1867 R GpioCtrlRegs_GPAGMUX2 0x0000000B 0x00000000
1868 R GpioCtrlRegs_GPACSEL1 0x0000000B 0x00000000
1869 R GpioCtrlRegs_GPACSEL2 0x0000000B 0x00000000
1870 R GpioCtrlRegs_GPACSEL3 0x0000000B 0x00000000
1871 R GpioCtrlRegs_GPACSEL4 0x0000000B 0x00000000
1872 R GpioCtrlRegs_GPALOCK 0x0000000B 0x00000000
1873 R GpioCtrlRegs_GPACR 0x0000000B 0x00000000
1874 R GpioCtrlRegs_GPBCTRL 0x0000000B 0x00000000
1875 R GpioCtrlRegs_GPBQSEL1 0x0000000B 0x00000000
1876 R GpioCtrlRegs_GPBQSEL2 0x0000000B 0x00000000
1877 R GpioCtrlRegs_GPBMUX1 0x0000000B 0x000000C0
1878 R GpioCtrlRegs_GPBMUX2 0x0000000B 0x00000000
1879 R GpioCtrlRegs_GPBDIR 0x0000000B 0x00000000
1880 R GpioCtrlRegs_GBPUD 0x0000000B 0xFFFFFFFF
1881 R GpioCtrlRegs_GPBINV 0x0000000B 0x00000000
1882 R GpioCtrlRegs_GPBODR 0x0000000B 0x00000000
1883 R GpioCtrlRegs_GPBGMUX1 0x0000000B 0x000000C0
1884 R GpioCtrlRegs_GPBGMUX2 0x0000000B 0x00000000
1885 R GpioCtrlRegs_GPBCSEL1 0x0000000B 0x00000000

1886 R GpioCtrlRegs_GPBCSEL2 0x0000000B 0x00000000
1887 R GpioCtrlRegs_GPBCSEL3 0x0000000B 0x00000000
1888 R GpioCtrlRegs_GPBCSEL4 0x0000000B 0x00000000
1889 R GpioCtrlRegs_GPBLOCK 0x0000000B 0x00000000
1890 R GpioCtrlRegs_GPBCR 0x0000000B 0x00000000
1891 R GpioCtrlRegs_GPHCTRL 0x0000000B 0x00000000
1892 R GpioCtrlRegs_GPHQSEL1 0x0000000B 0x00000000
1893 R GpioCtrlRegs_GPHQSEL2 0x0000000B 0x00000000
1894 R GpioCtrlRegs_GPHINV 0x0000000B 0x00000000
1895 R GpioCtrlRegs_GPHAMSEL 0x0000000B 0xFFFFFFFF
1896 R GpioCtrlRegs_GPHLOCK 0x0000000B 0x00000000
1897 R GpioCtrlRegs_GPHCR 0x0000000B 0x00000000
1898 R GpioDataRegs_GPADAT 0x0000000B 0x01800D0C
1899 R GpioDataRegs_GPASET 0x0000000B 0x00000000
1900 R GpioDataRegs_GPACLEAR 0x0000000B 0x00000000
1901 R GpioDataRegs_GPATOGGLE 0x0000000B 0x00000000
1902 R GpioDataRegs_GPB DAT 0x0000000B 0x00000029
1903 R GpioDataRegs_GPBSET 0x0000000B 0x00000000
1904 R GpioDataRegs_GPBCLEAR 0x0000000B 0x00000000
1905 R GpioDataRegs_GPBTOGGLE 0x0000000B 0x00000000
1906 R GpioDataRegs_GPHDAT 0x0000000B 0x00000000
1907 R HRCap6Regs_HRCTL 0x0000000B 0x00000000
1908 R HRCap6Regs_HRINTEN 0x0000000B 0x00000000
1909 R HRCap6Regs_HRFLG 0x0000000B 0x00000000
1910 R HRCap6Regs_HRCLR 0x0000000B 0x00000000
1911 R HRCap6Regs_HRFRC 0x0000000B 0x00000000
1912 R HRCap6Regs_HRCALPRD 0x0000000B 0x003FFFFFFF
1913 R HRCap6Regs_HRSYSCLKCTR 0x0000000B 0x00000000
1914 R HRCap6Regs_HRSYSCLKCAP 0x0000000B 0x00000000
1915 R HRCap6Regs_HRCLKCTR 0x0000000B 0x00000000
1916 R HRCap6Regs_HRCLKCAP 0x0000000B 0x00000000
1917 R HRCap7Regs_HRCTL 0x0000000B 0x00000000
1918 R HRCap7Regs_HRINTEN 0x0000000B 0x00000000
1919 R HRCap7Regs_HRFLG 0x0000000B 0x00000000
1920 R HRCap7Regs_HRCLR 0x0000000B 0x00000000
1921 R HRCap7Regs_HRFRC 0x0000000B 0x00000000
1922 R HRCap7Regs_HRCALPRD 0x0000000B 0x003FFFFFFF
1923 R HRCap7Regs_HRSYSCLKCTR 0x0000000B 0x00000000
1924 R HRCap7Regs_HRSYSCLKCAP 0x0000000B 0x00000000
1925 R HRCap7Regs_HRCLKCTR 0x0000000B 0x00000000
1926 R HRCap7Regs_HRCLKCAP 0x0000000B 0x00000000
1927 R I2caRegs_I2COAR 0x0000000F 0x0000
1928 R I2caRegs_I2CIER 0x0000000F 0x0000
1929 R I2caRegs_I2CSTR 0x0000000F 0x0410
1930 R I2caRegs_I2CCLKL 0x0000000F 0x0000
1931 R I2caRegs_I2CCLKH 0x0000000F 0x0000
1932 R I2caRegs_I2CCNT 0x0000000F 0x0000
1933 R I2caRegs_I2CDRR 0x0000000F 0x0000
1934 R I2caRegs_I2CSAR 0x0000000F 0x03FF
1935 R I2caRegs_I2CDXR 0x0000000F 0x0000
1936 R I2caRegs_I2CMDR 0x0000000F 0x0000
1937 R I2caRegs_I2CISRC 0x0000000F 0x0000
1938 R I2caRegs_I2CEMDR 0x0000000F 0x0001
1939 R I2caRegs_I2CPSC 0x0000000F 0x0000
1940 R I2caRegs_I2CFFTX 0x0000000F 0x0000
1941 R I2caRegs_I2CFFRX 0x0000000F 0x0000
1942 R InputXbarRegs_INPUT1SELECT 0x0000000F 0xFFFFE
1943 R InputXbarRegs_INPUT2SELECT 0x0000000F 0xFFFFE
1944 R InputXbarRegs_INPUT3SELECT 0x0000000F 0xFFFFE
1945 R InputXbarRegs_INPUT4SELECT 0x0000000F 0xFFFFE
1946 R InputXbarRegs_INPUT5SELECT 0x0000000F 0xFFFFE
1947 R InputXbarRegs_INPUT6SELECT 0x0000000F 0xFFFFE
1948 R InputXbarRegs_INPUT7SELECT 0x0000000F 0xFFFFE
1949 R InputXbarRegs_INPUT8SELECT 0x0000000F 0xFFFFE
1950 R InputXbarRegs_INPUT9SELECT 0x0000000F 0xFFFFE

1951 R InputXbarRegs_INPUT10SELECT 0x0000000F 0xFFFFE
1952 R InputXbarRegs_INPUT11SELECT 0x0000000F 0xFFFFE
1953 R InputXbarRegs_INPUT12SELECT 0x0000000F 0xFFFFE
1954 R InputXbarRegs_INPUT13SELECT 0x0000000F 0xFFFFE
1955 R InputXbarRegs_INPUT14SELECT 0x0000000F 0xFFFFE
1956 R InputXbarRegs_INPUT15SELECT 0x0000000F 0x000D
1957 R InputXbarRegs_INPUT16SELECT 0x0000000F 0xFFFFE
1958 R InputXbarRegs_INPUTSELECTLOCK 0x0000000B 0x00000000
1959 R LinaRegs_SCIGCR0 0x0000000B 0x00000000
1960 R LinaRegs_SCIGCR1 0x0000000B 0x00000000
1961 R LinaRegs_SCIGCR2 0x0000000B 0x00000000
1962 R LinaRegs_SCISSETINT 0x0000000B 0x00000000
1963 R LinaRegs_SCICLEARINT 0x0000000B 0x00000000
1964 R LinaRegs_SCISSETINTLVL 0x0000000B 0x00000000
1965 R LinaRegs_SCICLEARINTLVL 0x0000000B 0x00000000
1966 R LinaRegs_SCIFLR 0x0000000B 0x00000000
1967 R LinaRegs_SCIINTVECT0 0x0000000B 0x00000000
1968 R LinaRegs_SCIINTVECT1 0x0000000B 0x00000000
1969 R LinaRegs_SCIFORMAT 0x0000000B 0x00000000
1970 R LinaRegs_BRSR 0x0000000B 0x00000000
1971 R LinaRegs_SCIED 0x0000000B 0x00000000
1972 R LinaRegs_SCIRD 0x0000000B 0x00000000
1973 R LinaRegs_SCITD 0x0000000B 0x09040904
1974 R LinaRegs_SCIPIO0 0x0000000B 0x00000000
1975 R LinaRegs_SCIPIO2 0x0000000B 0x00000000
1976 R LinaRegs_LINCOMP 0x0000000B 0x00000000
1977 R LinaRegs_LINRD0 0x0000000B 0x00000000
1978 R LinaRegs_LINRD1 0x0000000B 0x00000000
1979 R LinaRegs_LINMASK 0x0000000B 0x00000000
1980 R LinaRegs_LINID 0x0000000B 0x00000000
1981 R LinaRegs_LINTD0 0x0000000B 0x00000000
1982 R LinaRegs_LINTD1 0x0000000B 0x00000000
1983 R LinaRegs_MBRSR 0x0000000B 0x00000000
1984 R LinaRegs_IODFTCTRL 0x0000000B 0x00000000
1985 R LinaRegs_LIN_GLB_INT_EN 0x0000000B 0x00000000
1986 R LinaRegs_LIN_GLB_INT_FLG 0x0000000B 0x00000000
1987 R LinaRegs_LIN_GLB_INT_CLR 0x0000000B 0x00000000
1988 R MemCfgRegs_DxLOCK 0x0000000B 0x00000000
1989 R MemCfgRegs_DxCOMMIT 0x0000000B 0x00000000
1990 R MemCfgRegs_DxACCPROT0 0x0000000B 0x00000000
1991 R MemCfgRegs_DxTEST 0x0000000B 0x00000000
1992 R MemCfgRegs_DxINIT 0x0000000B 0x00000000
1993 R MemCfgRegs_DxINITDONE 0x0000000B 0x00000000
1994 R MemCfgRegs_LSxLOCK 0x0000000B 0x00000000
1995 R MemCfgRegs_LSxCOMMIT 0x0000000B 0x00000000
1996 R MemCfgRegs_LSxMSEL 0x0000000B 0x00000015
1997 R MemCfgRegs_LSxCLAPGM 0x0000000B 0x00000001
1998 R MemCfgRegs_LSxACCPROT0 0x0000000B 0x00000000
1999 R MemCfgRegs_LSxACCPROT1 0x0000000B 0x00000000
2000 R MemCfgRegs_LSxTEST 0x0000000B 0x00000000
2001 R MemCfgRegs_LSxINIT 0x0000000B 0x00000000
2002 R MemCfgRegs_LSxINITDONE 0x0000000B 0x00000000
2003 R MemCfgRegs_GSxLOCK 0x0000000B 0x00000000
2004 R MemCfgRegs_GSxCOMMIT 0x0000000B 0x00000000
2005 R MemCfgRegs_GSxACCPROT0 0x0000000B 0x00000000
2006 R MemCfgRegs_GSxACCPROT1 0x0000000B 0x00000000
2007 R MemCfgRegs_GSxACCPROT2 0x0000000B 0x00000000
2008 R MemCfgRegs_GSxACCPROT3 0x0000000B 0x00000000
2009 R MemCfgRegs_GSxTEST 0x0000000B 0x00000000
2010 R MemCfgRegs_GSxINIT 0x0000000B 0x00000000
2011 R MemCfgRegs_GSxINITDONE 0x0000000B 0x00000000
2012 R MemCfgRegs_MSGxLOCK 0x0000000B 0x00000000
2013 R MemCfgRegs_MSGxCOMMIT 0x0000000B 0x00000000
2014 R MemCfgRegs_MSGxTEST 0x0000000B 0x00000000
2015 R MemCfgRegs_MSGxINIT 0x0000000B 0x00000000

2016 R MemCfgRegs_MSGxINITDONE 0x0000000B 0x00000000
2017 R MemoryErrorRegs_UCERRFLG 0x0000000B 0x00000000
2018 R MemoryErrorRegs_UCERRSET 0x0000000B 0x00000000
2019 R MemoryErrorRegs_UCERRCLR 0x0000000B 0x00000000
2020 R MemoryErrorRegs_UCCPUREADDR 0x0000000B 0x00000000
2021 R MemoryErrorRegs_UCDMAREADDR 0x0000000B 0x00000000
2022 R MemoryErrorRegs_UCCLA1READDR 0x0000000B 0x00000000
2023 R MemoryErrorRegs_CERRFLG 0x0000000B 0x00000000
2024 R MemoryErrorRegs_CERRSET 0x0000000B 0x00000000
2025 R MemoryErrorRegs_CERRCLR 0x0000000B 0x00000000
2026 R MemoryErrorRegs_CCPUREADDR 0x0000000B 0x00000000
2027 R MemoryErrorRegs_CERRCNT 0x0000000B 0x00000000
2028 R MemoryErrorRegs_CERRTHRES 0x0000000B 0x00000000
2029 R MemoryErrorRegs_CEINTFLG 0x0000000B 0x00000000
2030 R MemoryErrorRegs_CEINTCLR 0x0000000B 0x00000000
2031 R MemoryErrorRegs_CEINTSET 0x0000000B 0x00000000
2032 R MemoryErrorRegs_CEINTEN 0x0000000B 0x00000000
2033 R NmiIntruptRegs_NMICFG 0x0000000F 0x0001
2034 R NmiIntruptRegs_NMIFLG 0x0000000F 0x0000
2035 R NmiIntruptRegs_NMIFLGCLR 0x0000000F 0x0000
2036 R NmiIntruptRegs_NMIFLGFRC 0x0000000F 0x0000
2037 R NmiIntruptRegs_NMIWDCNT 0x0000000F 0x0000
2038 R NmiIntruptRegs_NMIWDPRD 0x0000000F 0xFFFF
2039 R NmiIntruptRegs_NMISHDFLG 0x0000000F 0x0000
2040 R OutputXbarRegs_OUTPUT1MUX0T015CFG 0x0000000B 0x00000000
2041 R OutputXbarRegs_OUTPUT1MUX16T031CFG 0x0000000B 0x00000000
2042 R OutputXbarRegs_OUTPUT2MUX0T015CFG 0x0000000B 0x00000000
2043 R OutputXbarRegs_OUTPUT2MUX16T031CFG 0x0000000B 0x00000000
2044 R OutputXbarRegs_OUTPUT3MUX0T015CFG 0x0000000B 0x00000000
2045 R OutputXbarRegs_OUTPUT3MUX16T031CFG 0x0000000B 0x00000000
2046 R OutputXbarRegs_OUTPUT4MUX0T015CFG 0x0000000B 0x00000000
2047 R OutputXbarRegs_OUTPUT4MUX16T031CFG 0x0000000B 0x00000000
2048 R OutputXbarRegs_OUTPUT5MUX0T015CFG 0x0000000B 0x00000000
2049 R OutputXbarRegs_OUTPUT5MUX16T031CFG 0x0000000B 0x00000000
2050 R OutputXbarRegs_OUTPUT6MUX0T015CFG 0x0000000B 0x00000000
2051 R OutputXbarRegs_OUTPUT6MUX16T031CFG 0x0000000B 0x00000000
2052 R OutputXbarRegs_OUTPUT7MUX0T015CFG 0x0000000B 0x00000000
2053 R OutputXbarRegs_OUTPUT7MUX16T031CFG 0x0000000B 0x00000000
2054 R OutputXbarRegs_OUTPUT8MUX0T015CFG 0x0000000B 0x00000000
2055 R OutputXbarRegs_OUTPUT8MUX16T031CFG 0x0000000B 0x00000000
2056 R OutputXbarRegs_OUTPUT1MUXENABLE 0x0000000B 0x00000000
2057 R OutputXbarRegs_OUTPUT2MUXENABLE 0x0000000B 0x00000000
2058 R OutputXbarRegs_OUTPUT3MUXENABLE 0x0000000B 0x00000000
2059 R OutputXbarRegs_OUTPUT4MUXENABLE 0x0000000B 0x00000000
2060 R OutputXbarRegs_OUTPUT5MUXENABLE 0x0000000B 0x00000000
2061 R OutputXbarRegs_OUTPUT6MUXENABLE 0x0000000B 0x00000000
2062 R OutputXbarRegs_OUTPUT7MUXENABLE 0x0000000B 0x00000000
2063 R OutputXbarRegs_OUTPUT8MUXENABLE 0x0000000B 0x00000000
2064 R OutputXbarRegs_OUTPUTLATCH 0x0000000B 0x00000000
2065 R OutputXbarRegs_OUTPUTLATCHCLR 0x0000000B 0x00000000
2066 R OutputXbarRegs_OUTPUTLATCHFRC 0x0000000B 0x00000000
2067 R OutputXbarRegs_OUTPUTLATCHENABLE 0x0000000B 0x00000000
2068 R OutputXbarRegs_OUTPUTINV 0x0000000B 0x00000000
2069 R OutputXbarRegs_OUTPUTLOCK 0x0000000B 0x00000000
2070 R SysPeriphAcRegs_ADCA_AC 0x0000000B 0x0000003F
2071 R SysPeriphAcRegs_ADCB_AC 0x0000000B 0x0000003F
2072 R SysPeriphAcRegs_ADCC_AC 0x0000000B 0x0000003F
2073 R SysPeriphAcRegs_CMPSS1_AC 0x0000000B 0x0000003F
2074 R SysPeriphAcRegs_CMPSS2_AC 0x0000000B 0x0000003F
2075 R SysPeriphAcRegs_CMPSS3_AC 0x0000000B 0x0000003F
2076 R SysPeriphAcRegs_CMPSS4_AC 0x0000000B 0x0000003F
2077 R SysPeriphAcRegs_CMPSS5_AC 0x0000000B 0x0000003F
2078 R SysPeriphAcRegs_CMPSS6_AC 0x0000000B 0x0000003F
2079 R SysPeriphAcRegs_CMPSS7_AC 0x0000000B 0x0000003F
2080 R SysPeriphAcRegs_DACA_AC 0x0000000B 0x0000003F

2081 R SysPeriphAcRegs_DACB_AC 0x0000000B 0x0000003F
2082 R SysPeriphAcRegs_PGA1_AC 0x0000000B 0x0000003F
2083 R SysPeriphAcRegs_PGA2_AC 0x0000000B 0x0000003F
2084 R SysPeriphAcRegs_PGA3_AC 0x0000000B 0x0000003F
2085 R SysPeriphAcRegs_PGA4_AC 0x0000000B 0x0000003F
2086 R SysPeriphAcRegs_PGA5_AC 0x0000000B 0x0000003F
2087 R SysPeriphAcRegs_PGA6_AC 0x0000000B 0x0000003F
2088 R SysPeriphAcRegs_PGA7_AC 0x0000000B 0x0000003F
2089 R SysPeriphAcRegs_EPWM1_AC 0x0000000B 0x0000003F
2090 R SysPeriphAcRegs_EPWM2_AC 0x0000000B 0x0000003F
2091 R SysPeriphAcRegs_EPWM3_AC 0x0000000B 0x0000003F
2092 R SysPeriphAcRegs_EPWM4_AC 0x0000000B 0x0000003F
2093 R SysPeriphAcRegs_EPWM5_AC 0x0000000B 0x0000003F
2094 R SysPeriphAcRegs_EPWM6_AC 0x0000000B 0x0000003F
2095 R SysPeriphAcRegs_EPWM7_AC 0x0000000B 0x0000003F
2096 R SysPeriphAcRegs_EPWM8_AC 0x0000000B 0x0000003F
2097 R SysPeriphAcRegs_EQEP1_AC 0x0000000B 0x0000003F
2098 R SysPeriphAcRegs_EQEP2_AC 0x0000000B 0x0000003F
2099 R SysPeriphAcRegs_ECAP1_AC 0x0000000B 0x0000003F
2100 R SysPeriphAcRegs_ECAP2_AC 0x0000000B 0x0000003F
2101 R SysPeriphAcRegs_ECAP3_AC 0x0000000B 0x0000003F
2102 R SysPeriphAcRegs_ECAP4_AC 0x0000000B 0x0000003F
2103 R SysPeriphAcRegs_ECAP5_AC 0x0000000B 0x0000003F
2104 R SysPeriphAcRegs_ECAP6_AC 0x0000000B 0x0000003F
2105 R SysPeriphAcRegs_ECAP7_AC 0x0000000B 0x0000003F
2106 R SysPeriphAcRegs_SDFM1_AC 0x0000000B 0x0000003F
2107 R SysPeriphAcRegs_CLB1_AC 0x0000000B 0x0000000F
2108 R SysPeriphAcRegs_CLB2_AC 0x0000000B 0x0000000F
2109 R SysPeriphAcRegs_CLB3_AC 0x0000000B 0x0000000F
2110 R SysPeriphAcRegs_CLB4_AC 0x0000000B 0x0000000F
2111 R SysPeriphAcRegs_CLA1PROMCRC_AC 0x0000000B 0x0000000F
2112 R SysPeriphAcRegs_SPIA_AC 0x0000000B 0x0000003F
2113 R SysPeriphAcRegs_SPIB_AC 0x0000000B 0x0000003F
2114 R SysPeriphAcRegs_PMBUS_A_AC 0x0000000B 0x0000003F
2115 R SysPeriphAcRegs_LIN_A_AC 0x0000000B 0x0000003F
2116 R SysPeriphAcRegs_DCANA_AC 0x0000000B 0x00000033
2117 R SysPeriphAcRegs_DCANB_AC 0x0000000B 0x00000033
2118 R SysPeriphAcRegs_FSIATX_AC 0x0000000B 0x0000003F
2119 R SysPeriphAcRegs_FSIARX_AC 0x0000000B 0x0000003F
2120 R SysPeriphAcRegs_HRPWM_A_AC 0x0000000B 0x0000003F
2121 R SysPeriphAcRegs_PERIPH_AC_LOCK 0x0000000B 0x00000000
2122 R Pga1Regs_PGACTL 0x0000000B 0x00000000
2123 R Pga1Regs_PGALOCK 0x0000000F 0x0000
2124 R Pga1Regs_PGAGAIN3TRIM 0x0000000F 0x0000
2125 R Pga1Regs_PGAGAIN6TRIM 0x0000000F 0x0000
2126 R Pga1Regs_PGAGAIN12TRIM 0x0000000F 0x0000
2127 R Pga1Regs_PGAGAIN24TRIM 0x0000000F 0x0000
2128 R Pga1Regs_PGATYPE 0x0000000F 0x0000
2129 R Pga2Regs_PGACTL 0x0000000B 0x00000000
2130 R Pga2Regs_PGALOCK 0x0000000F 0x0000
2131 R Pga2Regs_PGAGAIN3TRIM 0x0000000F 0x0000
2132 R Pga2Regs_PGAGAIN6TRIM 0x0000000F 0x0000
2133 R Pga2Regs_PGAGAIN12TRIM 0x0000000F 0x0000
2134 R Pga2Regs_PGAGAIN24TRIM 0x0000000F 0x0000
2135 R Pga2Regs_PGATYPE 0x0000000F 0x0000
2136 R Pga3Regs_PGACTL 0x0000000B 0x00000000
2137 R Pga3Regs_PGALOCK 0x0000000F 0x0000
2138 R Pga3Regs_PGAGAIN3TRIM 0x0000000F 0x0000
2139 R Pga3Regs_PGAGAIN6TRIM 0x0000000F 0x0000
2140 R Pga3Regs_PGAGAIN12TRIM 0x0000000F 0x0000
2141 R Pga3Regs_PGAGAIN24TRIM 0x0000000F 0x0000
2142 R Pga3Regs_PGATYPE 0x0000000F 0x0000
2143 R Pga4Regs_PGACTL 0x0000000B 0x00000000
2144 R Pga4Regs_PGALOCK 0x0000000F 0x0000
2145 R Pga4Regs_PGAGAIN3TRIM 0x0000000F 0x0000

2146 R Pga4Regs_PGAGAIN6TRIM 0x0000000F 0x0000
2147 R Pga4Regs_PGAGAIN12TRIM 0x0000000F 0x0000
2148 R Pga4Regs_PGAGAIN24TRIM 0x0000000F 0x0000
2149 R Pga4Regs_PGATYPE 0x0000000F 0x0000
2150 R Pga5Regs_PGACTL 0x0000000B 0x00000000
2151 R Pga5Regs_PGALOCK 0x0000000F 0x0000
2152 R Pga5Regs_PGAGAIN3TRIM 0x0000000F 0x0000
2153 R Pga5Regs_PGAGAIN6TRIM 0x0000000F 0x0000
2154 R Pga5Regs_PGAGAIN12TRIM 0x0000000F 0x0000
2155 R Pga5Regs_PGAGAIN24TRIM 0x0000000F 0x0000
2156 R Pga5Regs_PGATYPE 0x0000000F 0x0000
2157 R Pga6Regs_PGACTL 0x0000000B 0x00000000
2158 R Pga6Regs_PGALOCK 0x0000000F 0x0000
2159 R Pga6Regs_PGAGAIN3TRIM 0x0000000F 0x0000
2160 R Pga6Regs_PGAGAIN6TRIM 0x0000000F 0x0000
2161 R Pga6Regs_PGAGAIN12TRIM 0x0000000F 0x0000
2162 R Pga6Regs_PGAGAIN24TRIM 0x0000000F 0x0000
2163 R Pga6Regs_PGATYPE 0x0000000F 0x0000
2164 R Pga7Regs_PGACTL 0x0000000B 0x00000000
2165 R Pga7Regs_PGALOCK 0x0000000F 0x0000
2166 R Pga7Regs_PGAGAIN3TRIM 0x0000000F 0x0000
2167 R Pga7Regs_PGAGAIN6TRIM 0x0000000F 0x0000
2168 R Pga7Regs_PGAGAIN12TRIM 0x0000000F 0x0000
2169 R Pga7Regs_PGAGAIN24TRIM 0x0000000F 0x0000
2170 R Pga7Regs_PGATYPE 0x0000000F 0x0000
2171 R PieCtrlRegs_PIECTRL 0x0000000F 0x0DE1
2172 R PieCtrlRegs_PIEACK 0x0000000F 0x0000
2173 R PieCtrlRegs_PIEIER1 0x0000000F 0x0041
2174 R PieCtrlRegs_PIEIFR1 0x0000000F 0x0000
2175 R PieCtrlRegs_PIEIER2 0x0000000F 0x0000
2176 R PieCtrlRegs_PIEIFR2 0x0000000F 0x0000
2177 R PieCtrlRegs_PIEIER3 0x0000000F 0x0000
2178 R PieCtrlRegs_PIEIFR3 0x0000000F 0x0042
2179 R PieCtrlRegs_PIEIER4 0x0000000F 0x0001
2180 R PieCtrlRegs_PIEIFR4 0x0000000F 0x0000
2181 R PieCtrlRegs_PIEIER5 0x0000000F 0x0000
2182 R PieCtrlRegs_PIEIFR5 0x0000000F 0x0000
2183 R PieCtrlRegs_PIEIER6 0x0000000F 0x0000
2184 R PieCtrlRegs_PIEIFR6 0x0000000F 0x0000
2185 R PieCtrlRegs_PIEIER7 0x0000000F 0x0000
2186 R PieCtrlRegs_PIEIFR7 0x0000000F 0x0000
2187 R PieCtrlRegs_PIEIER8 0x0000000F 0x0000
2188 R PieCtrlRegs_PIEIFR8 0x0000000F 0x0000
2189 R PieCtrlRegs_PIEIER9 0x0000000F 0x0000
2190 R PieCtrlRegs_PIEIFR9 0x0000000F 0x0000
2191 R PieCtrlRegs_PIEIER10 0x0000000F 0x0000
2192 R PieCtrlRegs_PIEIFR10 0x0000000F 0x0000
2193 R PieCtrlRegs_PIEIER11 0x0000000F 0x0001
2194 R PieCtrlRegs_PIEIFR11 0x0000000F 0x0080
2195 R PieCtrlRegs_PIEIER12 0x0000000F 0x0000
2196 R PieCtrlRegs_PIEIFR12 0x0000000F 0x0000
2197 R PmbusaRegs_PMBMC 0x0000000B 0x00000000
2198 R PmbusaRegs_PMBTXBUF 0x0000000B 0x00000000
2199 R PmbusaRegs_PMBRXBUF 0x0000000B 0x00000000
2200 R PmbusaRegs_PMBACK 0x0000000B 0x00000000
2201 R PmbusaRegs_PMBSTS 0x0000000B 0x00340000
2202 R PmbusaRegs_PMBINTM 0x0000000B 0x000003D3
2203 R PmbusaRegs_PMBSC 0x0000000B 0x0060FF6A
2204 R PmbusaRegs_PMBHSA 0x0000000B 0x00000000
2205 R PmbusaRegs_PMBCTRL 0x0000000B 0x02200000
2206 R PmbusaRegs_PMBTIMCTL 0x0000000B 0x00000001
2207 R PmbusaRegs_PMBTIMCLK 0x0000000B 0x00C40061
2208 R PmbusaRegs_PMBTIMSTSETUP 0x0000000B 0x0000005E
2209 R PmbusaRegs_PMBTIMBIDLE 0x0000000B 0x000003E7
2210 R PmbusaRegs_PMBTIMLOWTIMOUT 0x0000000B 0x000AAE5F

2211 R PmbusaRegs_PMBTIMHIGHTIMOUT 0x0000000B 0x000003E7
2212 R RomPrefetchRegs_ROMPREFETCH 0x0000000B 0x00000000
2213 R RomWaitStateRegs_ROMWAITSTATE 0x0000000B 0x00000000
2214 R SciaRegs_SCICCR 0x0000000F 0x0000
2215 R SciaRegs_SCICTL1 0x0000000F 0x0000
2216 R SciaRegs_SCIHBAUD 0x0000000F 0x0000
2217 R SciaRegs_SCILBAUD 0x0000000F 0x0000
2218 R SciaRegs_SCICTL2 0x0000000F 0x00C0
2219 R SciaRegs_SCIRXST 0x0000000F 0x0000
2220 R SciaRegs_SCIRXEMU 0x0000000F 0x0000
2221 R SciaRegs_SCIRXBUF 0x0000000F 0x0000
2222 R SciaRegs_SCITXBUF 0x0000000F 0x0000
2223 R SciaRegs_SCIFFTX 0x0000000F 0xA000
2224 R SciaRegs_SCIFFRX 0x0000000F 0x201F
2225 R SciaRegs_SCIFFCT 0x0000000F 0x0000
2226 R SciaRegs_SCIPRI 0x0000000F 0x0000
2227 R ScibRegs_SCICCR 0x0000000F 0x0000
2228 R ScibRegs_SCICTL1 0x0000000F 0x0000
2229 R ScibRegs_SCIHBAUD 0x0000000F 0x0000
2230 R ScibRegs_SCILBAUD 0x0000000F 0x0000
2231 R ScibRegs_SCICTL2 0x0000000F 0x00C0
2232 R ScibRegs_SCIRXST 0x0000000F 0x0000
2233 R ScibRegs_SCIRXEMU 0x0000000F 0x0000
2234 R ScibRegs_SCIRXBUF 0x0000000F 0x0000
2235 R ScibRegs_SCITXBUF 0x0000000F 0x0000
2236 R ScibRegs_SCIFFTX 0x0000000F 0xA000
2237 R ScibRegs_SCIFFRX 0x0000000F 0x201F
2238 R ScibRegs_SCIFFCT 0x0000000F 0x0000
2239 R ScibRegs_SCIPRI 0x0000000F 0x0000
2240 R Sdfm1Regs_SDIFLG 0x0000000B 0x00000000
2241 R Sdfm1Regs_SDIFLGCLR 0x0000000B 0x00000000
2242 R Sdfm1Regs_SDCTL 0x0000000F 0x0000
2243 R Sdfm1Regs_SDMFILEN 0x0000000F 0x0000
2244 R Sdfm1Regs_SDSTATUS 0x0000000F 0x0000
2245 R Sdfm1Regs_SDCTLPARM1 0x0000000F 0x0000
2246 R Sdfm1Regs_SDDFPARM1 0x0000000F 0x0000
2247 R Sdfm1Regs_SDDPARM1 0x0000000F 0x0000
2248 R Sdfm1Regs_SDCMPH1 0x0000000F 0x7FFF
2249 R Sdfm1Regs_SDCMPL1 0x0000000F 0x0000
2250 R Sdfm1Regs_SDCPARM1 0x0000000F 0x0000
2251 R Sdfm1Regs_SDDATA1 0x0000000B 0x00000000
2252 R Sdfm1Regs_SDDATFIFO1 0x0000000B 0x00000000
2253 R Sdfm1Regs_SDCDATA1 0x0000000F 0x0000
2254 R Sdfm1Regs_SDCMPHZ1 0x0000000F 0x0000
2255 R Sdfm1Regs_SDFIFOCTL1 0x0000000F 0x0000
2256 R Sdfm1Regs_SDSYNC1 0x0000000F 0x0400
2257 R Sdfm1Regs_SDCTLPARM2 0x0000000F 0x0000
2258 R Sdfm1Regs_SDDFPARM2 0x0000000F 0x0000
2259 R Sdfm1Regs_SDDPARM2 0x0000000F 0x0000
2260 R Sdfm1Regs_SDCMPH2 0x0000000F 0x7FFF
2261 R Sdfm1Regs_SDCMPL2 0x0000000F 0x0000
2262 R Sdfm1Regs_SDCPARM2 0x0000000F 0x0000
2263 R Sdfm1Regs_SDDATA2 0x0000000B 0x00000000
2264 R Sdfm1Regs_SDDATFIFO2 0x0000000B 0x00000000
2265 R Sdfm1Regs_SDCDATA2 0x0000000F 0x0000
2266 R Sdfm1Regs_SDCMPHZ2 0x0000000F 0x0000
2267 R Sdfm1Regs_SDFIFOCTL2 0x0000000F 0x0000
2268 R Sdfm1Regs_SDSYNC2 0x0000000F 0x0400
2269 R Sdfm1Regs_SDCTLPARM3 0x0000000F 0x0000
2270 R Sdfm1Regs_SDDFPARM3 0x0000000F 0x0000
2271 R Sdfm1Regs_SDDPARM3 0x0000000F 0x0000
2272 R Sdfm1Regs_SDCMPH3 0x0000000F 0x7FFF
2273 R Sdfm1Regs_SDCMPL3 0x0000000F 0x0000
2274 R Sdfm1Regs_SDCPARM3 0x0000000F 0x0000
2275 R Sdfm1Regs_SDDATA3 0x0000000B 0x00000000

2276 R Sdfm1Regs_SDDATFIFO3 0x0000000B 0x00000000
2277 R Sdfm1Regs_SDCDATA3 0x0000000F 0x0000
2278 R Sdfm1Regs_SDCMPHZ3 0x0000000F 0x0000
2279 R Sdfm1Regs_SDFIFOCTL3 0x0000000F 0x0000
2280 R Sdfm1Regs_SDSYNC3 0x0000000F 0x0400
2281 R Sdfm1Regs_SDCTLPARM4 0x0000000F 0x0000
2282 R Sdfm1Regs_SDDFPARM4 0x0000000F 0x0000
2283 R Sdfm1Regs_SDDPARM4 0x0000000F 0x0000
2284 R Sdfm1Regs_SDCMPH4 0x0000000F 0x7FFF
2285 R Sdfm1Regs_SDCMPL4 0x0000000F 0x0000
2286 R Sdfm1Regs_SDCPARM4 0x0000000F 0x0000
2287 R Sdfm1Regs_SDDATA4 0x0000000B 0x00000000
2288 R Sdfm1Regs_SDDATFIFO4 0x0000000B 0x00000000
2289 R Sdfm1Regs_SDCDATA4 0x0000000F 0x0000
2290 R Sdfm1Regs_SDCMPHZ4 0x0000000F 0x0000
2291 R Sdfm1Regs_SDFIFOCTL4 0x0000000F 0x0000
2292 R Sdfm1Regs_SDSYNC4 0x0000000F 0x0400
2293 R SpiaRegs_SPICCR 0x0000000F 0x0000
2294 R SpiaRegs_SPICTL 0x0000000F 0x0000
2295 R SpiaRegs_SPISTS 0x0000000F 0x0000
2296 R SpiaRegs_SPIBRR 0x0000000F 0x0000
2297 R SpiaRegs_SPIRXEMU 0x0000000F 0x0000
2298 R SpiaRegs_SPIRXBUF 0x0000000F 0x0000
2299 R SpiaRegs_SPITXBUF 0x0000000F 0x0000
2300 R SpiaRegs_SPIDAT 0x0000000F 0x0000
2301 R SpiaRegs_SPIFFTX 0x0000000F 0xA000
2302 R SpiaRegs_SPIFFRX 0x0000000F 0x201F
2303 R SpiaRegs_SPIFFCT 0x0000000F 0x0000
2304 R SpiaRegs_SPIPRI 0x0000000F 0x0000
2305 R SpibRegs_SPICCR 0x0000000F 0x0000
2306 R SpibRegs_SPICTL 0x0000000F 0x0000
2307 R SpibRegs_SPISTS 0x0000000F 0x0000
2308 R SpibRegs_SPIBRR 0x0000000F 0x0000
2309 R SpibRegs_SPIRXEMU 0x0000000F 0x0000
2310 R SpibRegs_SPIRXBUF 0x0000000F 0x0000
2311 R SpibRegs_SPITXBUF 0x0000000F 0x0000
2312 R SpibRegs_SPIDAT 0x0000000F 0x0000
2313 R SpibRegs_SPIFFTX 0x0000000F 0xA000
2314 R SpibRegs_SPIFFRX 0x0000000F 0x201F
2315 R SpibRegs_SPIFFCT 0x0000000F 0x0000
2316 R SpibRegs_SPIPRI 0x0000000F 0x0000
2317 R SyncSocRegs_SYNCSELECT 0x0000000B 0xE003FFFF
2318 R SyncSocRegs_ADCSOCOUTSELECT 0x0000000B 0x00000000
2319 R SyncSocRegs_SYNCSOCLOCK 0x0000000B 0x00000000
2320 R WdRegs_SCSR 0x0000000F 0x0005
2321 R WdRegs_WDCNTR 0x0000000F 0x002C
2322 R WdRegs_WDKEY 0x0000000F 0x0040
2323 R WdRegs_WDCR 0x0000000F 0x0040
2324 R WdRegs_WDWCR 0x0000000F 0x0000
2325 R XbarRegs_XBARFLG1 0x0000000B 0x00000000
2326 R XbarRegs_XBARFLG2 0x0000000B 0x0000FF3F
2327 R XbarRegs_XBARFLG3 0x0000000B 0x00000000
2328 R XbarRegs_XBARFLG4 0x0000000B 0x00000000
2329 R XbarRegs_XBARCLR1 0x0000000B 0x00000000
2330 R XbarRegs_XBARCLR2 0x0000000B 0x00000000
2331 R XbarRegs_XBARCLR3 0x0000000B 0x00000000
2332 R XbarRegs_XBARCLR4 0x0000000B 0x00000000
2333 R XintRegs_XINT1CR 0x0000000F 0x0000
2334 R XintRegs_XINT2CR 0x0000000F 0x0000
2335 R XintRegs_XINT3CR 0x0000000F 0x0000
2336 R XintRegs_XINT4CR 0x0000000F 0x0000
2337 R XintRegs_XINT5CR 0x0000000F 0x0000
2338 R XintRegs_XINT1CTR 0x0000000F 0x0000
2339 R XintRegs_XINT2CTR 0x0000000F 0x0000
2340 R XintRegs_XINT3CTR 0x0000000F 0x0000