

Chip-out limits for WLP FETs

7.7.6 Front side chipping rules

Symbol	Chip out Location	Accept Specification
G	Front side Chip out Length	< 25% of die length (L)
H	Front side Chip out Depth	< 25% of die thickness
J	Front side Chip out Width	< 25% of die width (W)
K	Front side Chip out from Die Edge	<20um
M	Front side Corner Chip out	<20um

Chipping on front side

