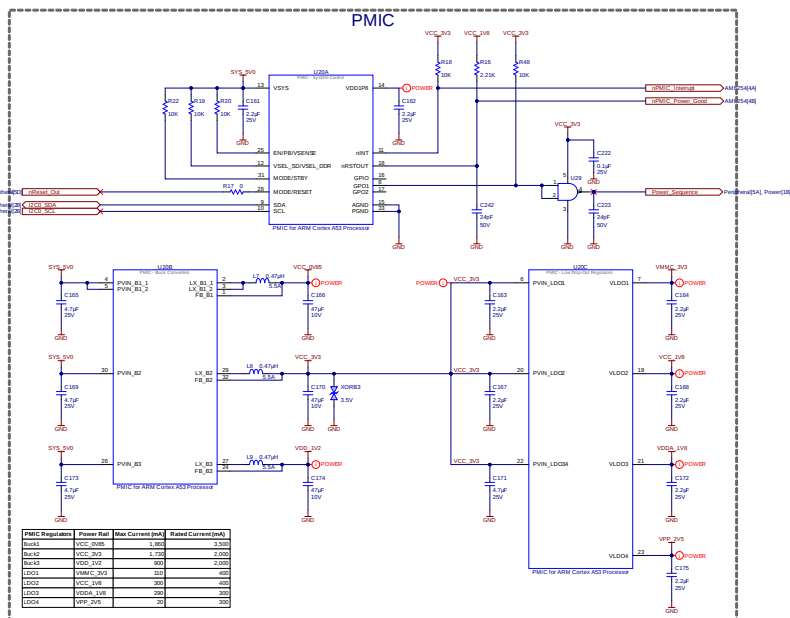




PMIC Regulators	Power Rail	Max Current (mA)	Rated Current (mA)
Buck1	VCC_0V18	1,860	3,500
Buck2	VCC_3V3	1,730	2,000
Buck3	VDD_1V2	900	2,000
LDO1	VMM_C_3V3	110	400
LDO2	VCC_1V8	200	400
LDO3	VDDA_1V8	290	300
LDO4	VPP_2V5	20	300

[illegible]

Notes

VSEL_SD/VSEL_DOR pin of the PMIC is pulled up-configuring LDO1 in Bypass mode, providing 3.3V for eMMC NAND Core.

Title Power Platform PP1 - Power			
Project 90033201 XL HD AM6254 Logic Board		Document Rev 1.0 of 1 Doc	
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Drawn by: KG	Drawn by Date: 1/23/2024	Checked by: SK	Checked by Date: *