

Network Switch

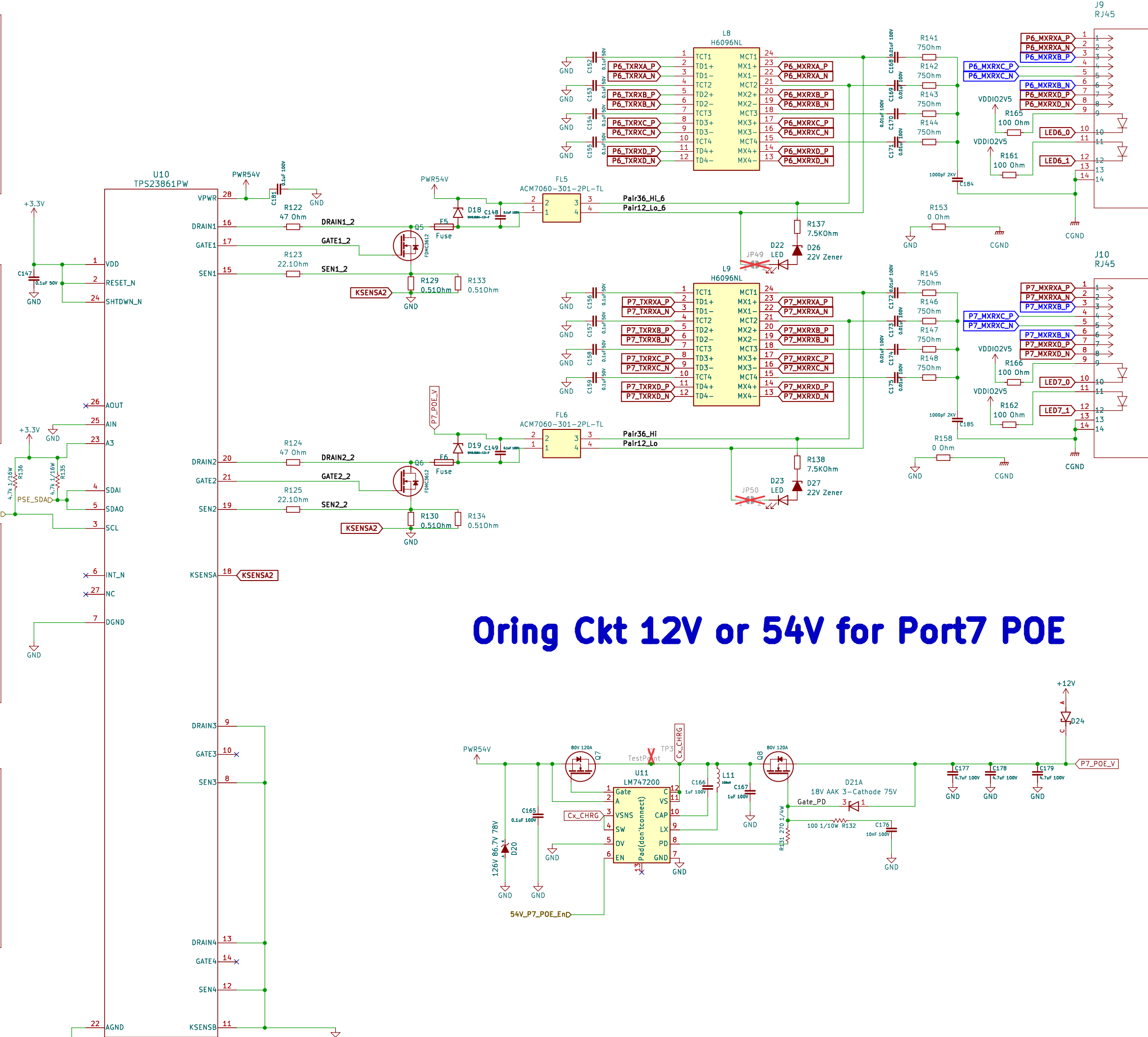
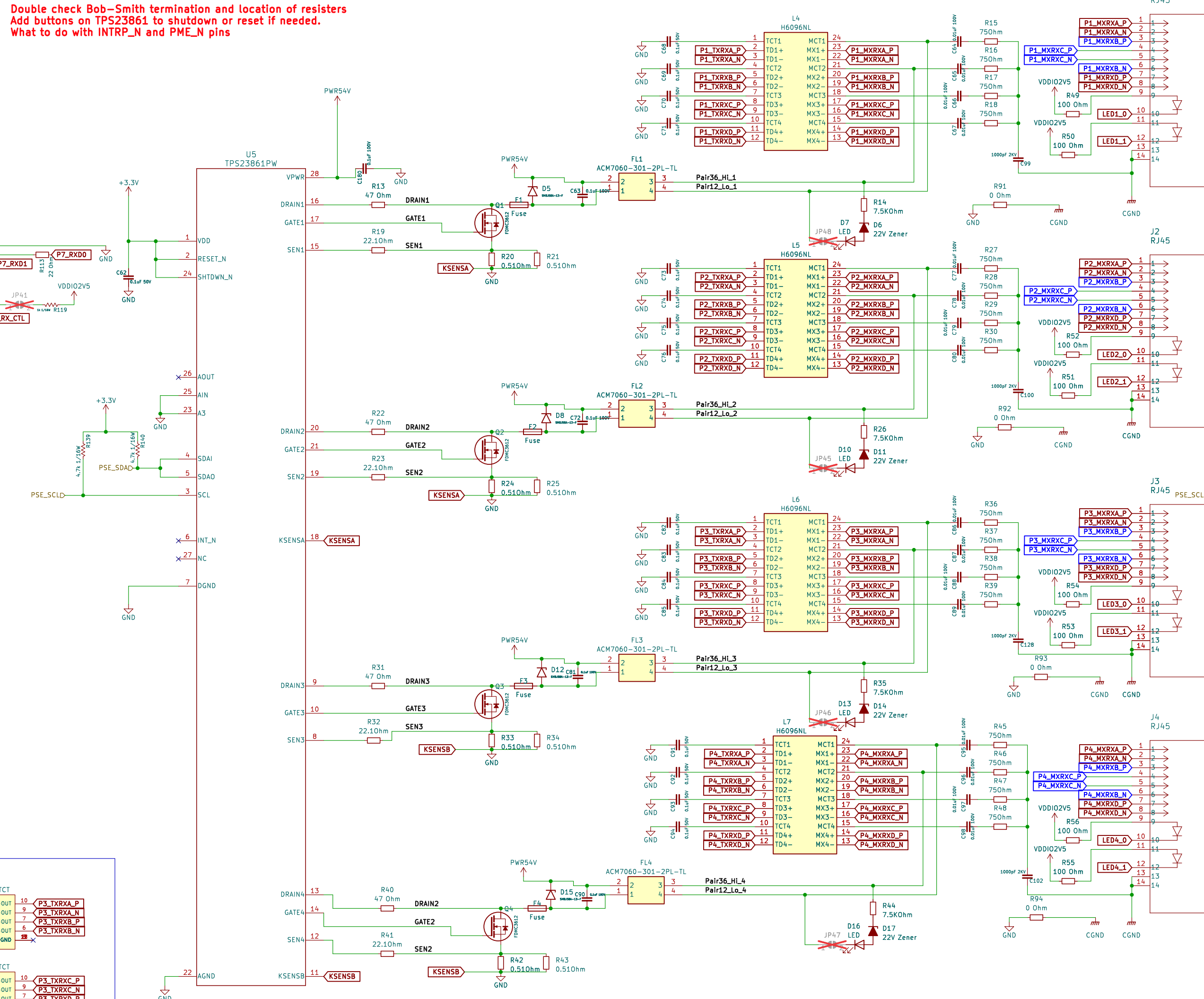
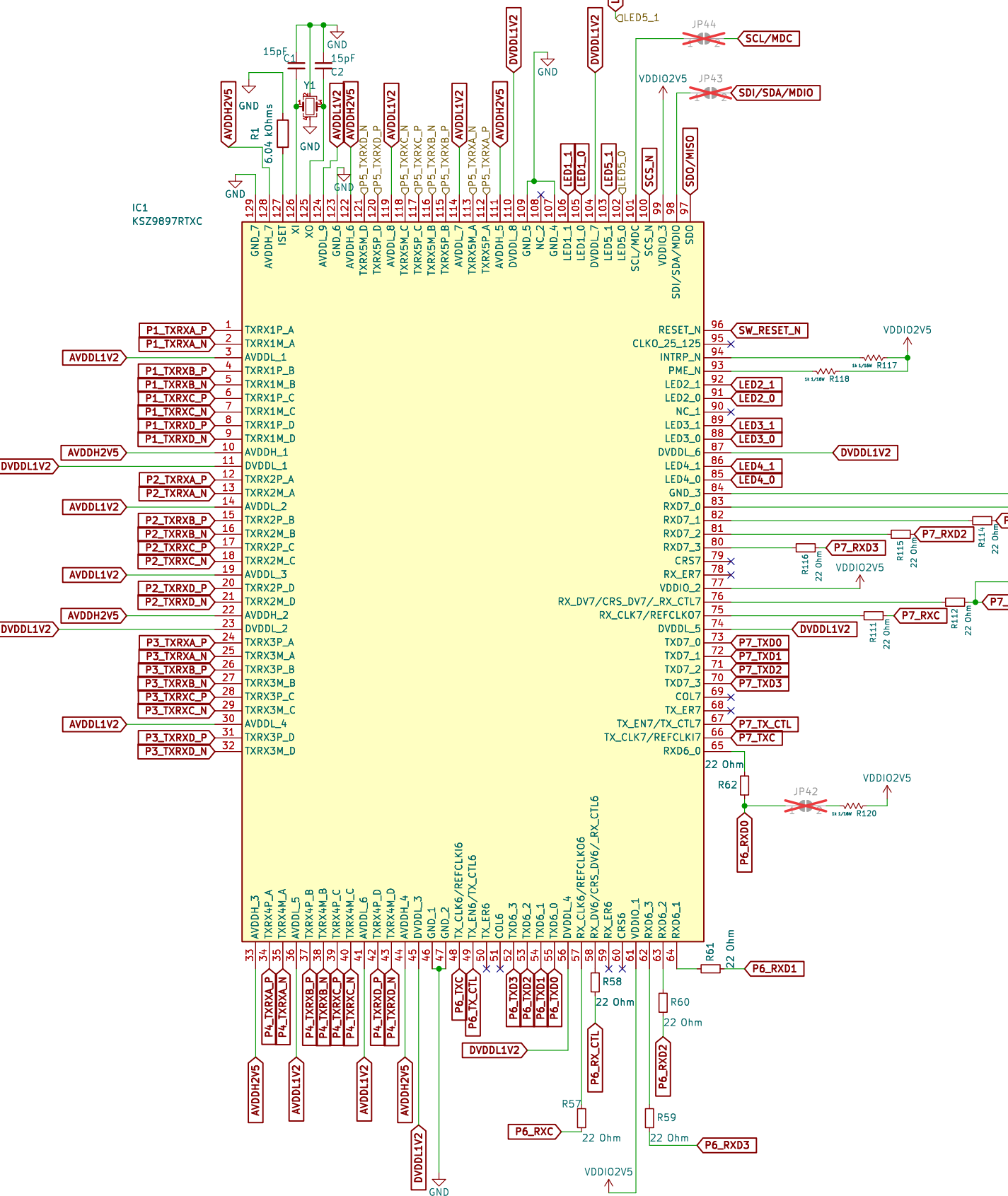
PSE (4 Port)

PSE (2 Port)

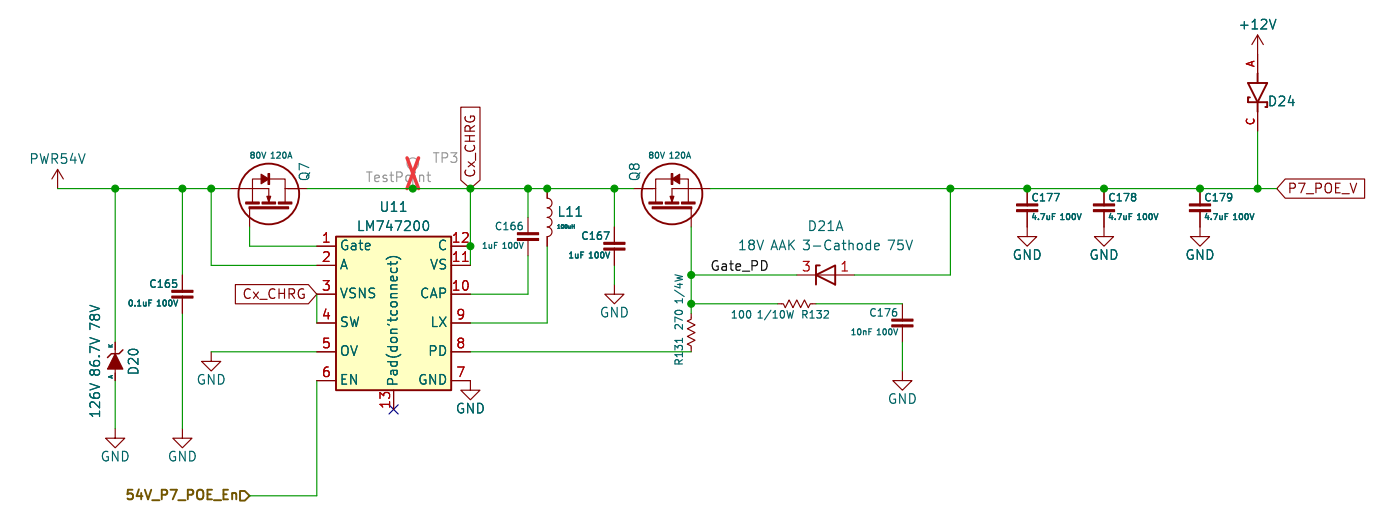
Add jumpers for SPI and other config
Check if unused pins need to be grounded or pulled up
Test points at various locations

TXC must be routed for additional 1.2ns or 1.5ns delay. Or setting bit4 in register 0x6301

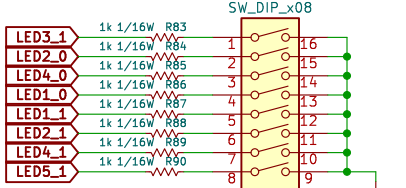
Double check Bob-Smith termination and location of resistors
Add buttons on TPS23861 to shutdown or reset if needed.
What to do with INTRP_N and PME_N pins



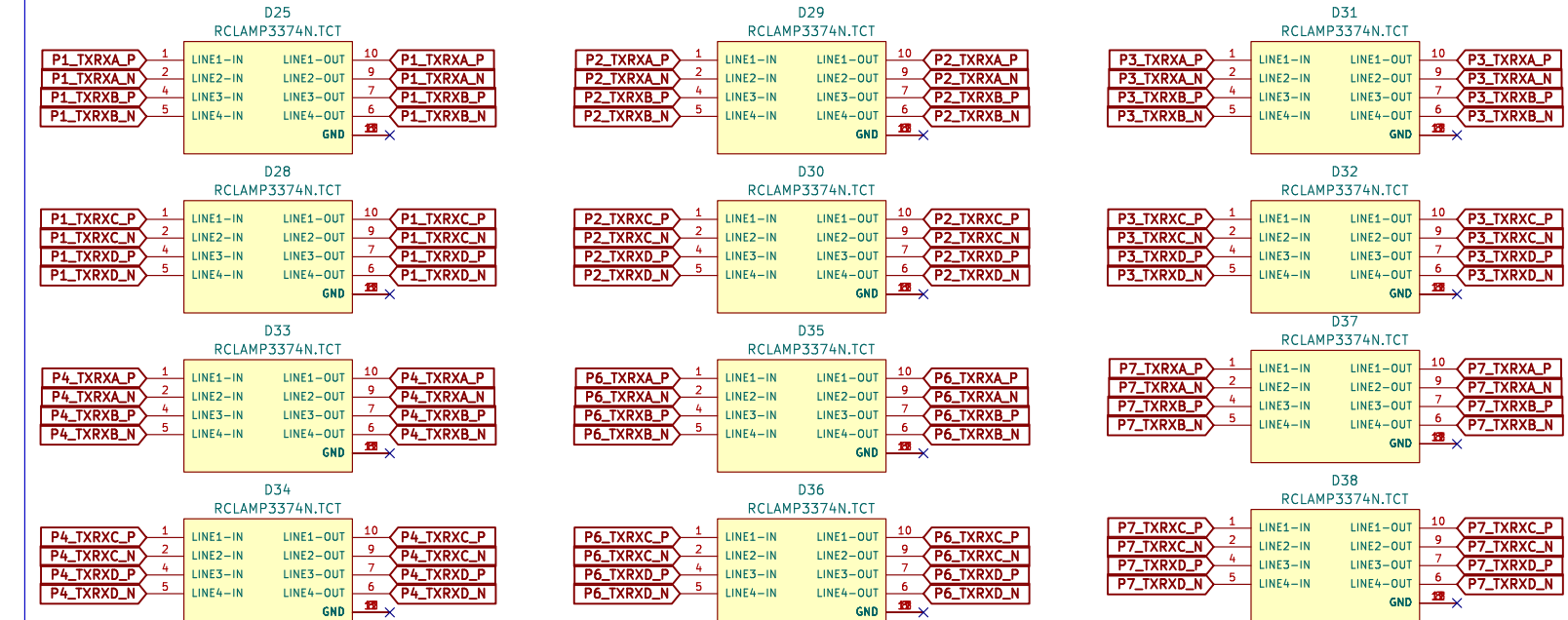
Oring Ckt 12V or 54V for Port7 P0E



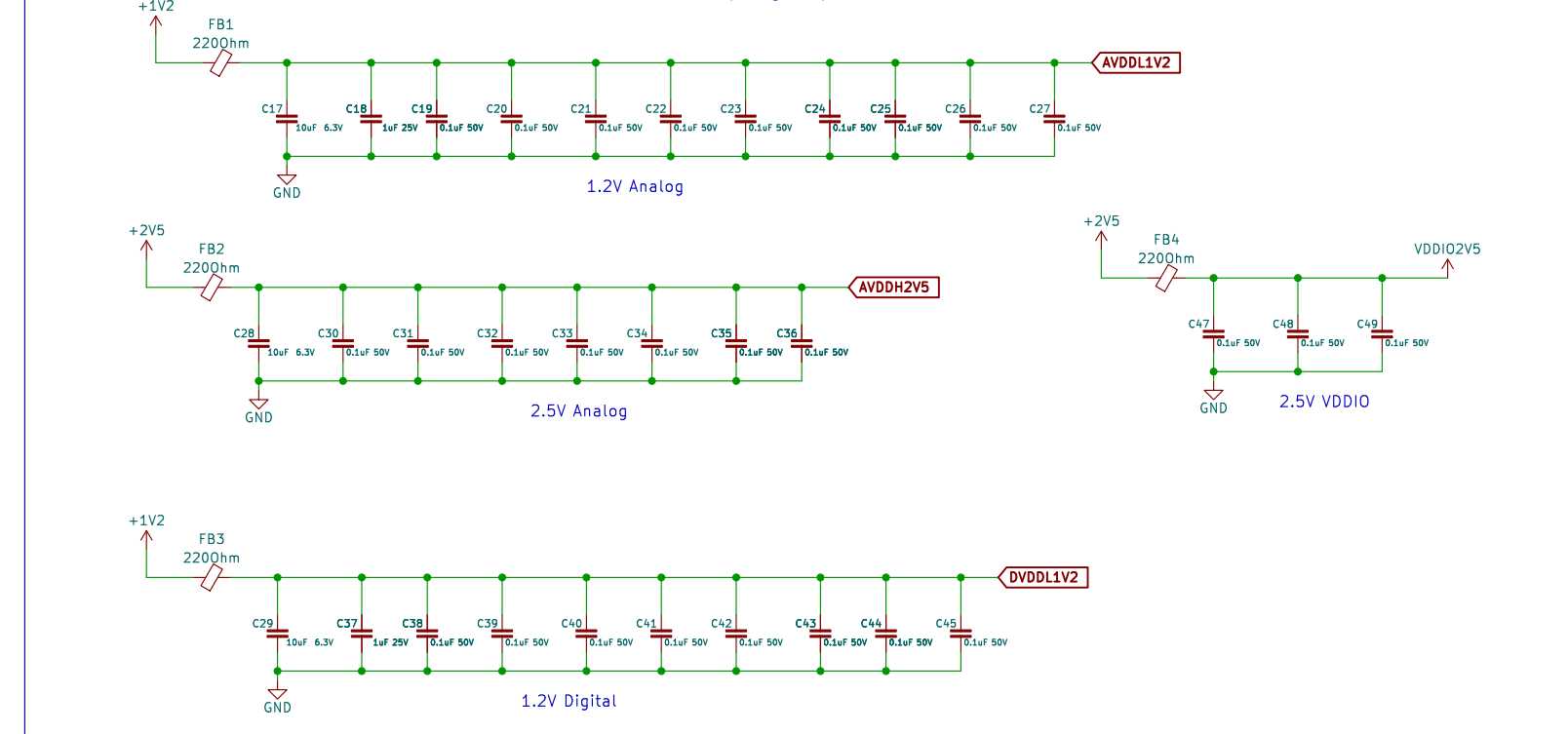
Strapping options for KSZ9897



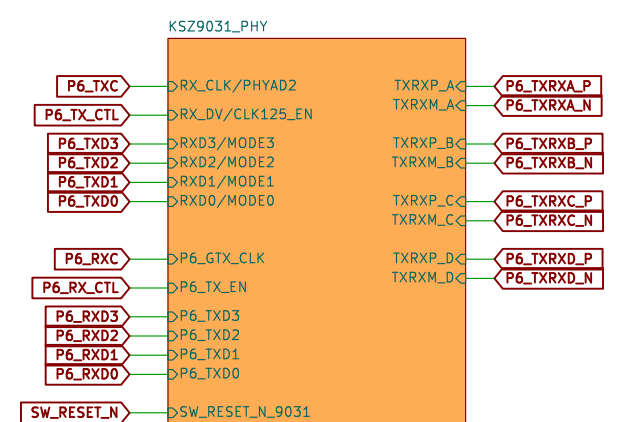
ESD Protection between transformer choke and KSZ9897



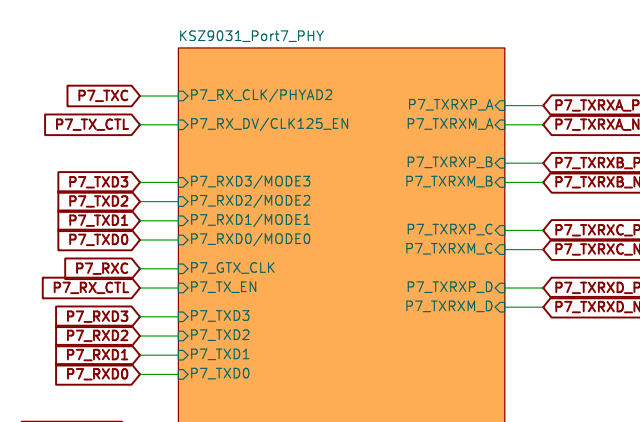
Decoupling Capacitors



Port6 of KSZ9897 to PHY KSZ9031



Port7 of KSZ9897 to 2nd PHY KSZ9031



Do both the 9031 PHYs connect to the same SCL/MDC and SDI/SDA/MDIO pins on KSZ9897