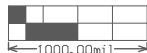
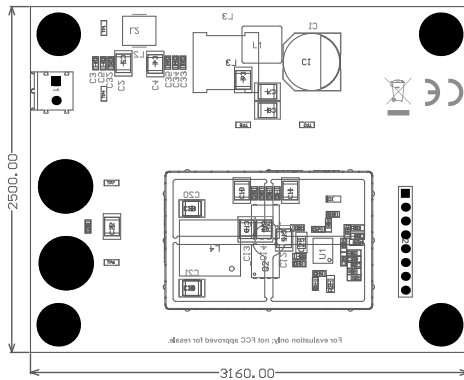


Layer	Name	Material	Thickness	Constant	Board Layer	Stack
	Top Overlay					
	Top Solder	Solder Resist	0.40mil	3.5		
1	Top Layer1		2.80mil			
	Dielectric1	FR-4	6.00mil	4.2		
2	Layer2		2.80mil			
	Dielectric 3	FR4	38.00mil	4.2		
3	Layer3		2.80mil			
	Dielectric 2	FR4	6.00mil	4.2		
4	Bottom Layer4		2.80mil			
	Bottom Solder	Solder Resist	0.40mil	3.5		
	Bottom Overlay					

Z22 ■ These assemblies are ESD sensitive, ESD precautions shall be observed.

Z23 ■ These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

Z24 ■ These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED TO THE JOURNAL 'DNP' OF THE COMPONENTS
ASSEMBLY VARIANT: [No Variations] [Variations: 0]

Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

DESIGN INFORMATION

MIN. TRACK WIDTH: 8 MIL
MIN. CLEARANCE: 0.2 mm
MIN. VIA PAD SIZE: 24 MIL

MINIMUM ANNUAL RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

MATERIAL:
☐ FR-408 ☒ FR-4 High Tg ☐ OTHER _____
THICKNESS: ☒ 62 MIL (1.6mm) +/-10% ☐ OTHER _____
TOLERANCE: ☒ ANSI IPC-6012 TYPE 3 CLASS 2
☐ OTHER +/- _____
BOW & TWIST: ☒ ANSI IPC-6012 TYPE 3 CLASS 2
☐ OTHER +/- _____

DRILLING:
REFERENCE: ☒ AS SHOWN ☒ NC_DRILL FILES
PTH MIN COPPER THICKNESS: ☒ 1MIL ☐ OTHER _____

DRILL FINISH:
SILKSCREEN: ☒ TOP ☒ BOTTOM
SILKSCREEN COLOR: ☒ WHITE ☐ OTHER _____
SOLDER RESIST COLOR:
☒ GREEN ☐ BLUE ☐ OTHER _____

SURFACE FINISH: ☒ IMMERSION GOLD (ENIG) ☐ ENEPIG
☐ IMM. TIN/SILVER OR EQUIV ☐ OTHER _____

ARRAY/PANEL: ☐ CUT AND TRIM PER MECH LAYER 1
☐ N.C. ROUTE ☒ V. SCORE

CERTIFICATION: MATERIALS AND WORKSMANSHIP FOR ALL PCBs
TO MEET OR EXCEED THE REQUIREMENTS OF:
☒ ANSI IPC-A-600F CLASS -> ☐ 1 ☒ 2 ☐ 3
☒ UL 94V-0 ☒ RoHS ☐ OTHER _____ PER ORDER

ADDITIONAL REQUIREMENTS:
MICROSECTION: ☐ YES

BASE BOARD ELEC. TEST: ☐ NONE ☒ REQUIRED ☐ PER ORDER

MANUFACTURER'S UL: ☐ RAIL ☐ METAL ☒ SILK



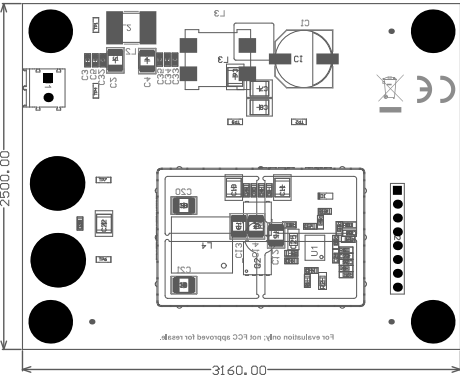

PROJECT TITLE:	LM5146-Q1-EVM12V
DESIGNED FOR:	Public Release
FILE NAME:	BSR049A.PcbDoc

ENGINEER: Tim Hegarty	LAYOUT BY: K Nielson/Tom Santiago
SCALE: 0.70	ALTUM DESIGNER VERSION: 25.2.1.25

[illegible]

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