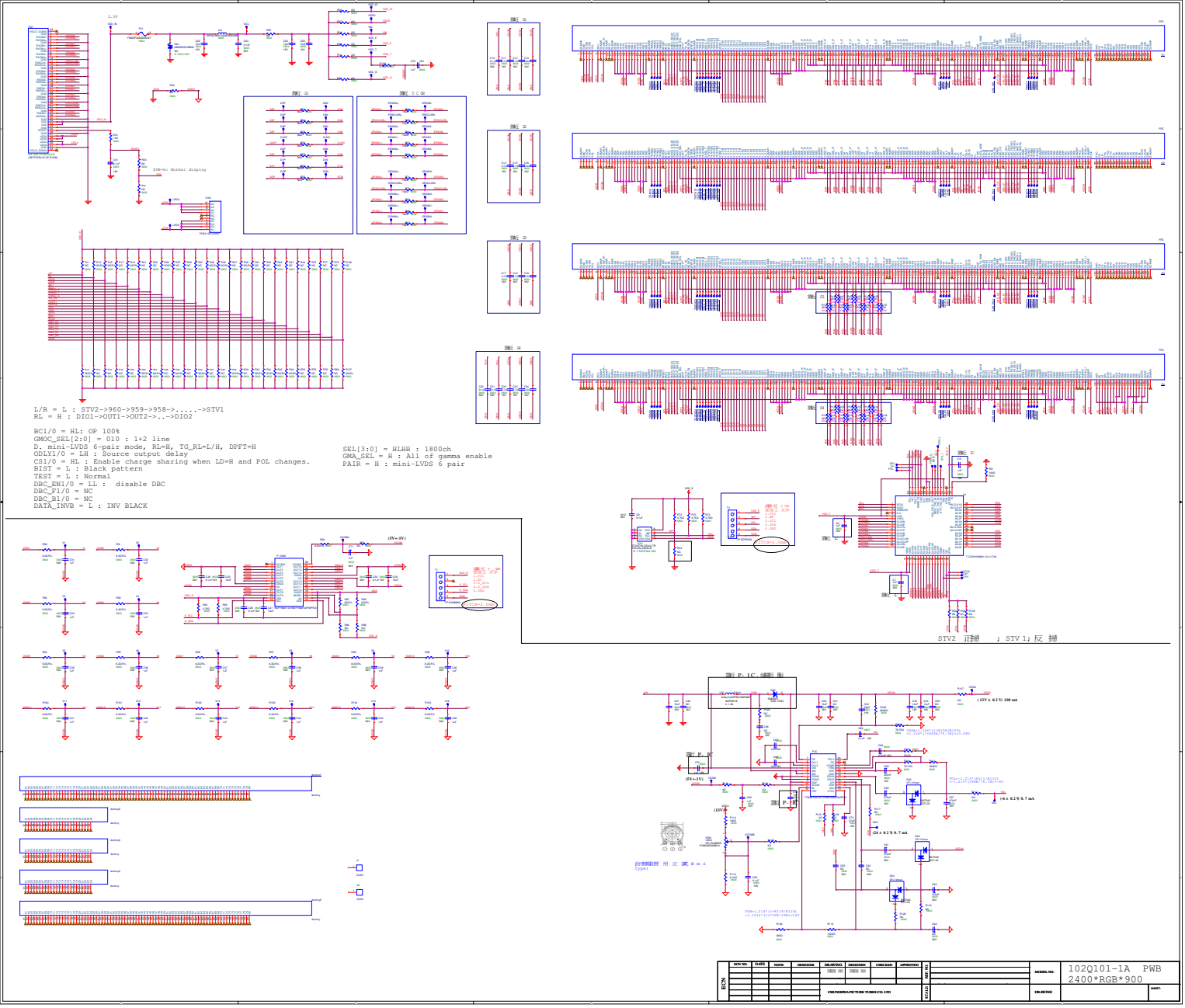
L/R = L : STV2->960->959->958->.....->STV1
RL = H : DIO1->OUT1->OUT2->..->DIO2
BC1/0 = HL: OP 100%
GMOC_SEL[2:0] = 010 : 1+2 line
D. mini-LVDS 6-pair mode, RL=H, TG_RL=L/H, DPFT=H
ODLY1/0 = LH : Source output delay
CS1/0 = HL : Enable charge sharing when LD=H and POL changes.
BIST = L : Black pattern
TEST = L : Normal
DBC_EN1/0 = LL : disable DBC
DBC_F1/0 = NC
DBC_B1/0 = NC
DATA_INVB = L : INV BLACK
SEL[3:0] = HLHH : 1800ch
GMA_SEL = H : All of gamma enable
PAIR = H : mini-LVDS 6 pair
STV2 正插 ; STV1反 插
(13V ± 0.2 V) 100 mA
(24 ± 0.2 V) 0.7 mA
(4.6 ± 0.2 V) 0.7 mA
(5V+-2V)
(13V)
(5V+-2V)
此 图 仅 用 于 工 厂 参 考 (Ref only)
ECN
REV NO.
DATE
NOTE
DESIGNER
DRAWING
DESIGNED
CHECKED
APPROVED
CHUNGHWA PICTURE TUBES CO.LTD
TITLE
MODEL NO.
102Q101-1A PWB
2400*RGB*900
DRAWING
SHEET