There was a problem that the synchronization frequency and SW shifted.

Curs1 X Pos ⓐ
-3.24µs

Curs2 X Pos ⓑ
-756.73ns

CH1 : 1V/div
CH2 : 5V/div
CH3 : 1V/div

CH1 : External sync signal
CH2 : SW
CH3 : trigger timing reset

Vin=24V Vout=5.5V