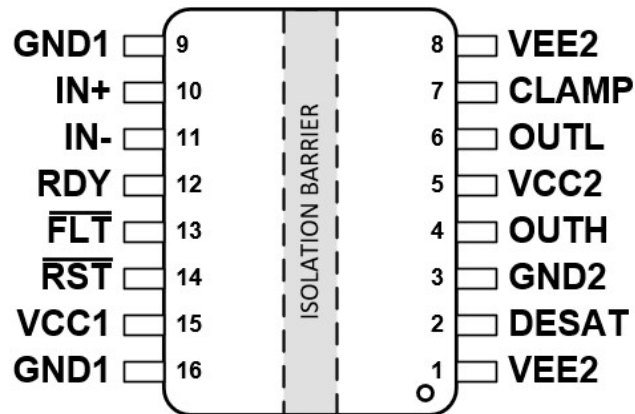
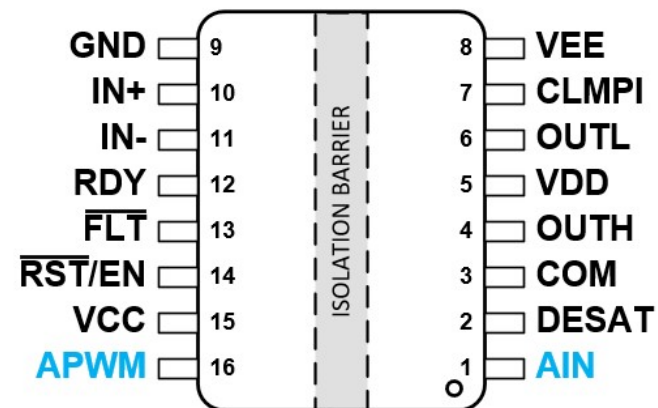


# Pinout Comparison – ISO5x52 and UCC21750/59

ISO5852S, ISO5452



UCC21750, UCC21759



The only layout changes to move from an ISO5x52 design to UCC21750 are

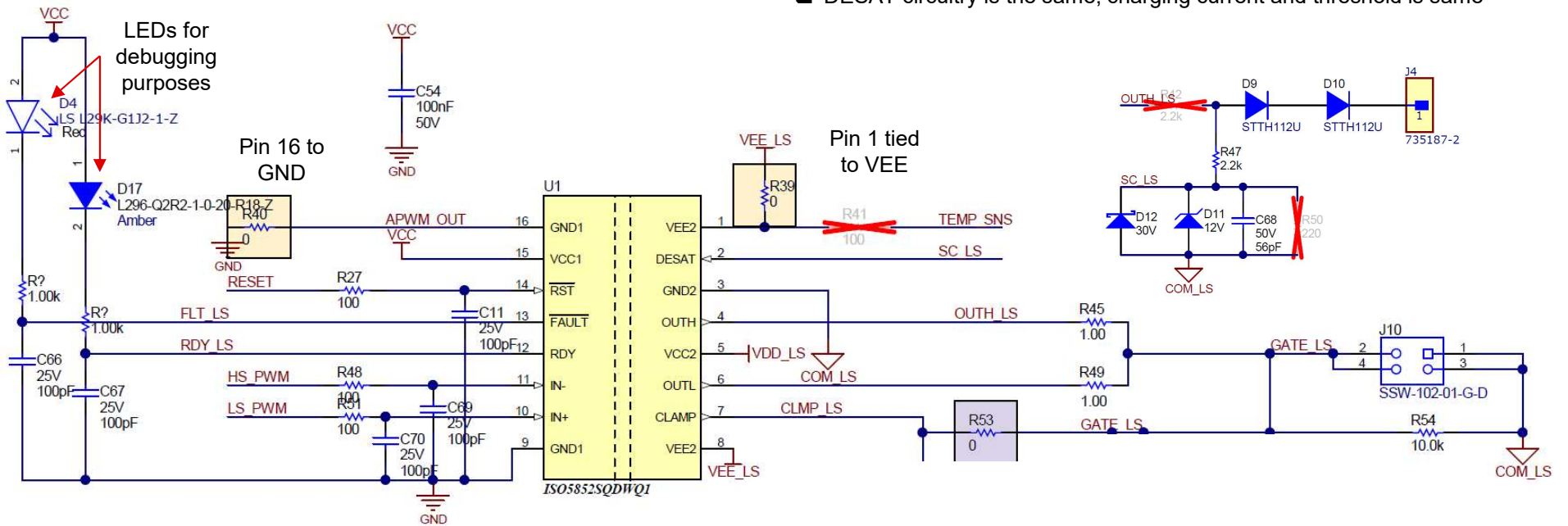
- Tie **AIN** to **COM**
- Float **APWM** pin

An ISO5852/ISO5452 design could most easily be adapted for UCC21750/9.

Jumpers (0 ohm resistors) can be used populated/not populated to tie pins 1 and 16 to respective nets.

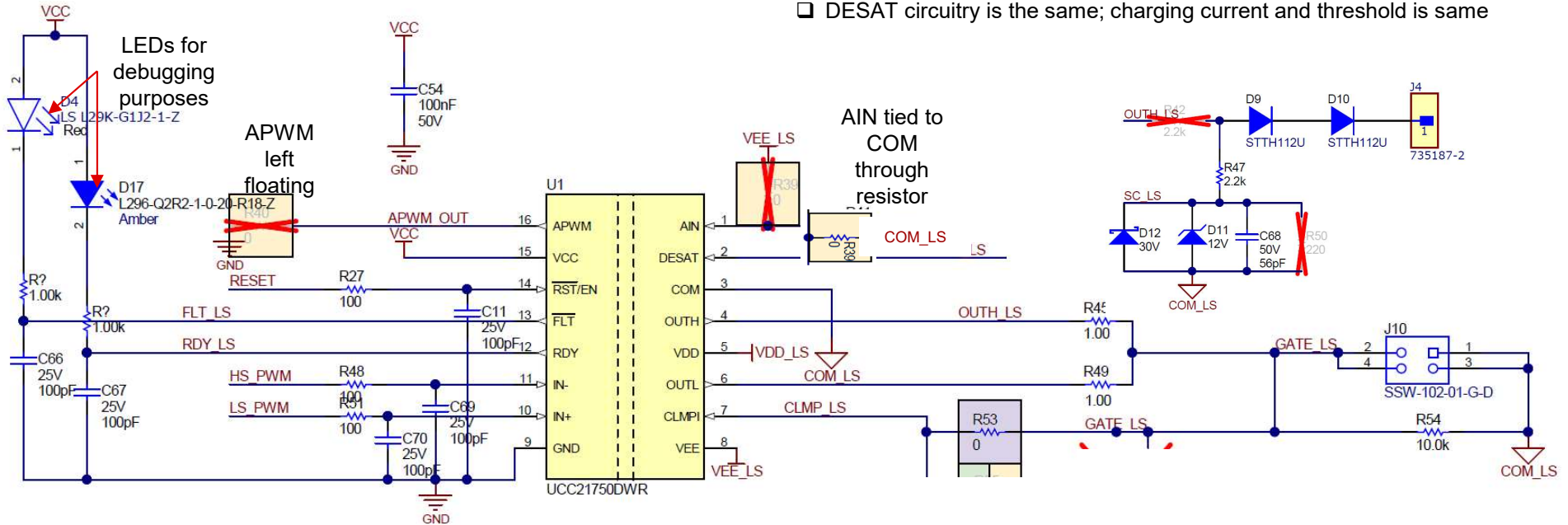
# ISO5852S Schematic

- ❑ The GND1 and VEE2 pins are tied to respective nets via 0ohm resistor
- ❑ DESAT circuitry is the same; charging current and threshold is same



# UCC21750 Schematic

- ❑ When not using AIN-to-APWM function, the AIN can be connected to COM, and the APWM left floating
- ❑ DESAT circuitry is the same; charging current and threshold is same



UCC21750 & ISO5852S schematics are extremely similar and it is easy to transition from a ISO5X5X to a UCC217XX design