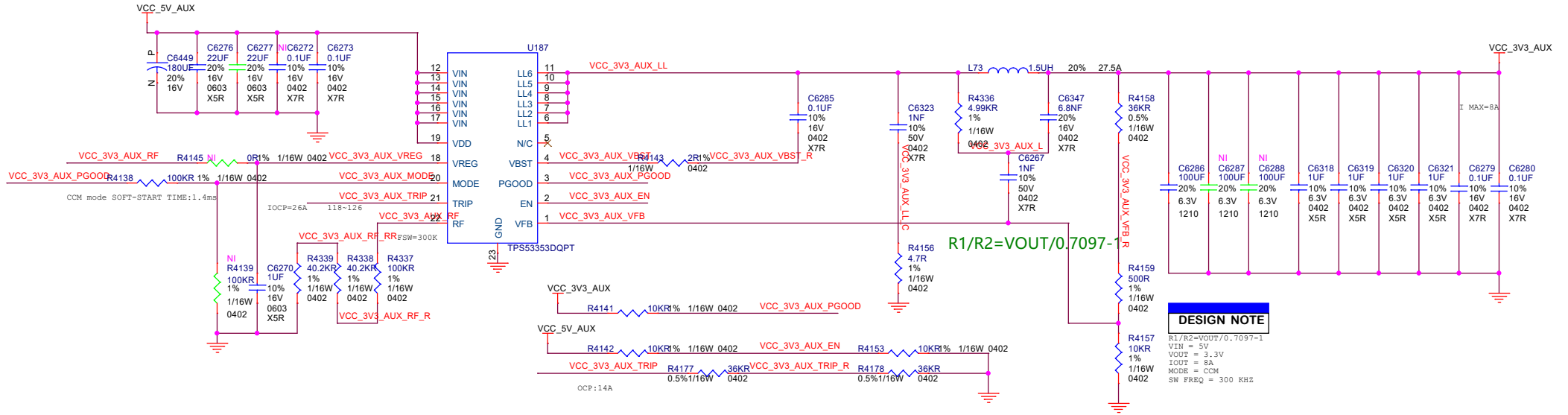
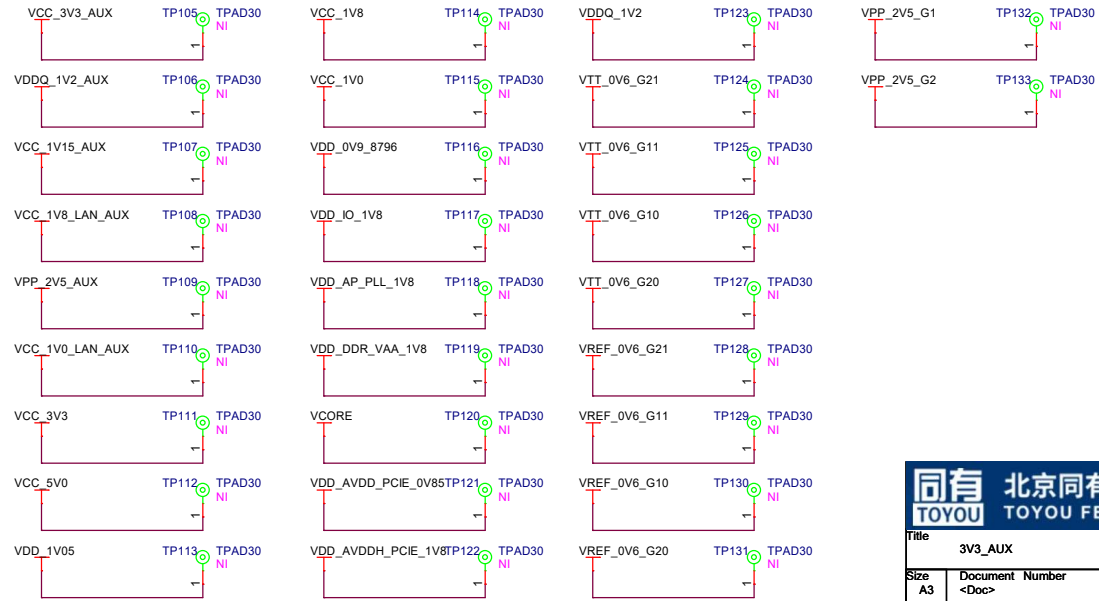


# 3V3\_AUX



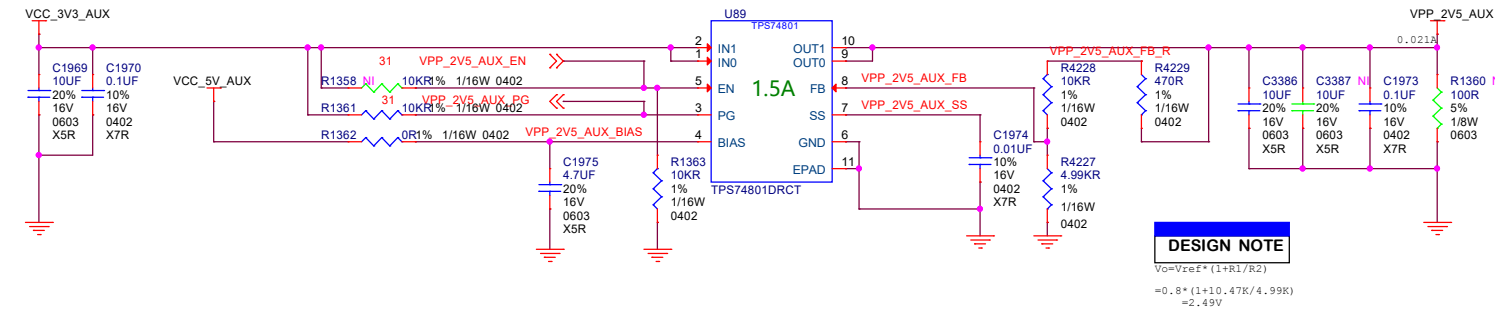
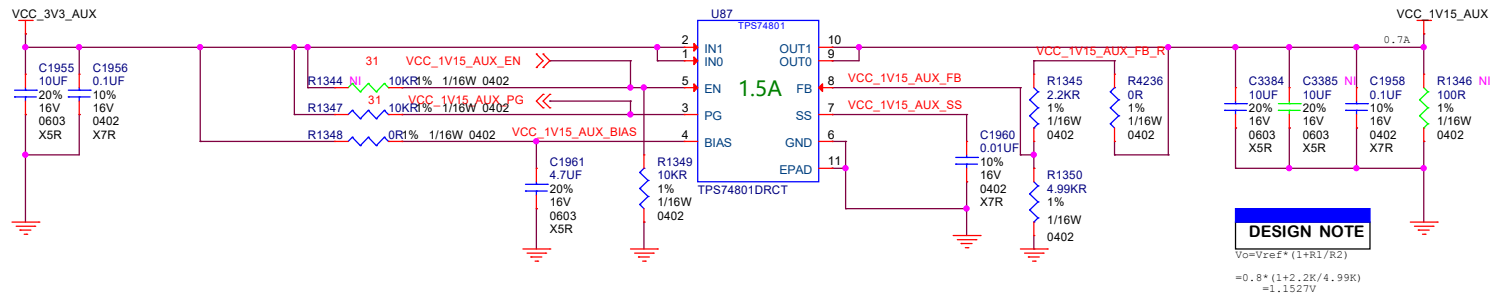
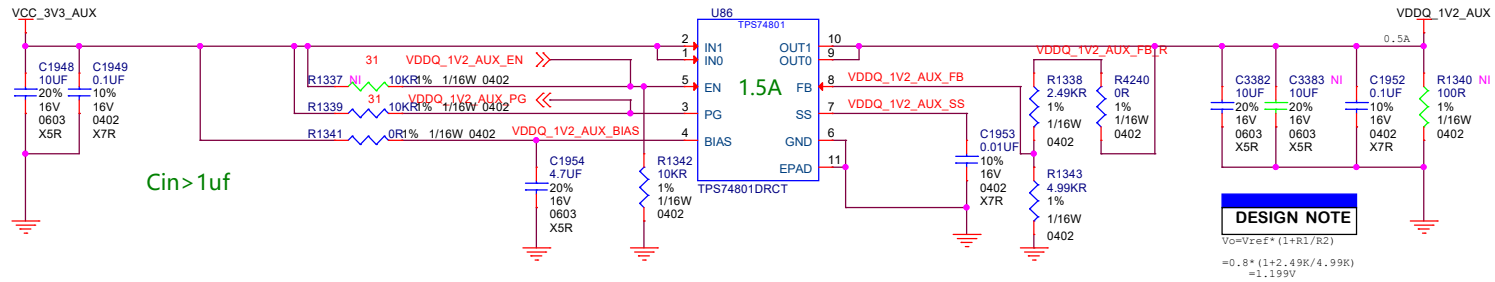
TP140	1	TPAD30	NI	C6455	0.22UF	10% 16V	0402	X7R	TPAD30	1	TP141	NI
TP142	1	TPAD30	NI	C6456	0.1UF	10% 16V	0402	X7R	TPAD30	1	TP143	NI
TP145	1	TPAD30	NI	C6457	0.01UF	16V	0402	X7R 10%	TPAD30	1	TP144	NI
TP147	1	TPAD30	NI	C6458	4.7UF	6.3V	0402	X5R 10%	TPAD30	1	TP146	NI
TP149	1	TPAD30	NI	C6460	22UF	20% 16V	0603	X5R	TPAD30	1	TP148	NI
TP151	1	TPAD30	NI	C6461	0.1UF	10% 6.3V	0201	X5R	TPAD30	1	TP150	NI
TP153	1	TPAD30	NI	C6462	0.01UF	10% 10V	0201	X5R	TPAD30	1	TP152	NI
TP155	1	TPAD30	NI	C6463	1UF	10% 6.3V	0402	X5R	TPAD30	1	TP154	NI
TP157	1	TPAD30	NI	C6464	10UF	20% 16V	0603	X5R	TPAD30	1	TP156	NI
TP159	1	TPAD30	NI	C6465	11NF	10% 50V	0402	X7R	TPAD30	1	TP158	NI
TP161	1	TPAD30	NI	C6466	1UF	10% 16V	0603	X5R	TPAD30	1	TP160	NI
TP163	1	TPAD30	NI	C6467	27PF	5% 50V	0402	COG	TPAD30	1	TP162	NI
TP164	1	TPAD30	NI	C6468	10PF	50V	0402	COG 5%	TPAD30	1	TP165	NI
TP167	1	TPAD30	NI	C6469	2200PF	50V	0402	X7R 10%	TPAD30	1	TP166	NI
TP168	1	TPAD30	NI	C6470	680PF	50V	0603	X7R 10%	TPAD30	1	TP169	NI
TP170	1	TPAD30	NI	C6471	100PF	5% 50V	0402	COG	TPAD30	1	TP171	NI
TP172	1	TPAD30	NI	C6472	33PF	5% 16V	0402	COG	TPAD30	1	TP173	NI
TP175	1	TPAD30	NI	C6473	18PF	5% 50V	0402	COG	TPAD30	1	TP174	NI
TP176	1	TPAD30	NI	C6474	4.7UF	20% 16V	0603	X5R	TPAD30	1	TP177	NI
TP178	1	TPAD30	NI	C6475	8.2NF	20% 16V	0402	X7R	TPAD30	1	TP179	NI
TP181	1	TPAD30	NI	C6476	470PF	5% 50V	0402	COG	TPAD30	1	TP180	NI
TP183	1	TPAD30	NI	C6477	100UF	20% 6.3V	1210		TPAD30	1	TP182	NI
TP185	1	TPAD30	NI	C6478	1000PF	10% 2KV	1206	X7R	TPAD30	1	TP184	NI
TP187	1	TPAD30	NI	C6480	6.8NF	20% 16V	0402	X7R	TPAD30	1	TP186	NI



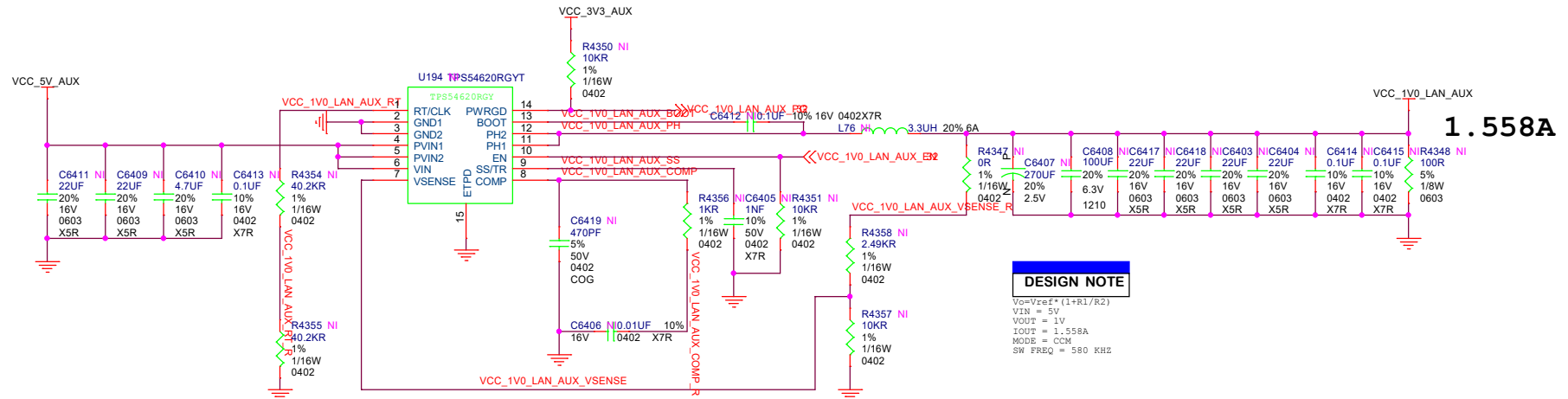
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# 1V2/1V15/2V5AUX



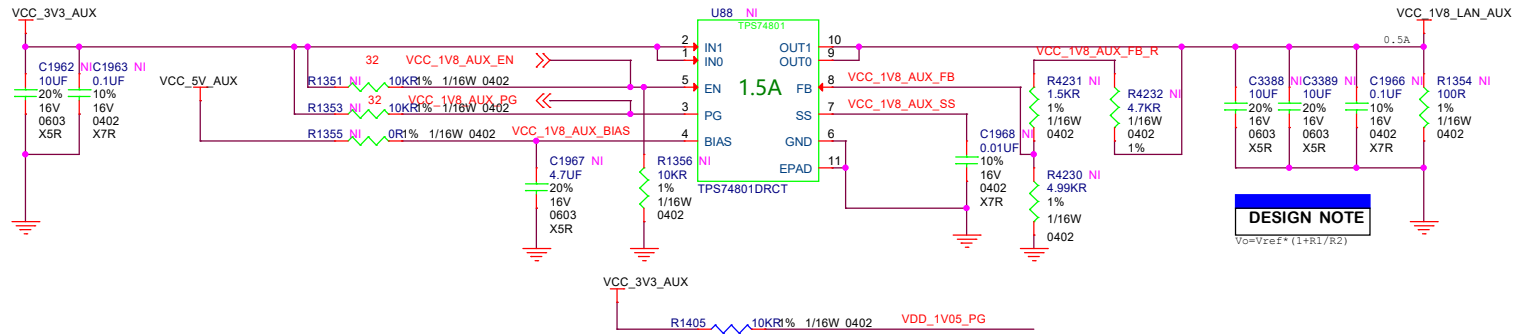
# I350\_1V0/1V8/VDD\_1V05



**1.558A**

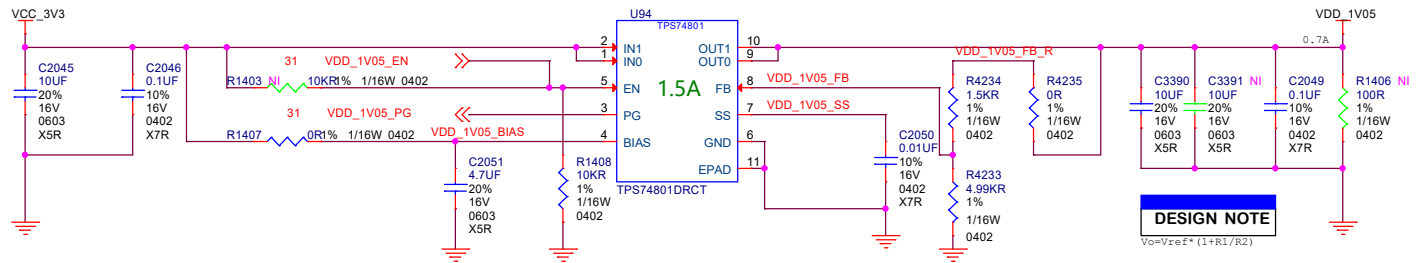
**DESIGN NOTE**

$V_o = V_{ref} * (1 + R1/R2)$   
 $V_{in} = 5V$   
 $V_{out} = 1V$   
 $I_{out} = 1.558A$   
 $MODE = CCM$   
 $SW FREQ = 580 KHZ$



**DESIGN NOTE**

$V_o = V_{ref} * (1 + R1/R2)$



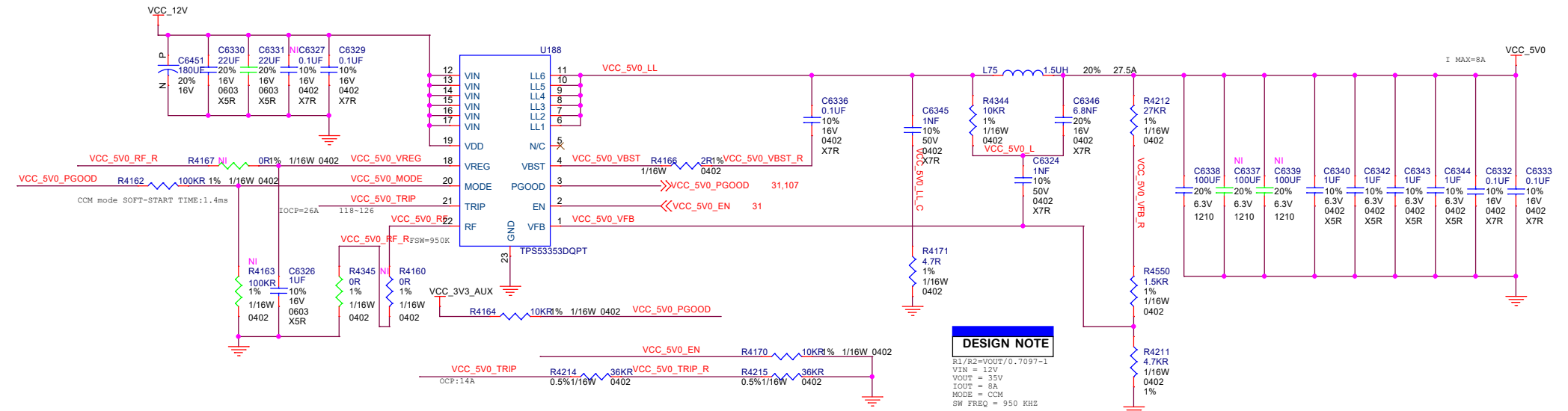
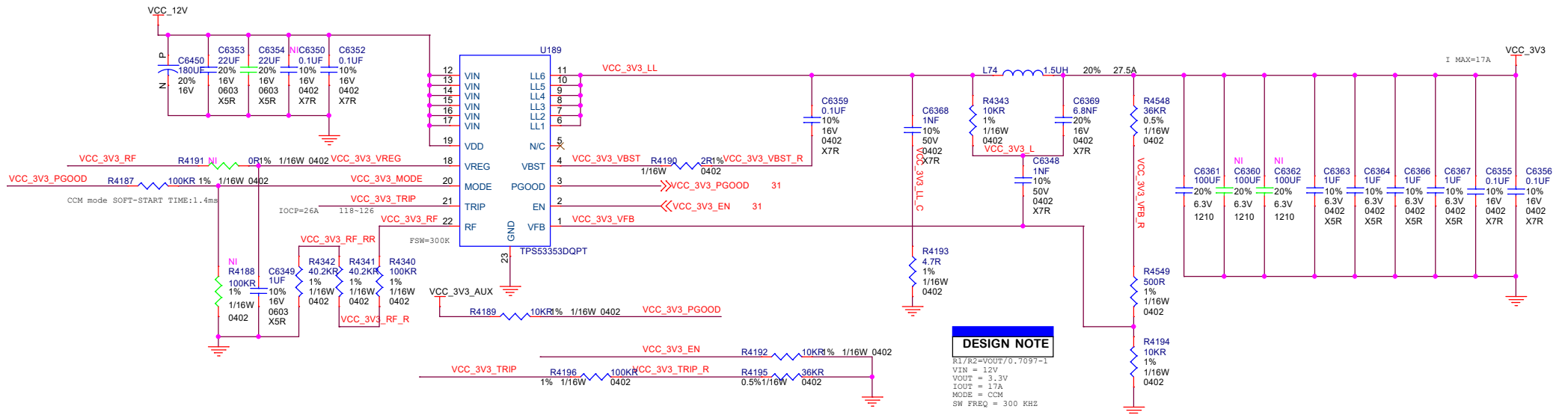
**DESIGN NOTE**

$V_o = V_{ref} * (1 + R1/R2)$



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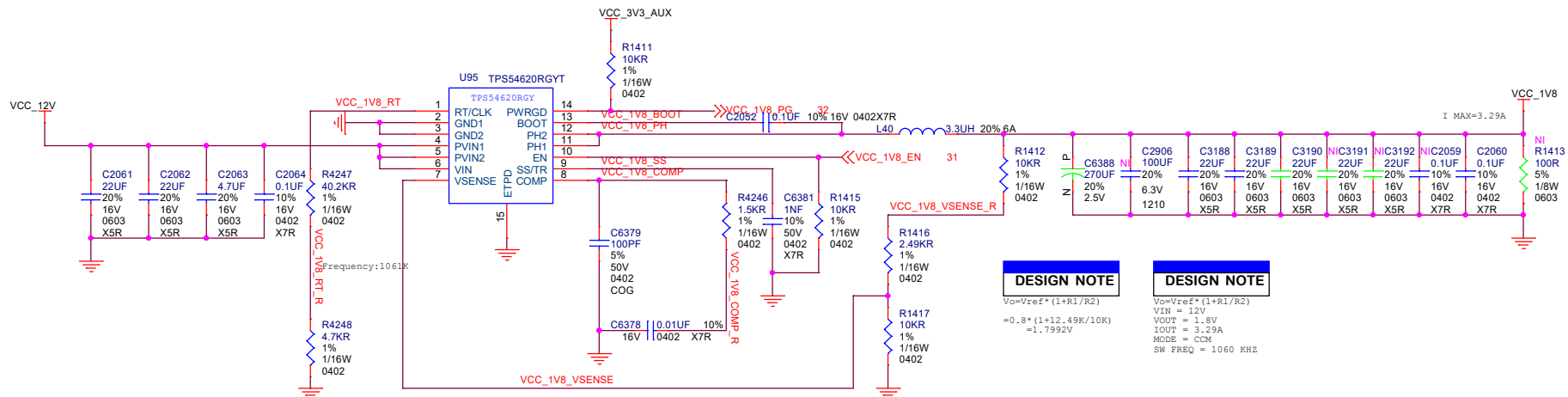
# VCC\_3V3/5V0



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# VCC\_1V8/1V0/1V2



### DESIGN NOTE

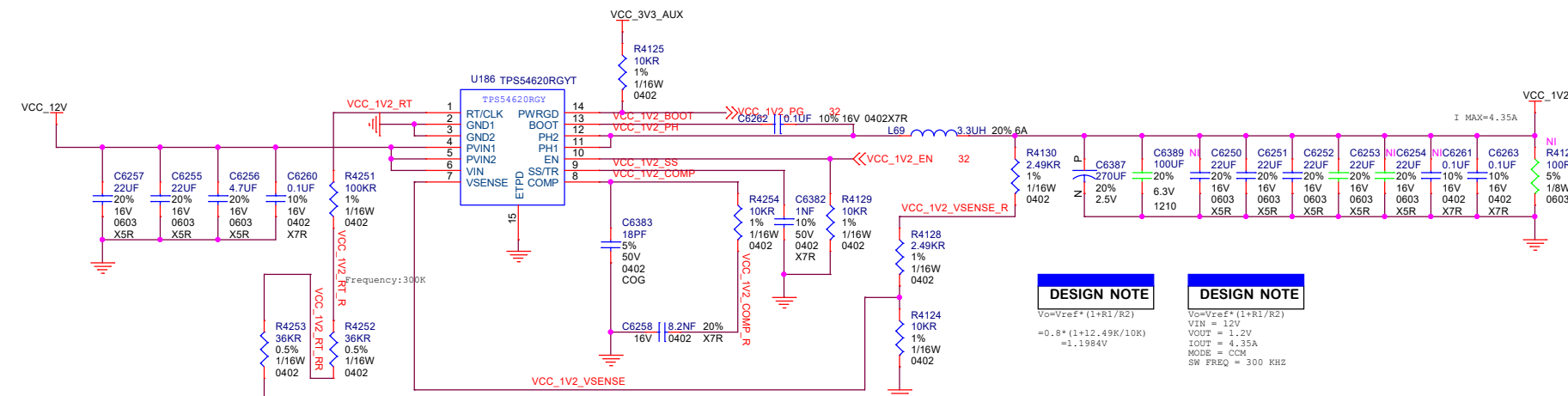
$$V_o = V_{ref} * (1 + R1/R2)$$

$$= 0.8 * (1 + 12.49K/10K)$$

$$= 1.7992V$$

### DESIGN NOTE

$V_o = V_{ref} * (1 + R1/R2)$   
 VIN = 12V  
 VOUT = 1.8V  
 IOUT = 3.29A  
 MODE = CCM  
 SW FREQ = 1060 KHZ



### DESIGN NOTE

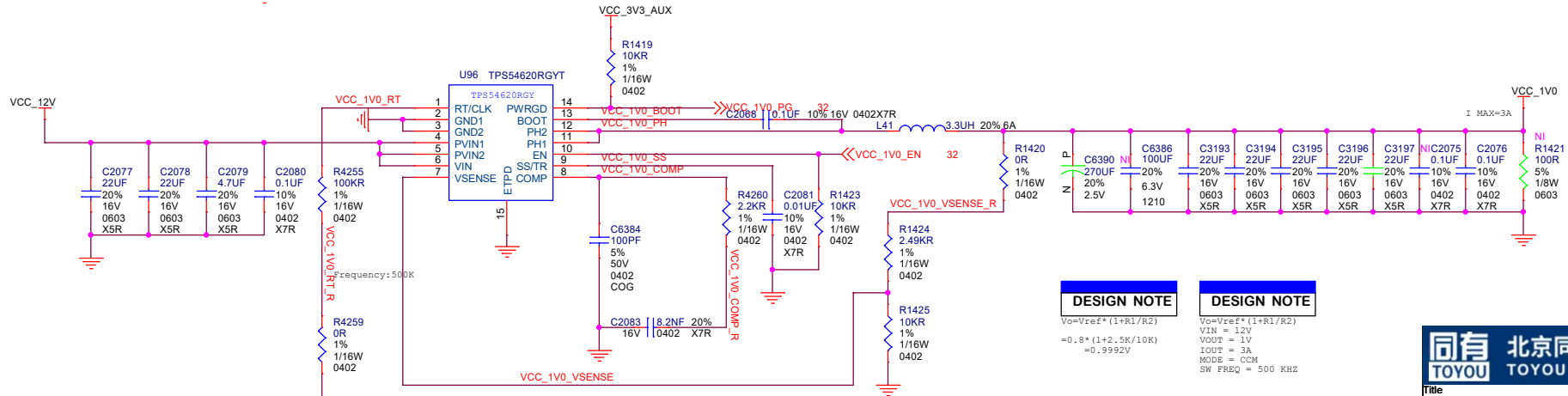
$$V_o = V_{ref} * (1 + R1/R2)$$

$$= 0.8 * (1 + 12.49K/10K)$$

$$= 1.1984V$$

### DESIGN NOTE

$V_o = V_{ref} * (1 + R1/R2)$   
 VIN = 12V  
 VOUT = 1.2V  
 IOUT = 4.35A  
 MODE = CCM  
 SW FREQ = 300 KHZ



### DESIGN NOTE

$$V_o = V_{ref} * (1 + R1/R2)$$

$$= 0.8 * (1 + 2.5K/10K)$$

$$= 0.9992V$$

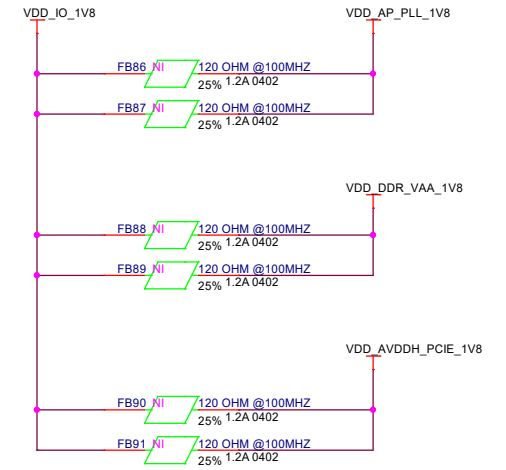
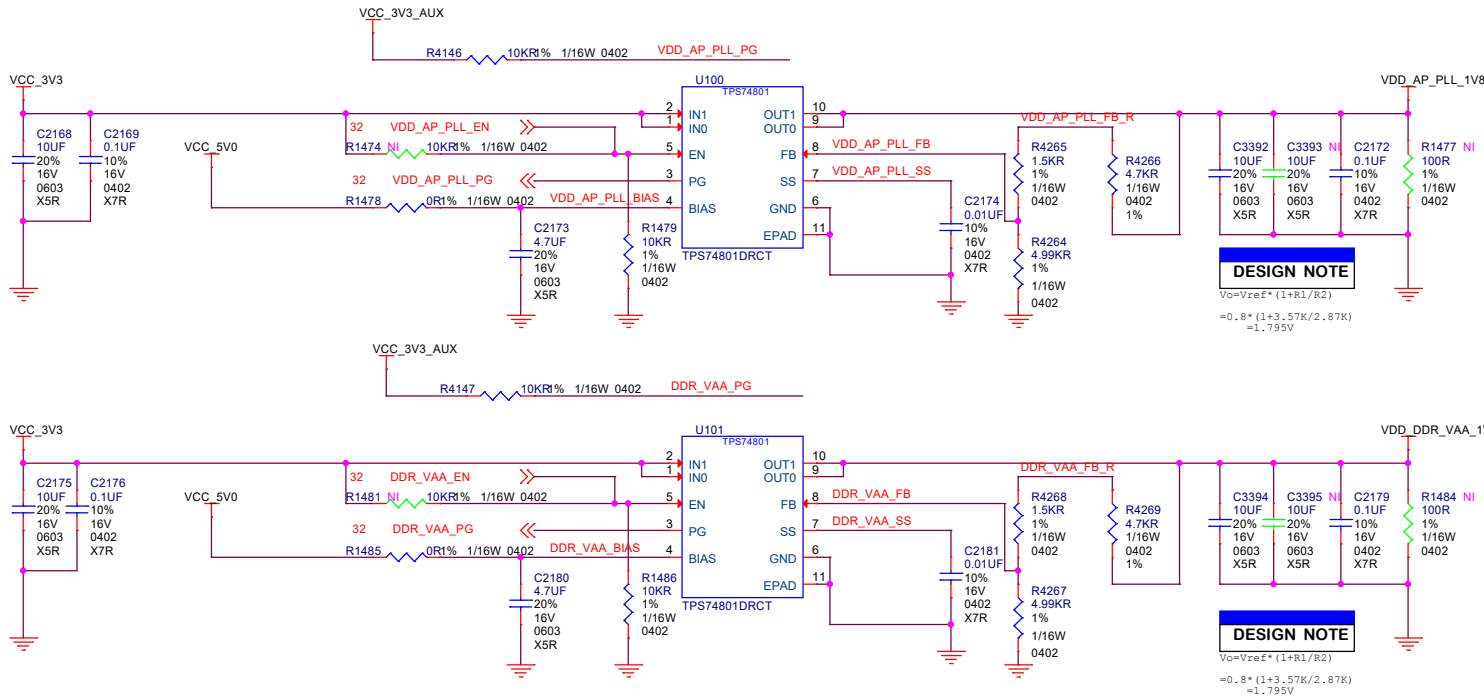
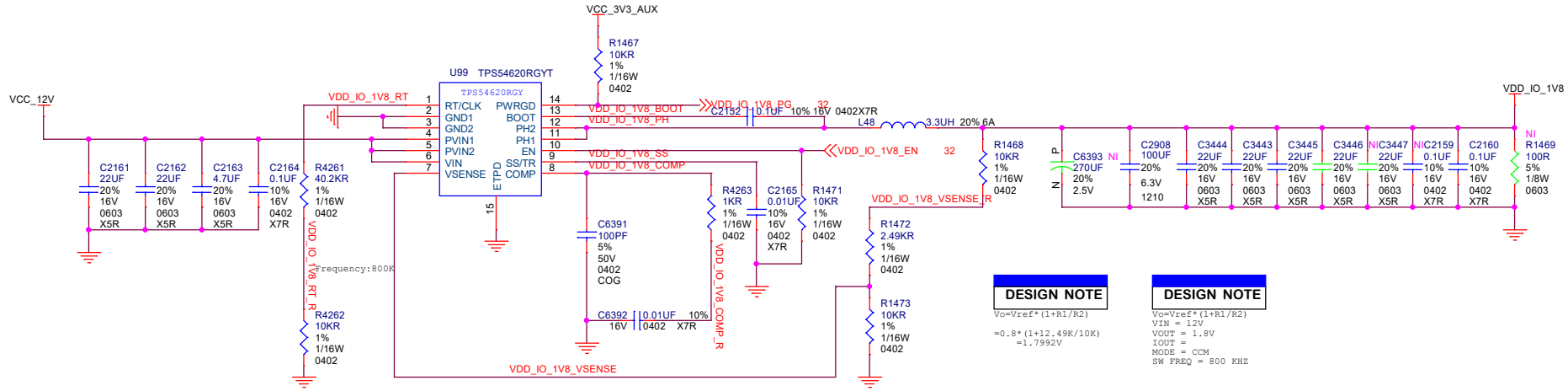
### DESIGN NOTE

$V_o = V_{ref} * (1 + R1/R2)$   
 VIN = 12V  
 VOUT = 1V  
 IOUT = 3A  
 MODE = CCM  
 SW FREQ = 500 KHZ



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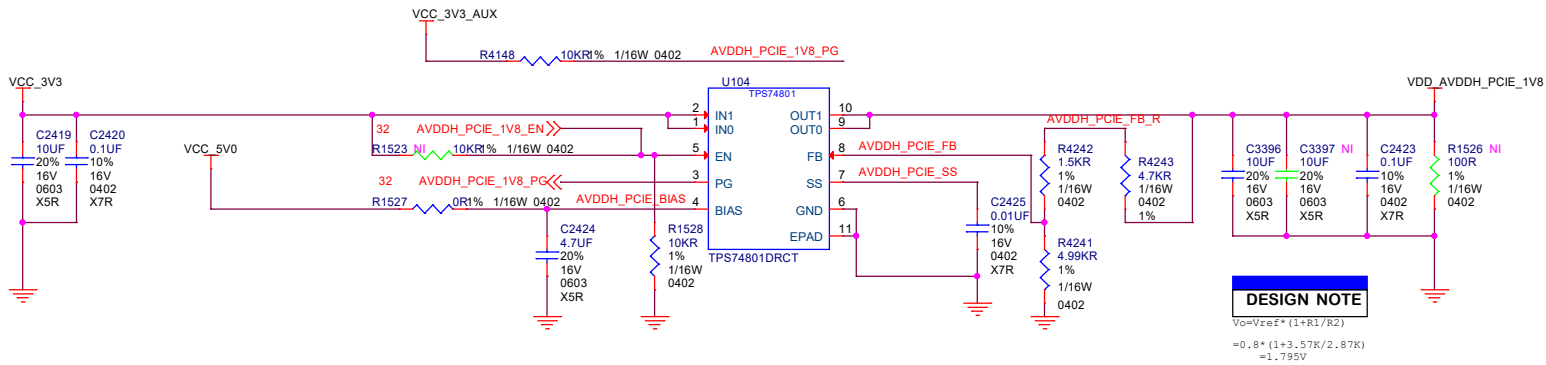
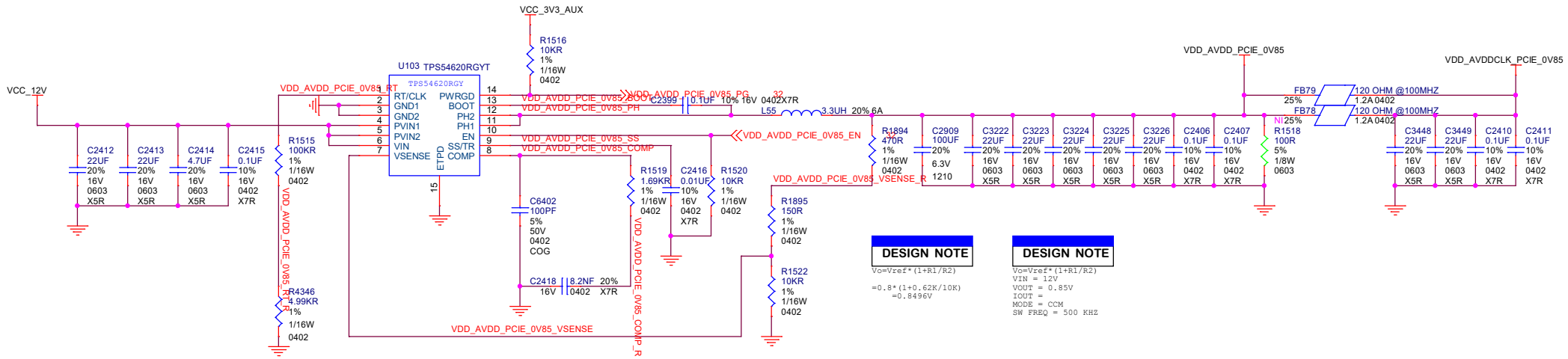
# VDD\_1V8



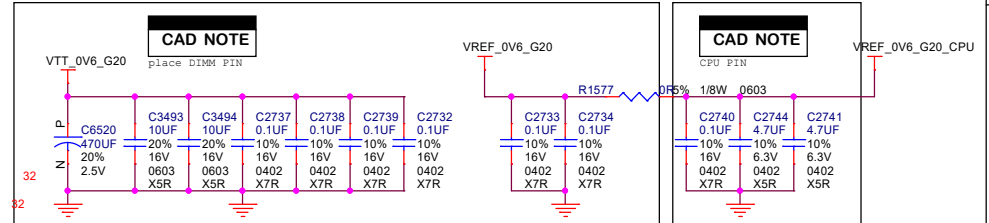
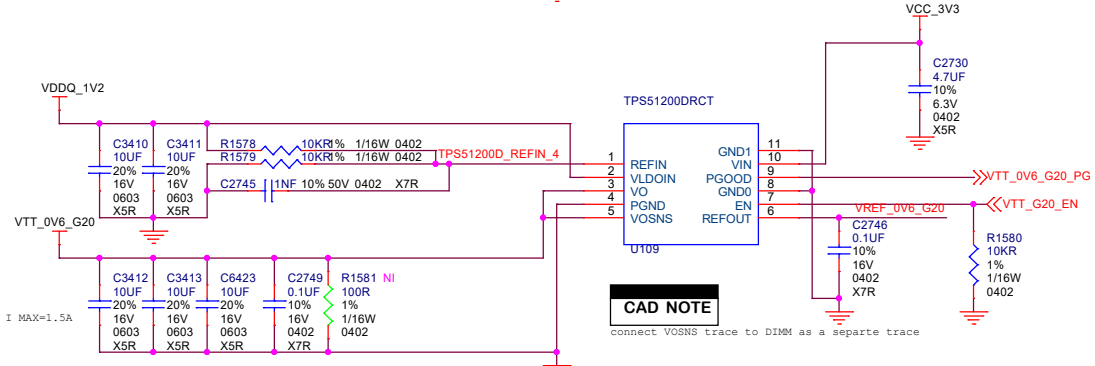
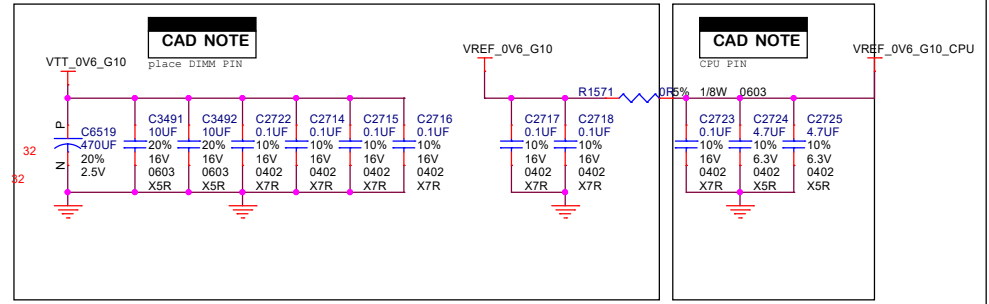
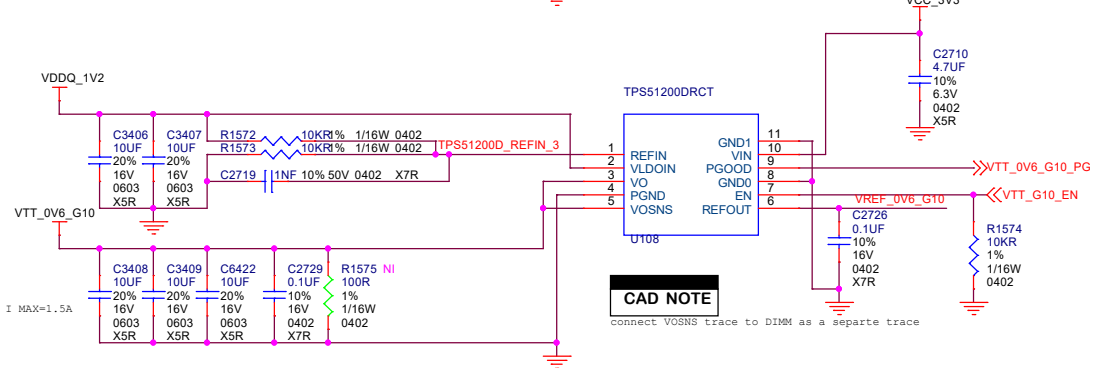
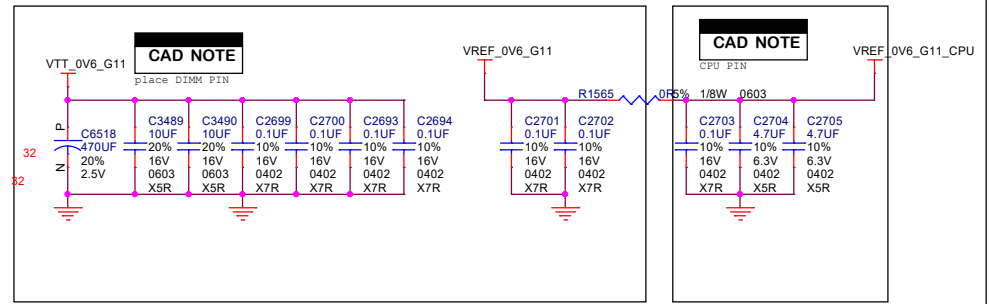
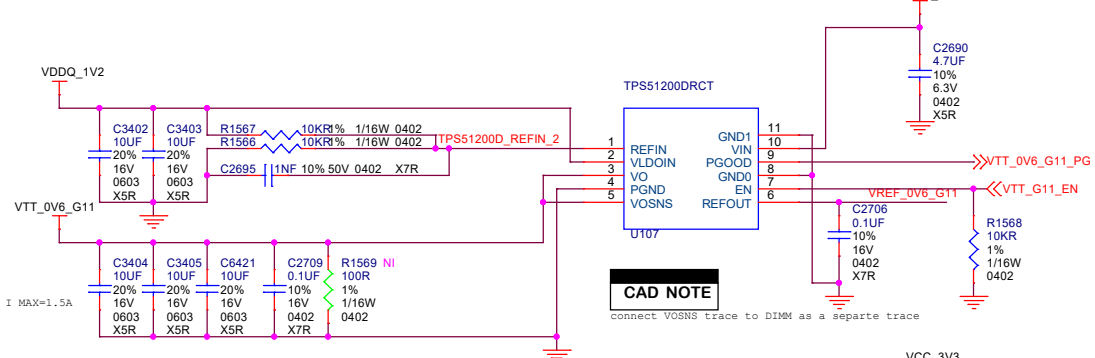
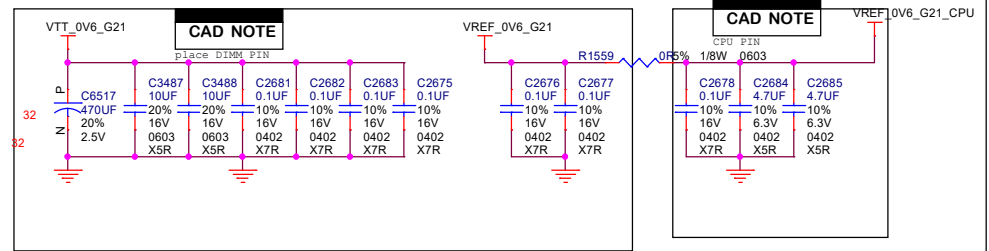
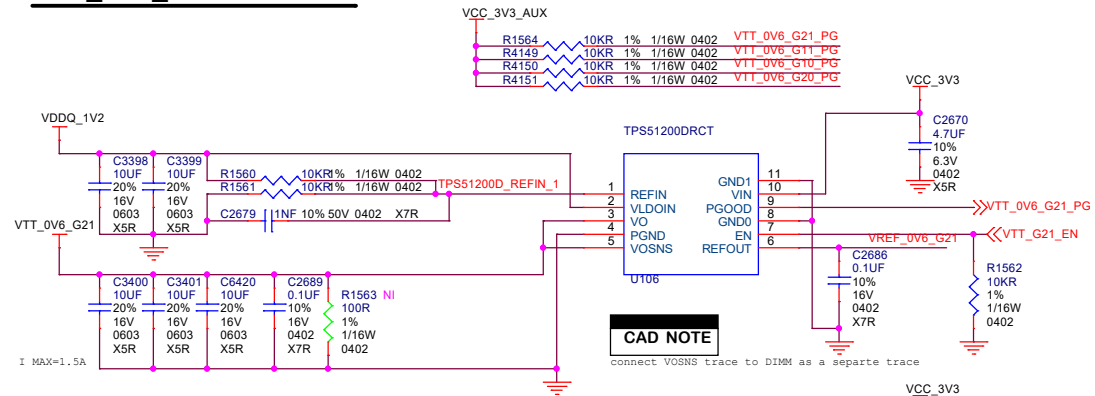
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# CPU\_AVDD/AVDDH\_PCIE



# CPU\_VTT\_POWER



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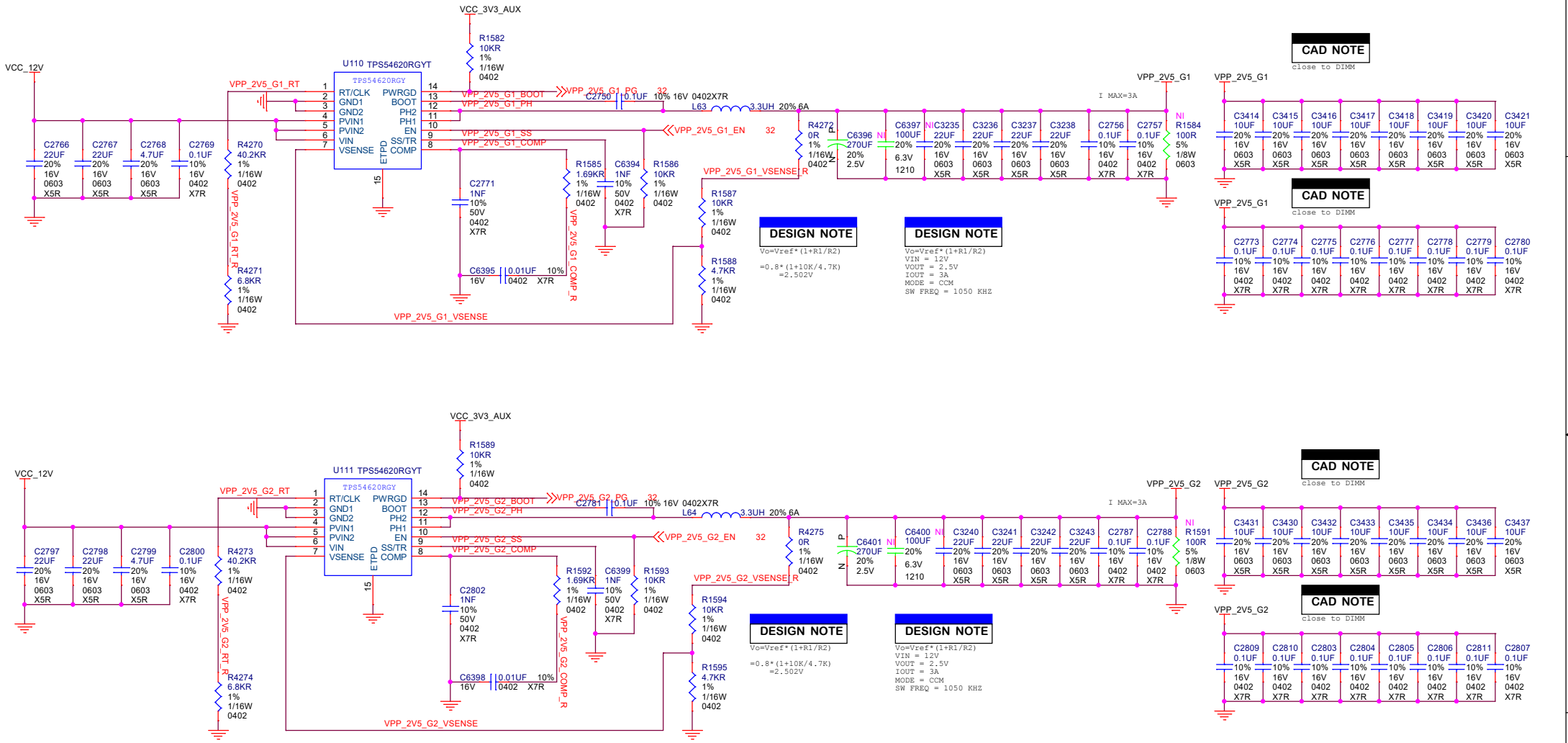
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# CPU\_VPP\_POWER



**DESIGN NOTE**  
 $V_o = V_{ref} * (1 + R1/R2)$   
 $= 0.8 * (1 + 10K/4.7K)$   
 $= 2.502V$

**DESIGN NOTE**  
 $V_o = V_{ref} * (1 + R1/R2)$   
 $V_{IN} = 12V$   
 $V_{OUT} = 2.5V$   
 $I_{OUT} = 3A$   
 $MODE = CCM$   
 $SW\ FREQ = 1050\ KHZ$

**DESIGN NOTE**  
 $V_o = V_{ref} * (1 + R1/R2)$   
 $= 0.8 * (1 + 10K/4.7K)$   
 $= 2.502V$

**DESIGN NOTE**  
 $V_o = V_{ref} * (1 + R1/R2)$   
 $V_{IN} = 12V$   
 $V_{OUT} = 2.5V$   
 $I_{OUT} = 3A$   
 $MODE = CCM$   
 $SW\ FREQ = 1050\ KHZ$

**CAD NOTE**  
 close to DIMM

**CAD NOTE**  
 close to DIMM

**CAD NOTE**  
 close to DIMM

**CAD NOTE**  
 close to DIMM



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