

BATTERY CHARGER

DESIGN NOTE

VBUS: 3.9V to 14V

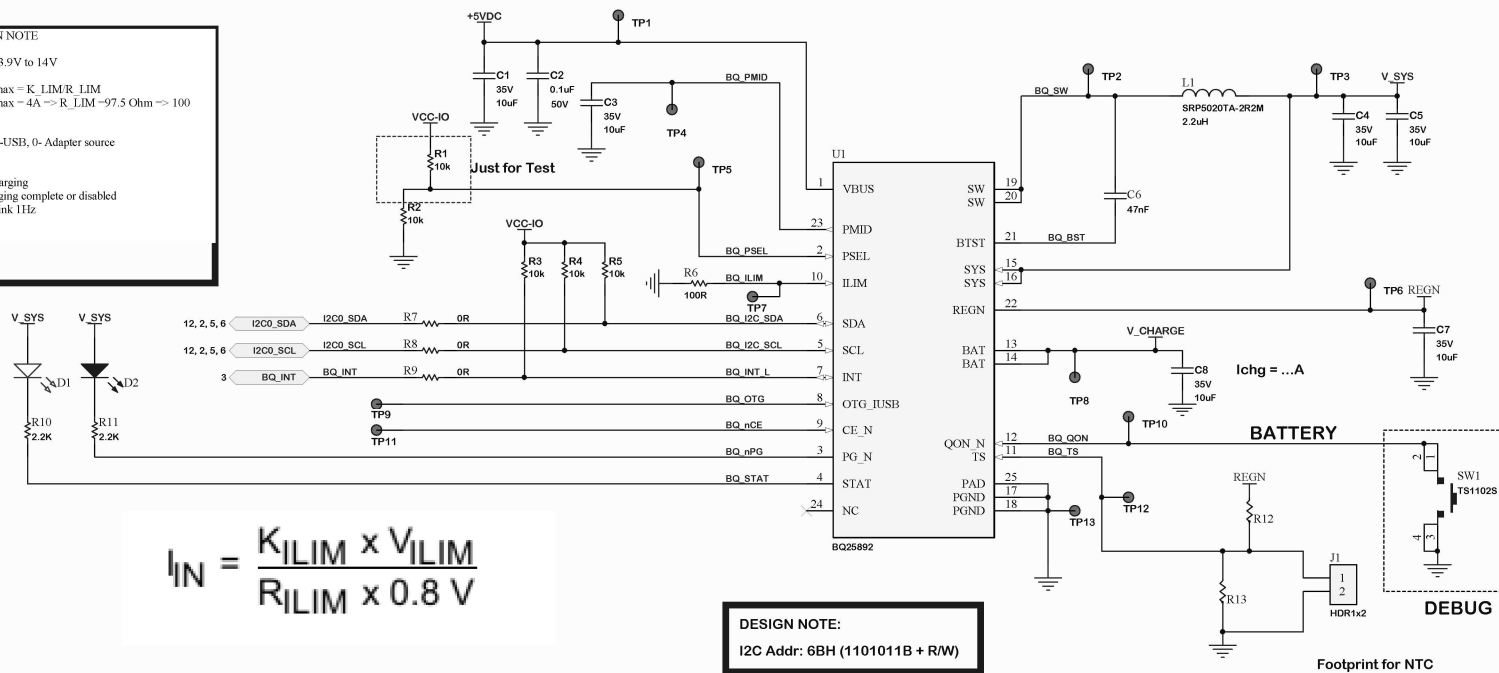
$$I_{LIM\ max} = K_{LIM} / R_{LIM}$$

$$I_{LIM\ max} = 4A \Rightarrow R_{LIM} = 97,5\ \Omega \Rightarrow 100\ \Omega$$

PSEL: 1-USB, 0- Adapter source

STAT:

Low: charging
Hi: charging complete or disabled
Fault: blink 1 Hz

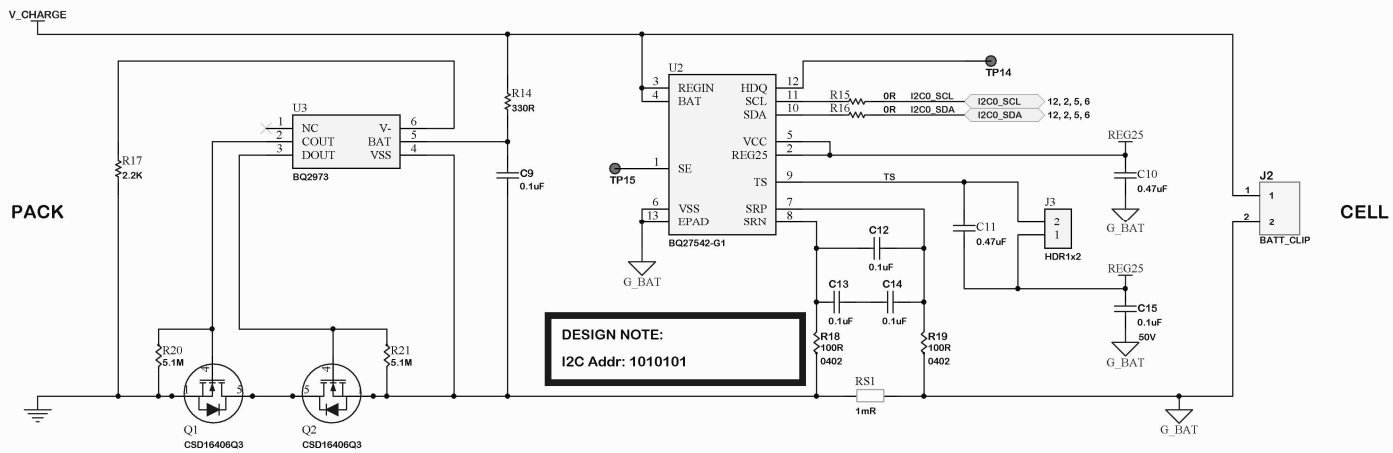


LAYOUT NOTE:

Place C REGN close to IC

DESIGN NOTE:

I2C Addr: 6BH (1101011B + R/W)



DESIGN NOTE:
I2C Addr: 1010101

DESIGN NOTE:

BQ29732

OVP = 4.280V

UVP = 2.5V

Title: *		Lumi Viet Nam , JSC 4th Floor, LUMI Building 38th Du Duc Duc Street Nam Tu Liem District Ha Noi	Cannot open file C:\Users\NW\Documents sAllium logo.png
Project: Project	Revision: SCH_REV		
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