

Figure 1: Rev 1 board, no load

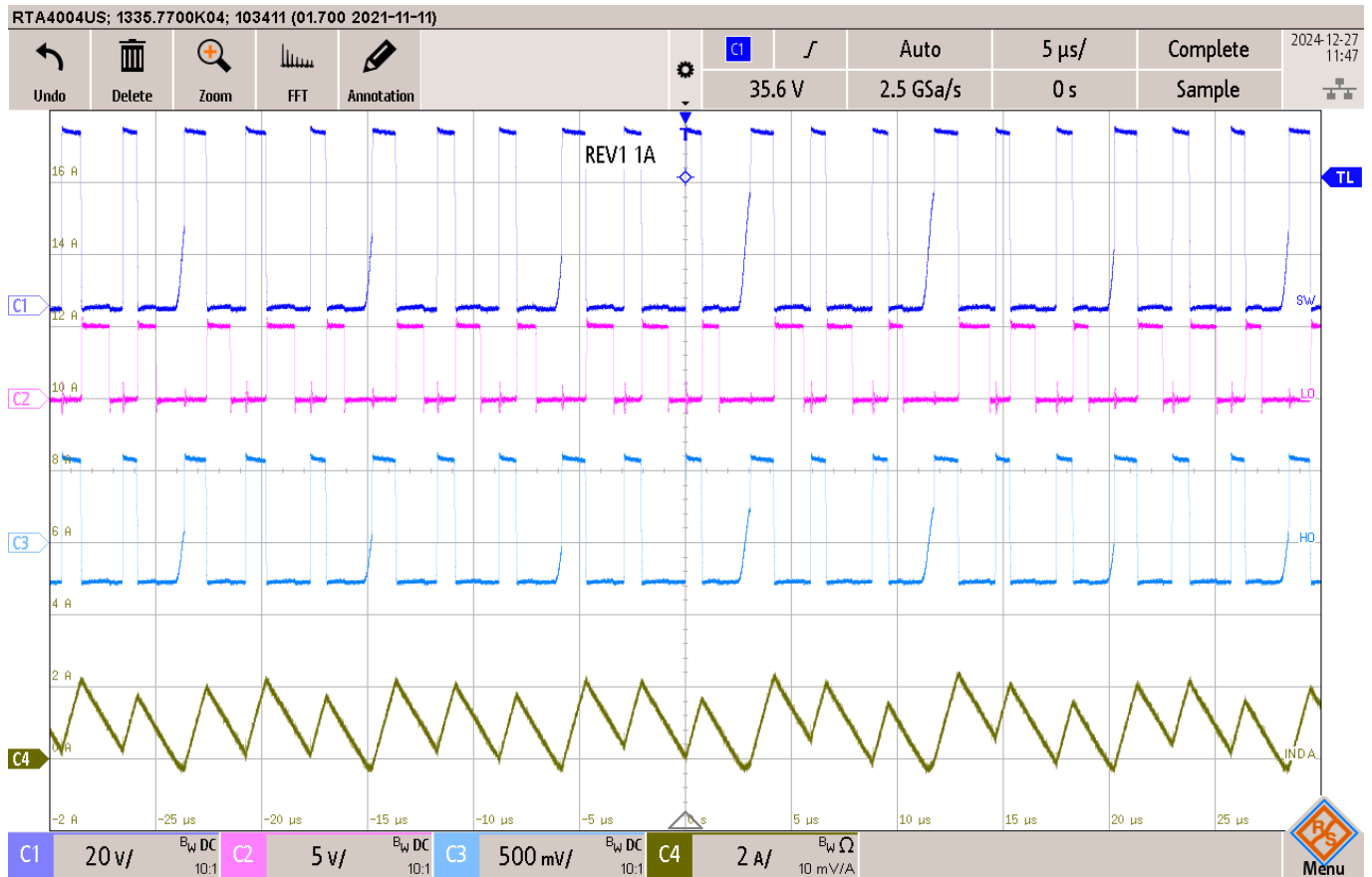


Figure 2: Rev 1 board, 1A load

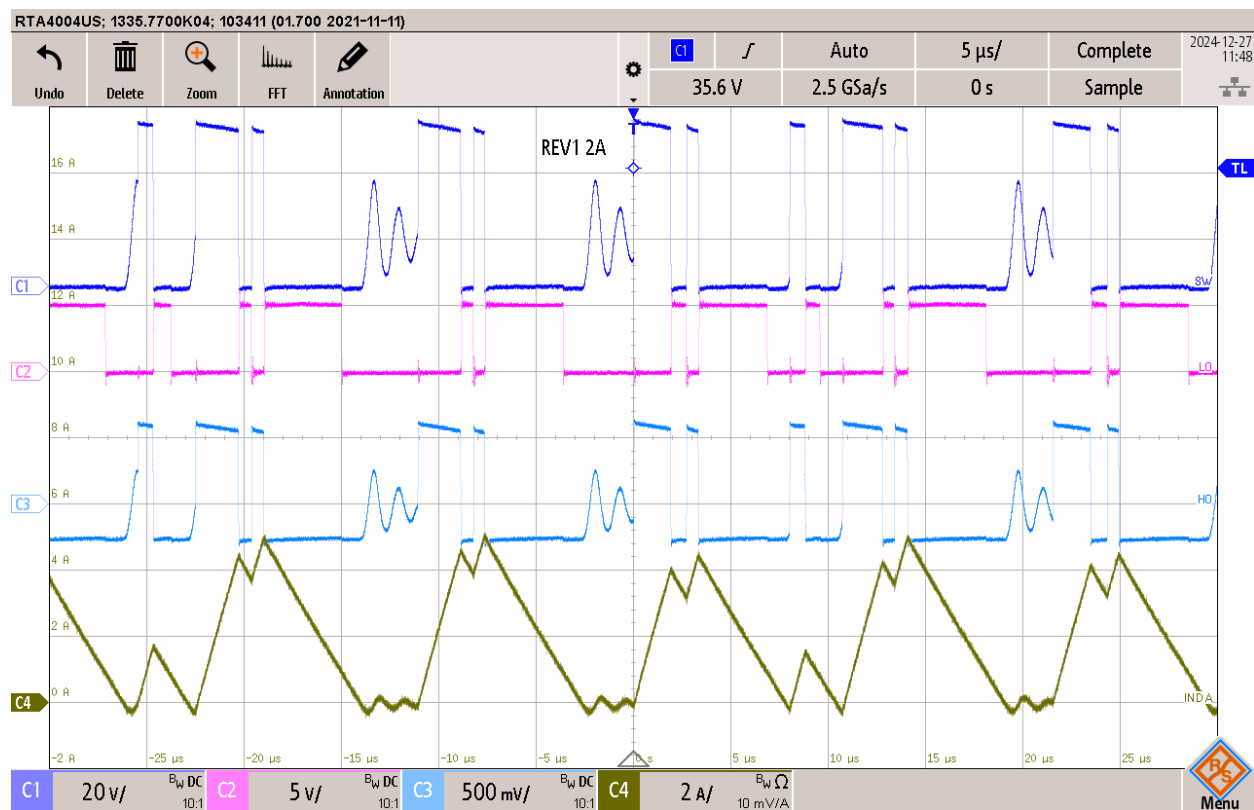


Figure 3: Rev 1 board, 2A load

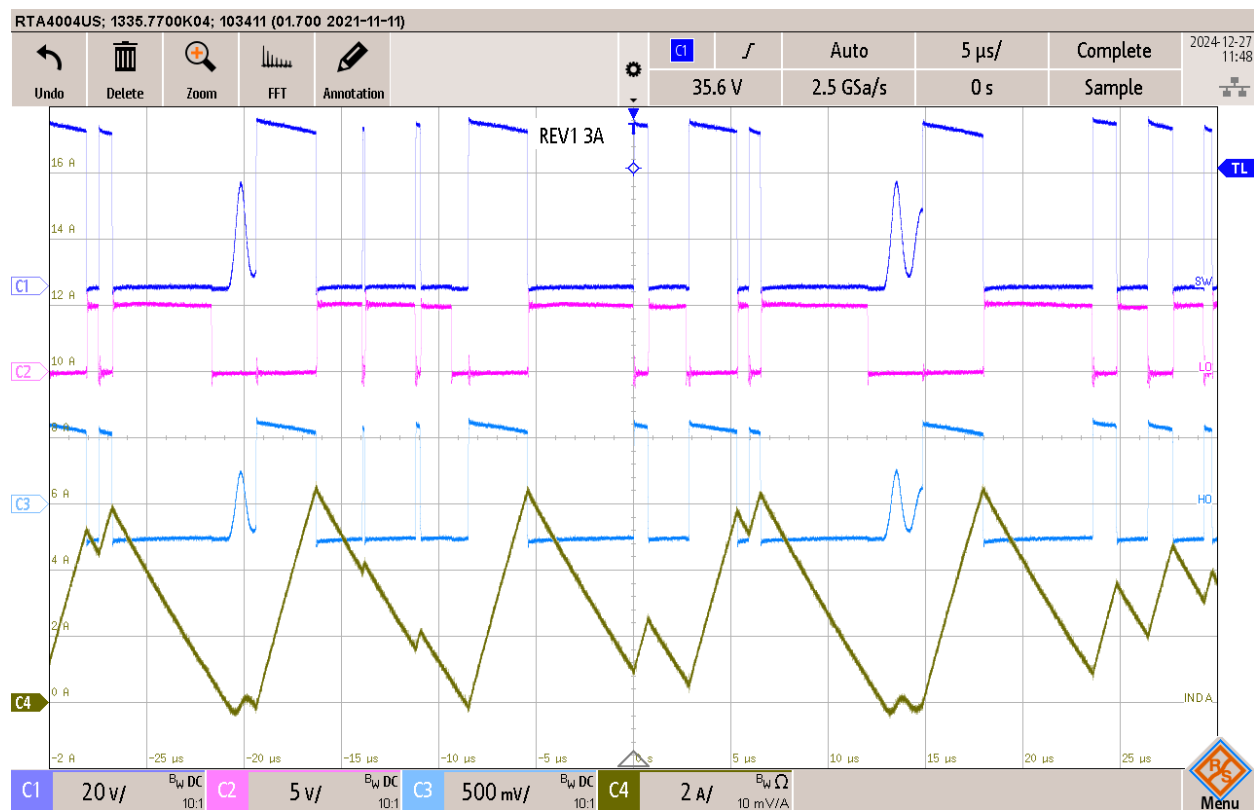


Figure 4: Rev 1 board, 3A load

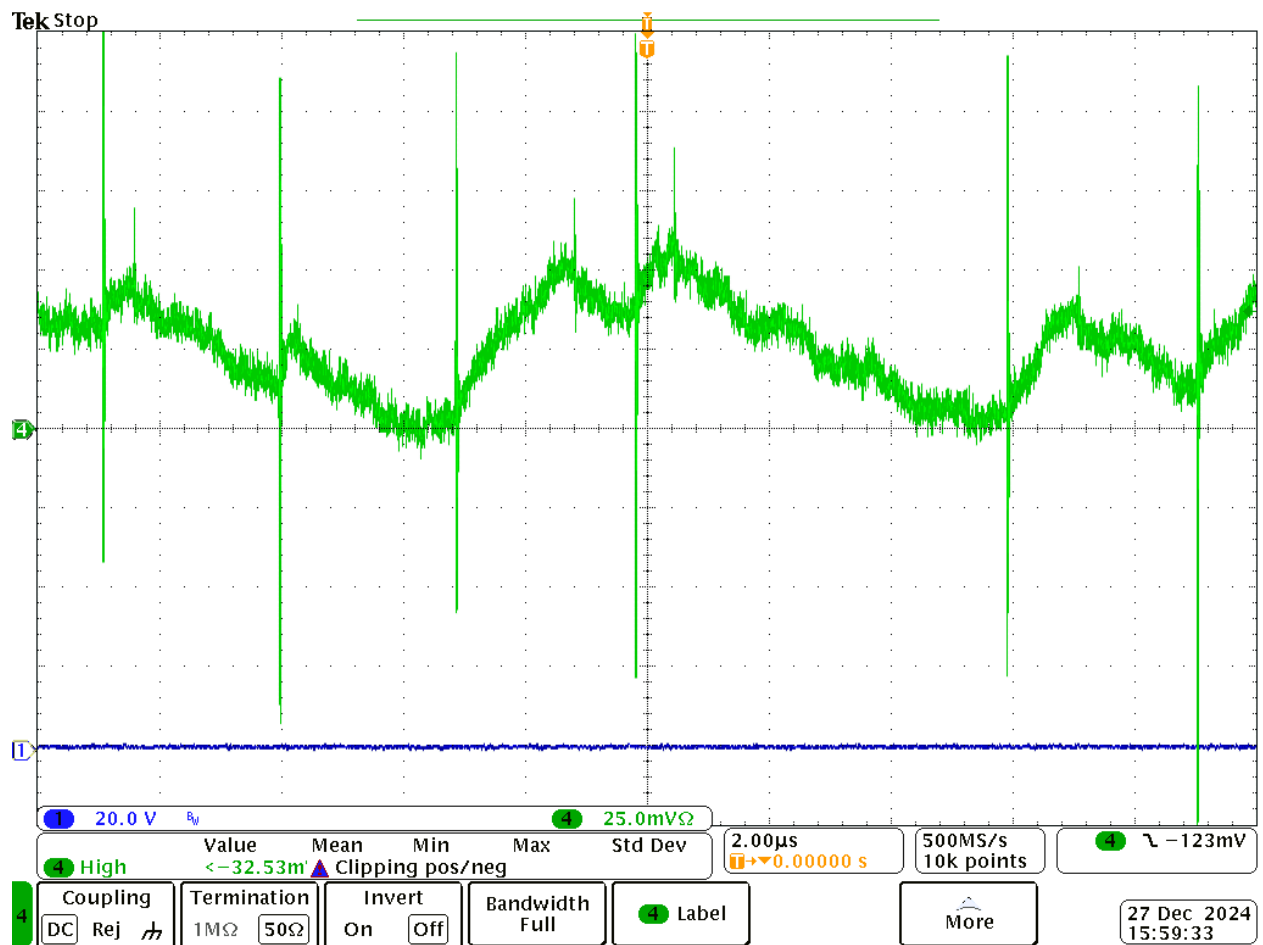


Figure 5: Rev 1 board, 2A load, current sense voltage. Note this was measured at the resistor, not at the IC

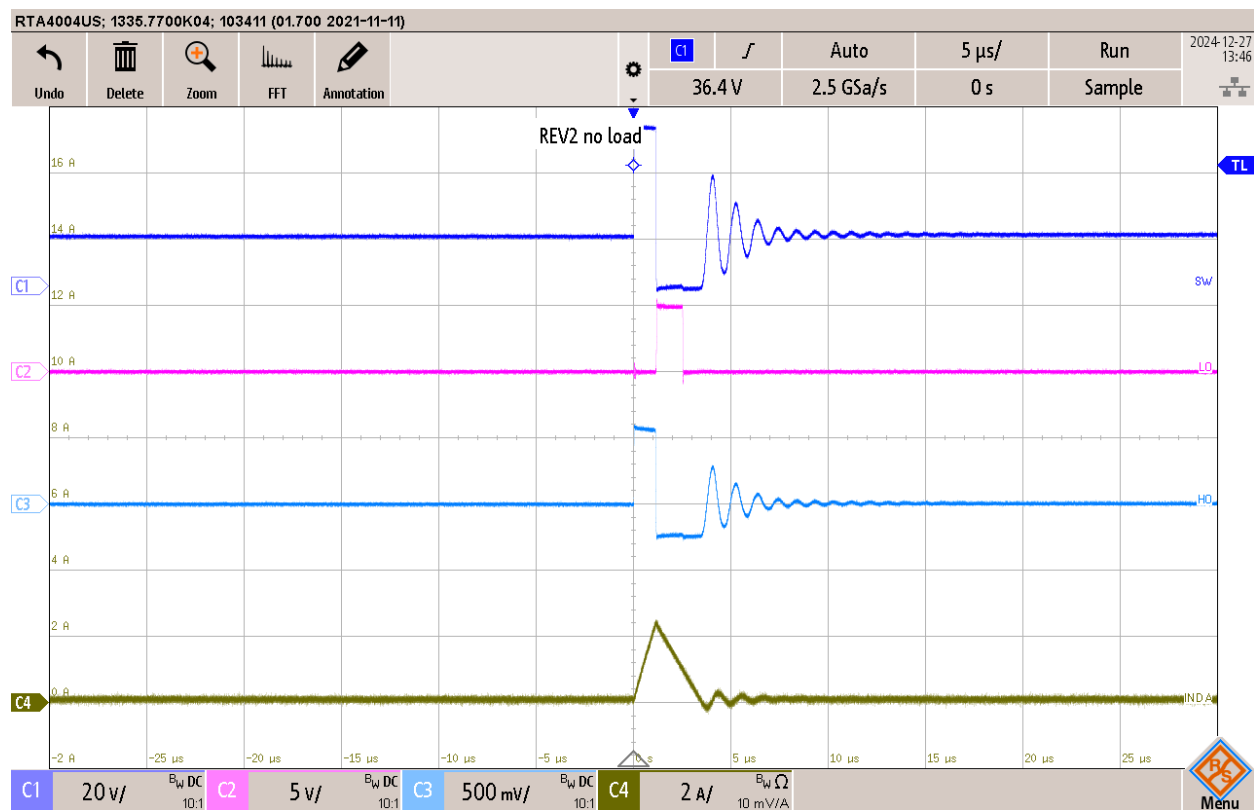


Figure 6: Rev 2 board, no load

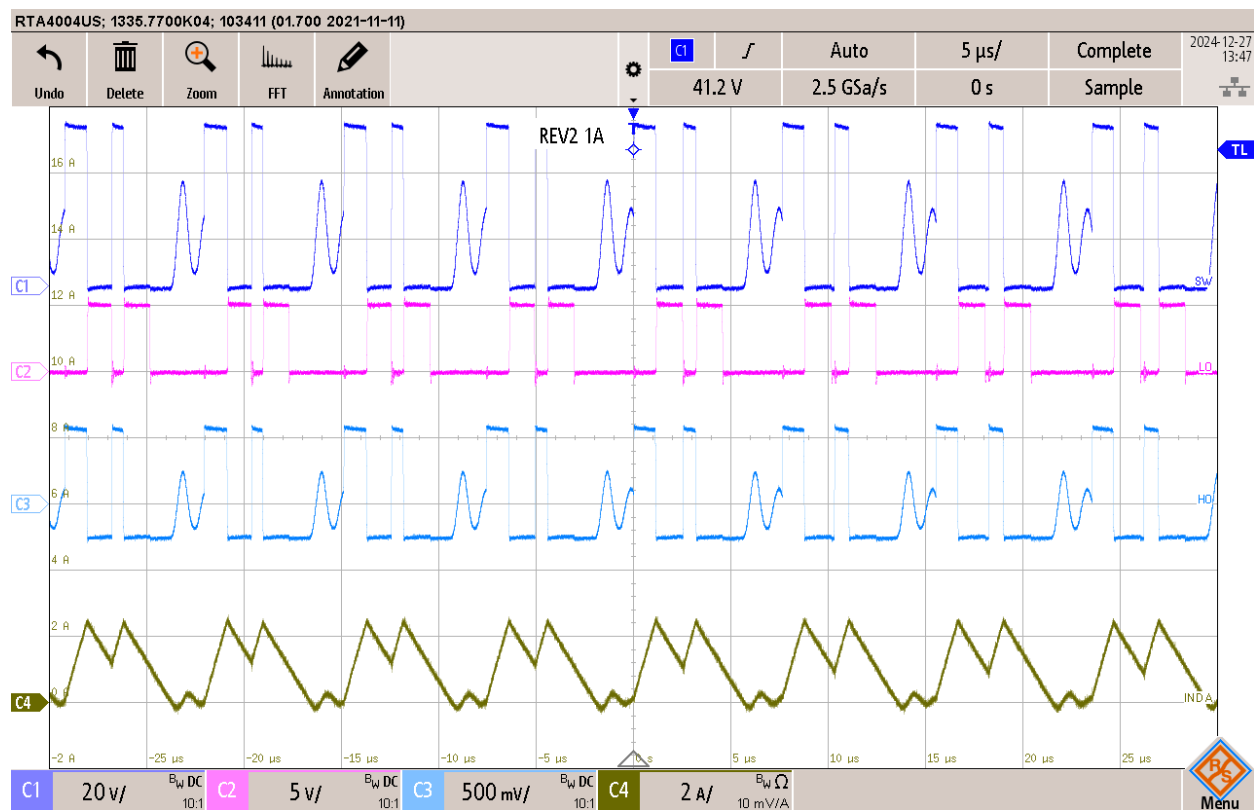


Figure 7: Rev 2 board, 1A load

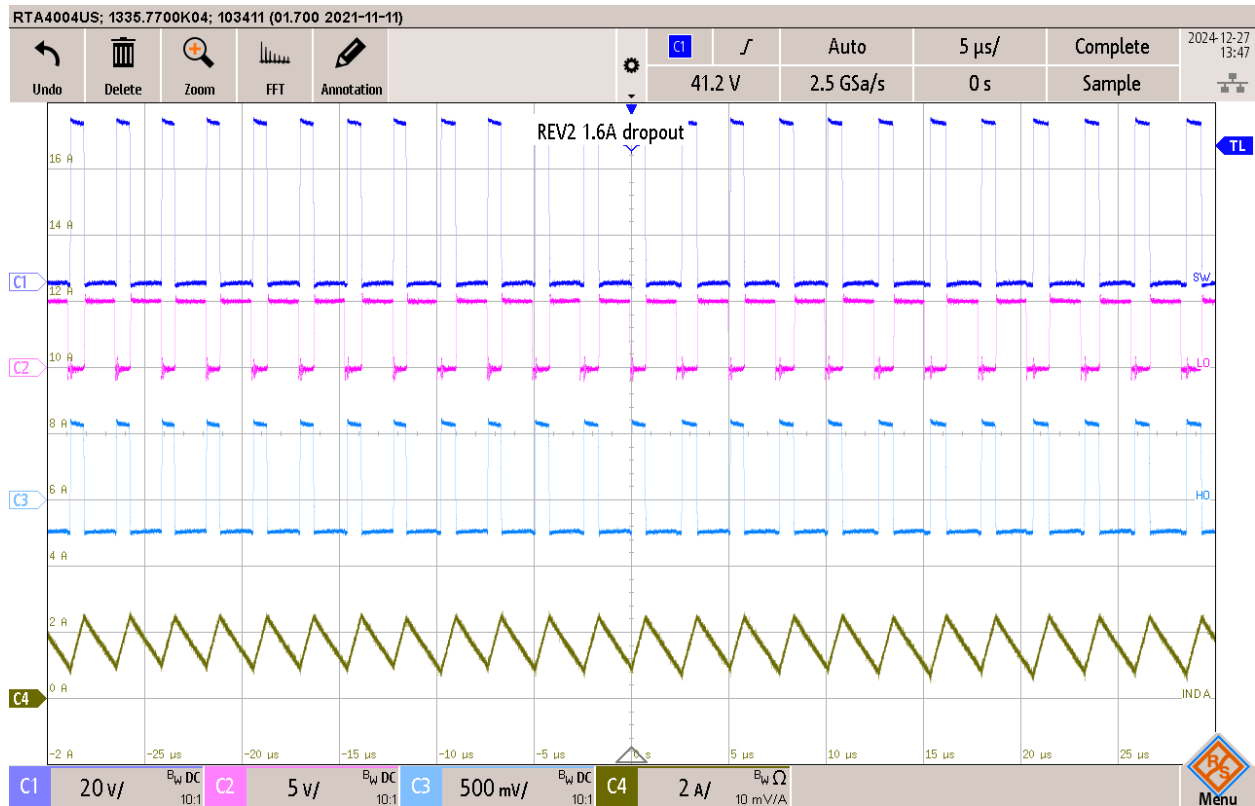


Figure 8: Rev 2 board, 1.6A load. Output voltage is out of regulation. You can see that duty cycle is being limited and inductor current is not being allowed to rise up to meet the demand as in the Rev 1 design



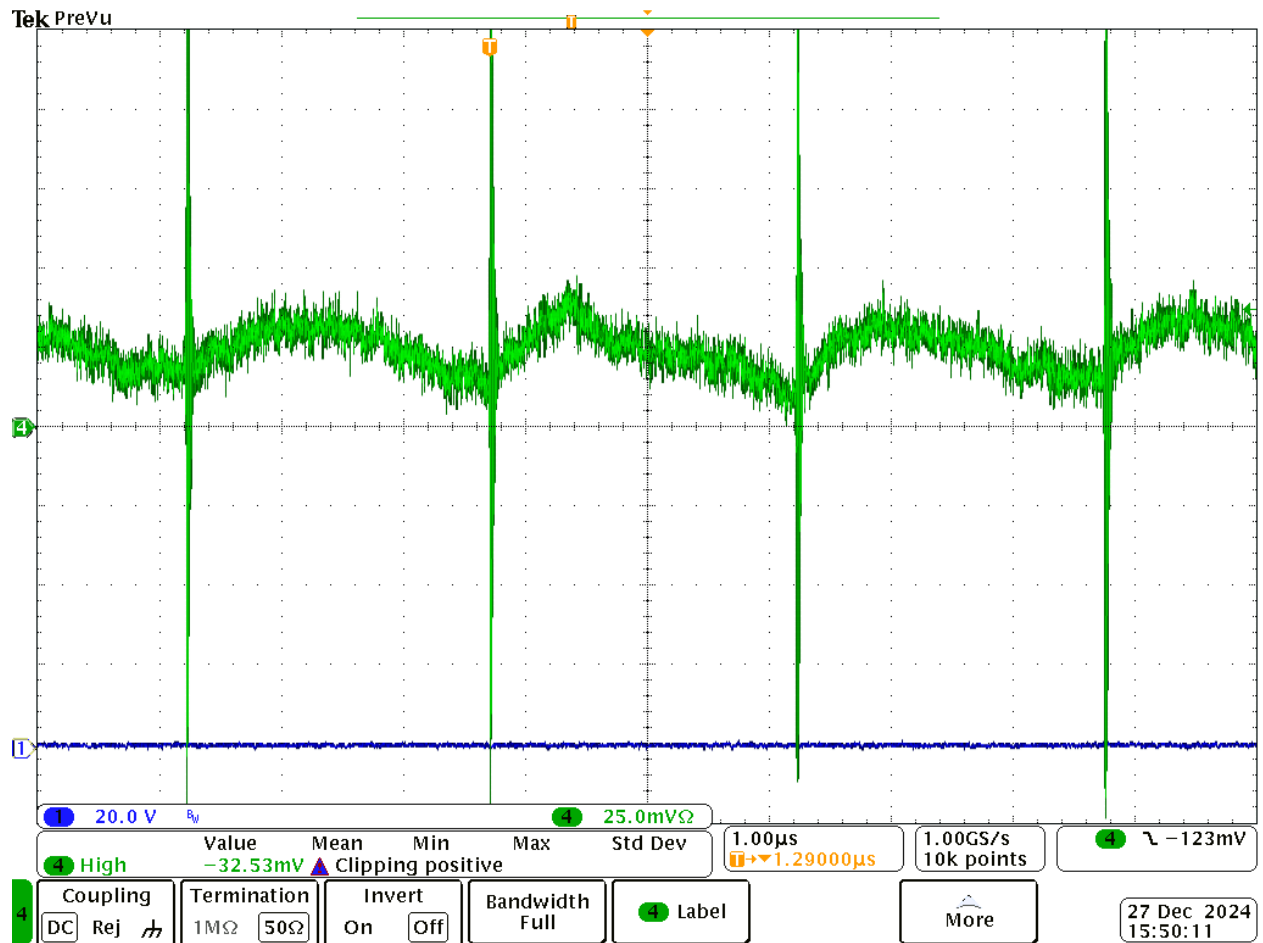


Figure 9: Rev 2 board 1.6A load, current sense voltage. Note this was measured at the resistor, not at the IC