

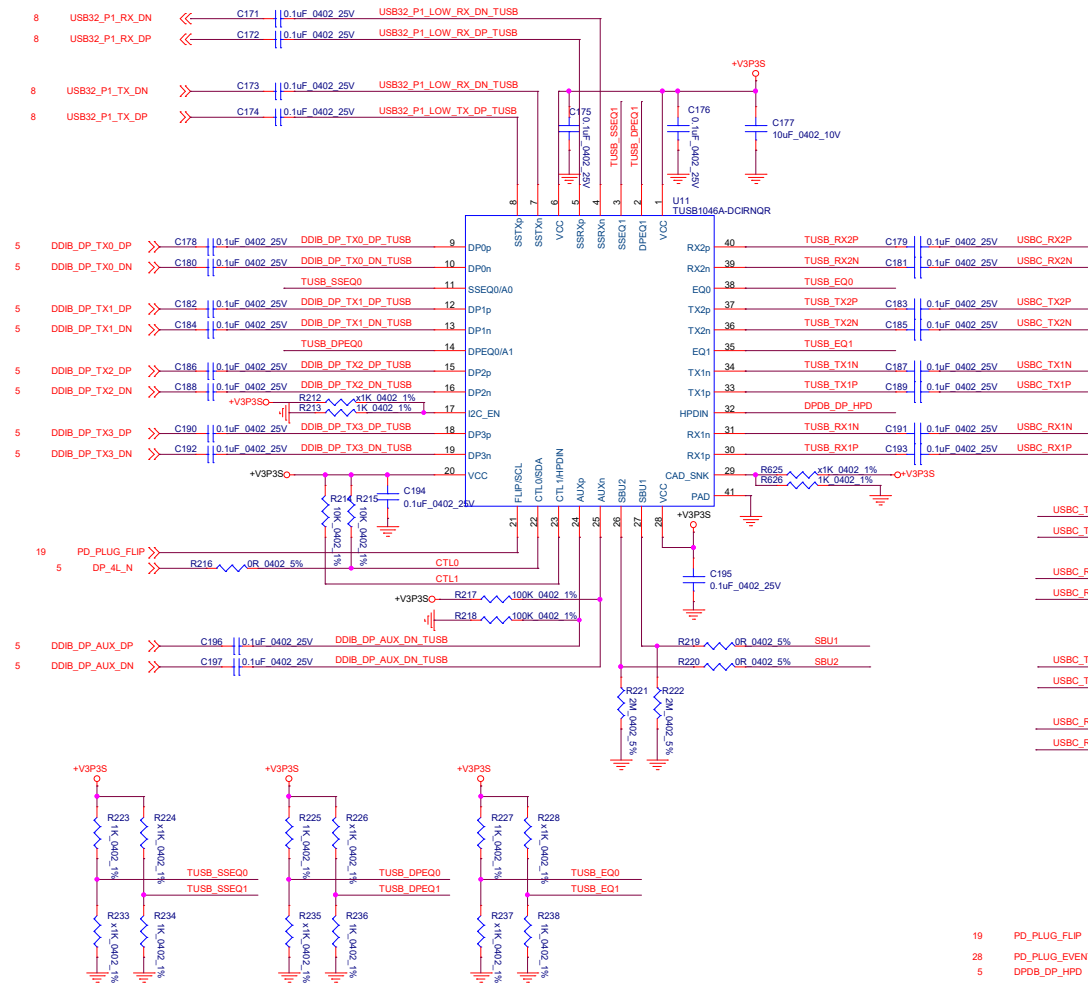


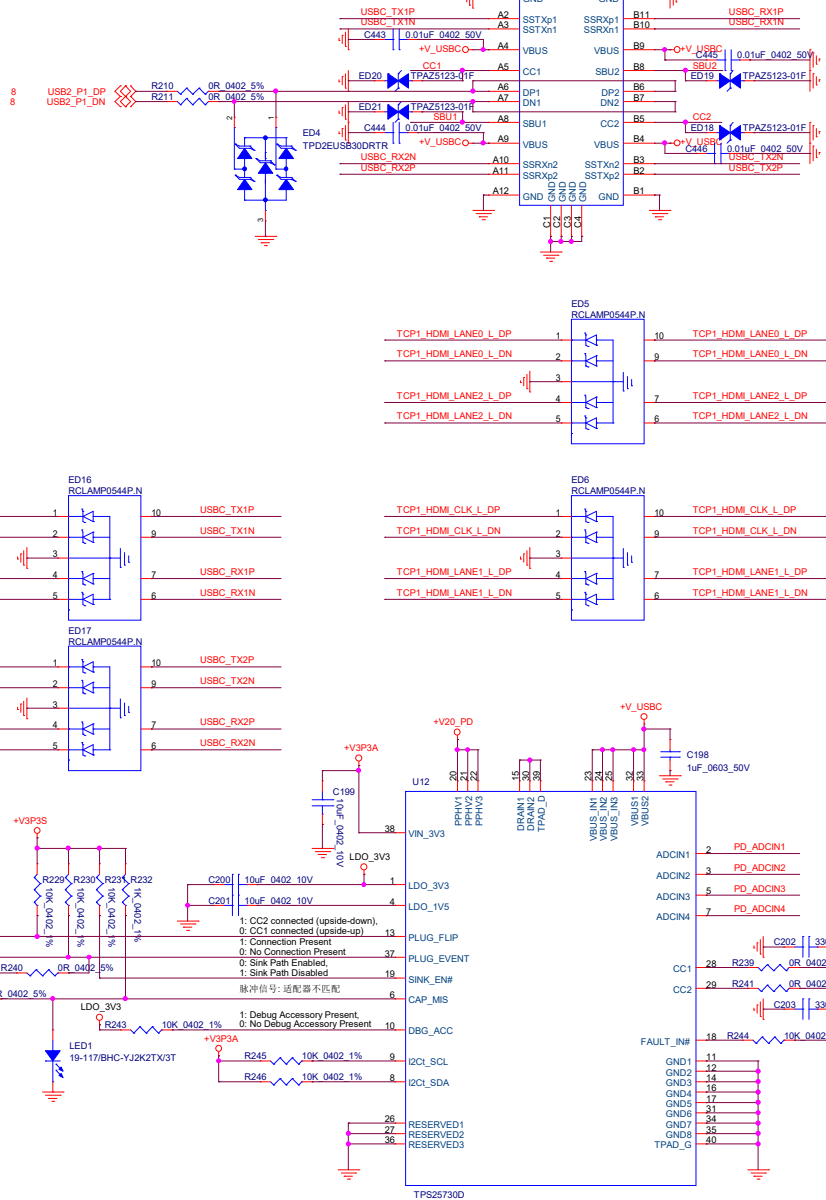
Table 2. GPIO Configuration Control

CTL1 PIN	CTL0 PIN	FLIP PIN	TUSB1046A-DCI CONFIGURATION	VESA DisplayPort ALT MODE DFP_D CONFIGURATION
L	L	L	Power Down	—
L	L	H	Power Down	—
L	H	L	One Port USB 3.1 - No Flip	—
L	H	H	One Port USB 3.1 - With Flip	—
H	L	L	4 Lane DP - No Flip	C and E
H	L	H	4 Lane DP - With Flip	C and E
H	H	L	One Port USB 3.1 + 2 Lane DP- No Flip	D and F
H	H	H	One Port USB 3.1 + 2 Lane DP- With Flip	D and F

Table 3. GPIO AuxP or AUXn to SBU1 or SBU2 Mapping

CTL1 PIN	FLIP PIN	MAPPING
H	L	AUXp → SBU1 AUXn → SBU2
H	H	AUXp → SBU2 AUXn → SBU1
L > 2 ms	X	Open

Table 4 Details the TUSB1046A-DCI's mux routing. This table is valid for both I²C and GPIO configuration modes.



20V@5A充电器

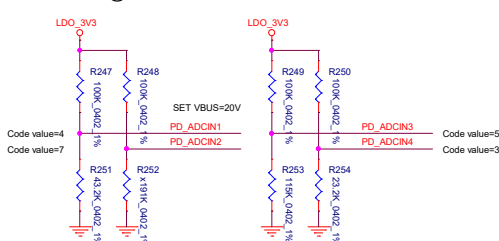


表 2-2. ADCINx 引脚的解码

最小值	目标	最大值	不使用 R _{UP} 或 R _{DOWN}	ADCINx 解码度
0	0.0114	0.0228	连接到 GND	0
0.0229	0.0475	0.0722	不适用	1
0.0723	0.1074	0.1425	不适用	2
0.1425	0.1899	0.2372	不适用	3
0.2373	0.3022	0.3671	不适用	4
0.3672	0.5388	0.7084	连接到 LDO_1V5	5
0.7085	0.8062	0.9060	不适用	6
0.9061	0.9530	1.0	连接到 LDO_3V3	7