

[illegible]

Current Sensing

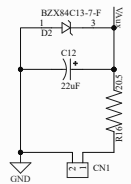
Rs - is used to sense the current from CT
 Rr, Cr - is used to reset the current sense Transformer
 (DR) need to be designed to withstand the current sense transformers reset voltage (VR)

[illegible]

The schematic diagram illustrates the JPSX2 module's internal connections. It features two input signal traces, V_{Sense} and V_{in} , each passing through a $1.0M\Omega$ resistor (R36 and R20, respectively). These traces are connected to the module's input pins. The module's internal components include a $1.0M\Omega$ resistor (R35) and a $1.0M\Omega$ resistor (R19). The module is connected to a power supply (PGND) and ground (GND) through a $1.0M\Omega$ resistor (R35). The module's output is connected to a $1.0M\Omega$ resistor (R20) and a $1.0M\Omega$ resistor (R19). The module's internal components are labeled with their respective values and names.

Component values and labels:

- V_{Sense} (Input)
- R36: $1.0M\Omega$
- R35: $1.0M\Omega$
- JPSX2 (Module)
- R19: $1.0M\Omega$
- R20: $1.0M\Omega$
- V_{in} (Input)
- PGND (Power Ground)
- GND (Ground)



Title 1 KW Interleaved Boost PFC using UCC2807A Controller		
PEMD Lab		
Aaqib Sheikh Muhammad		
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