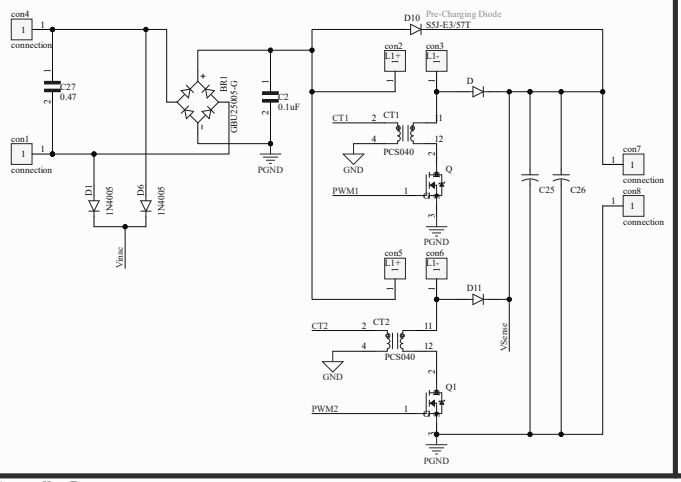
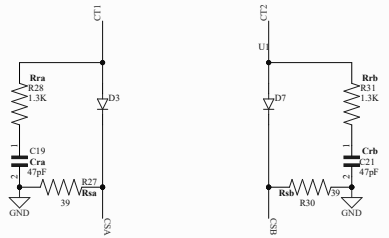
Power Circuit



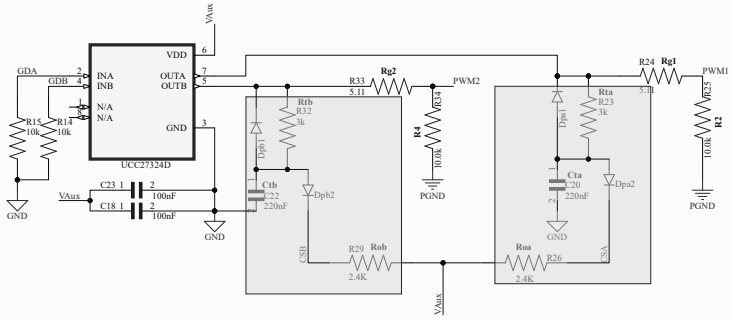
Current Sensing



Rs - is used to sense the current from CT
Rr, Cr - is used to reset the current sense Transformer

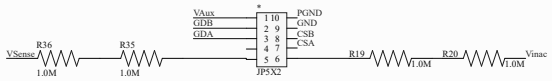
(DR) need to be designed to withstand the current sense transformers reset voltage (VR)

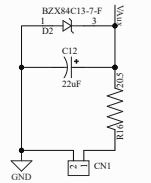
Gate Driver & Gate Signal



To improve noise immunity at extremely light loads, a PWM ramp with a dc offset is recommended to be added to the current sense signals. Electrical components RTA, RTB, CTA, CTB, DPA1, DPA2, DPB1, and DPB2 form a PWM ramp that is activated and deactivated by the gate drive outputs of the UCC28070. Resistor ROA and ROB add a DC offset to the CS resistors (RSA and RSB).

Controller Part





Title	**1 KW Interleaved Boost PFC using UCC28070A Controller** **PEMD Lab** **Aaqib Sheikh Muhammad**	
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