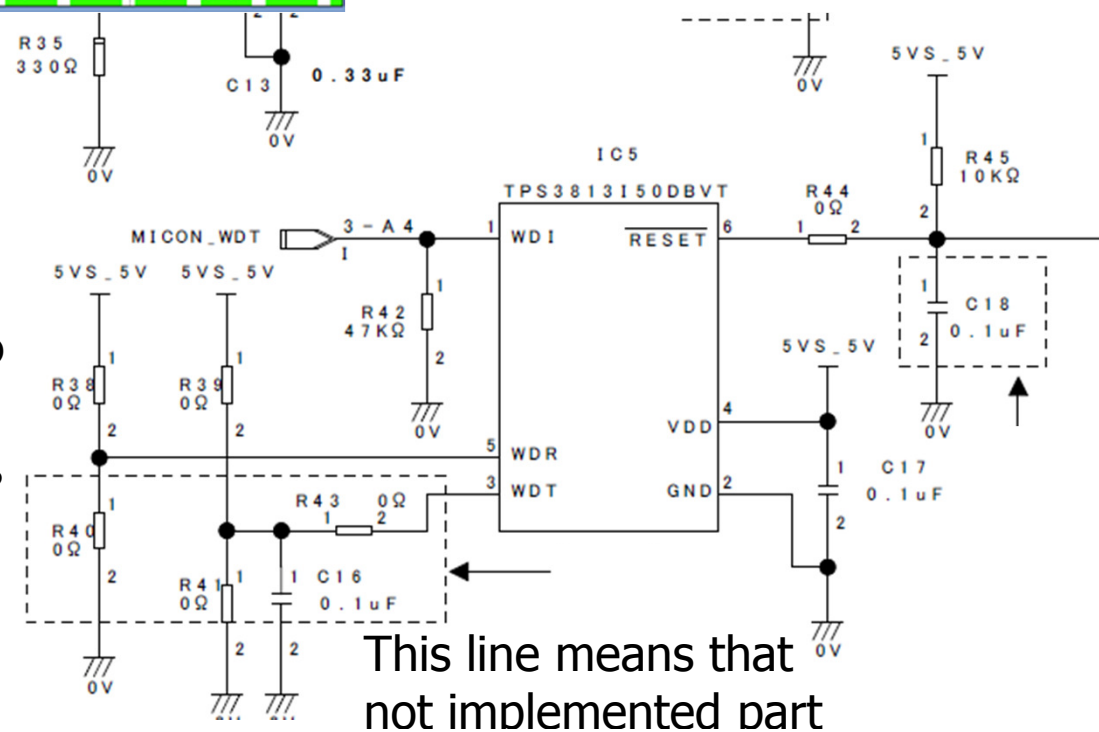


<Question>

Above figure, even though WDI is low, but Reset is toggled ON –OFF with 22ms cycles.

So, usually, WDT has to be connected to GND or VDD or Capacitance. This case is OPEN.

Is there the problem relation to above figure?



This line means that not implemented part