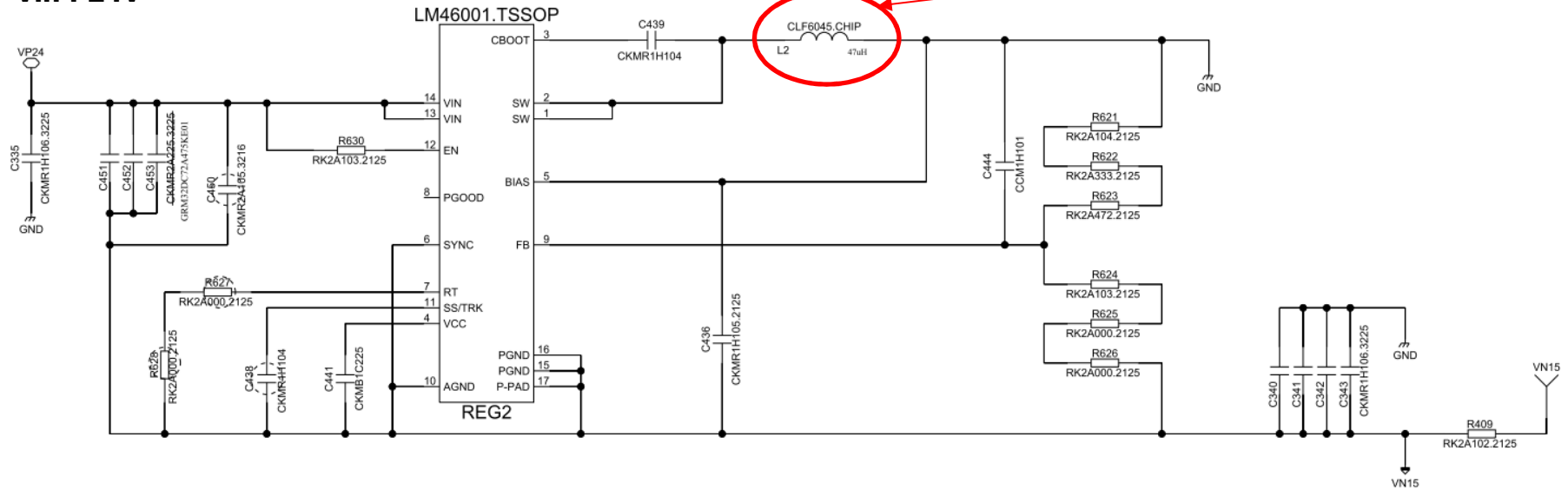


<The circuit of inverting DCDC using LM46001>

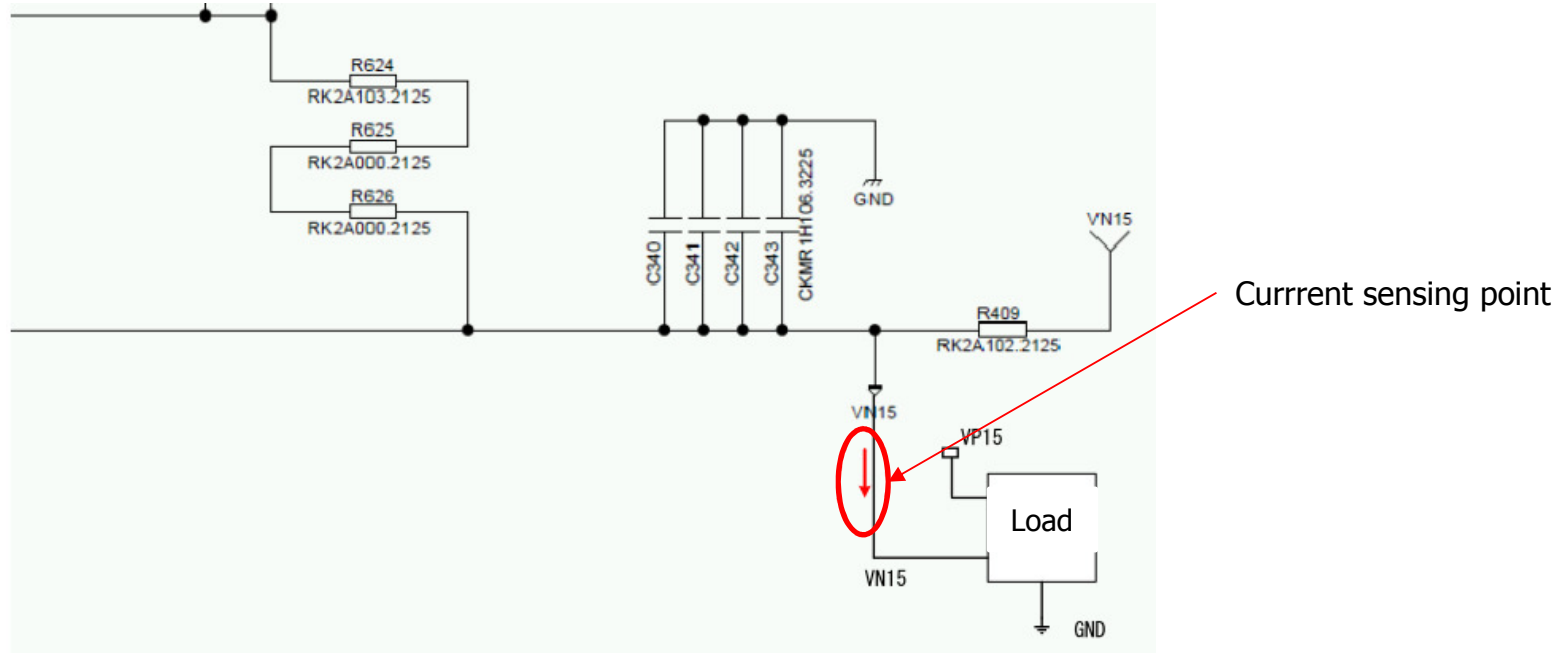
TDK CLF6045-470(47uF, IDC1=0.90A, IDC2=1.3A)
<http://www1.futureelectronics.com/doc/TDK/CLF6045T.pdf>

-Vin : 24V

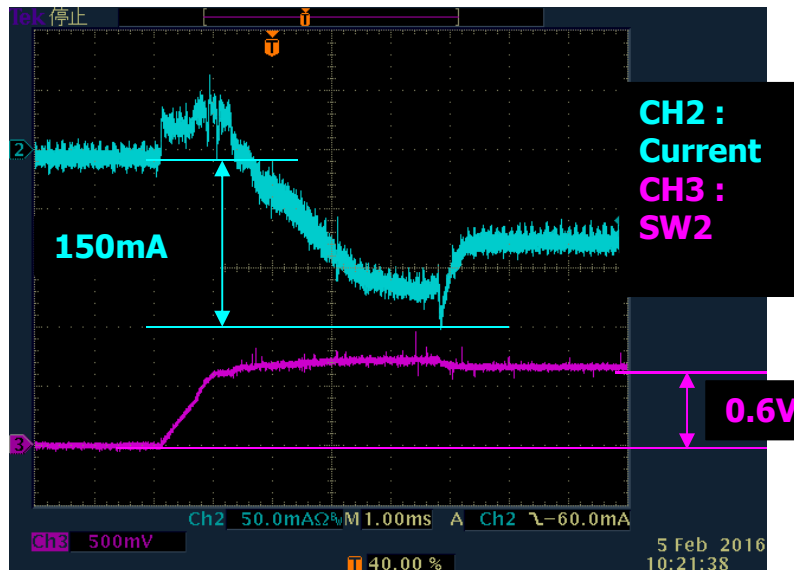


-Vout=-15V
-Iout=0.2A~0.3A
(Load)

<The point of current sensing>



<The waveform of current sensing and SW2>



<Question>

On the our customer's inverting DCDC circuit, we don't really know why, however after immediately after startup(1ms), the voltage increased to +voltage. (Originally, it goes to -voltage as your reference design document "SNVA722B".)

And then, there are two patterns as followings;

- ① If SW2 doesn't increase XXV within 1ms, the SW voltage goes to -15V.
- ② If SW2 is over XXV within 1ms, the SW voltage fixed 0.6V.

※ About XXV, we can't confirm it exactly.

So, if you have some advice, could you let us know?