<Customer's circuit>



## <Customer's questions>

Our customer would like to confirm the operation in case of failure mode like as follows; (We don't have EVM, therefor we could not confirm these.) If you have some knowledge for these, could you share us?

- 1. In case of C2=open, does the device temperature fall within the range less than 100C?
- 2. In case of C3=short, does the R2 temperature fall within the range less than 100C?
- 3. In case of D1=short, does the device temperature fall within the range less than 100C?
- 4. In case of short between VBAT pin and SW pin, do the device and R2 temperature fall within the range less than 100C?
- 5. In case of short between VBAT pin and VO pin, do the device and R2 temperature fall within the range less than 100C?
- 6. In case of short between VBAT pin and Cin pin, do the device and R2 temperature fall within the range less than 100C?
- 7. In case of PGND pin=open only, does the device temperature fall within the range less than 100C?
- 8. In case of GND pin=open only, does the device temperature fall within the range less than 100C?
- 9. In case of DCTRL pin=open only, what does the device behave?
- If the DCTRL pin has pull-down, we assume that the device doesn't operate only.
- 10. In case of L1=short, does the device temperature fall within the range less than 100C?

As the background of these questions, if some failures occur, they would like to protect temperature rise within 100C using fuse or device function.