

Type C Switch

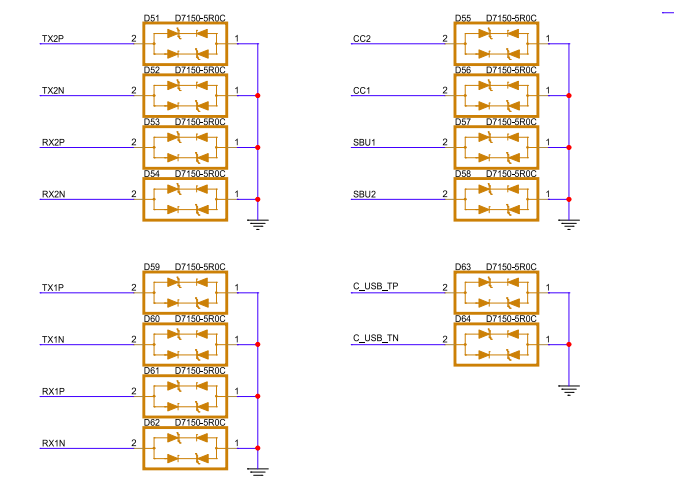
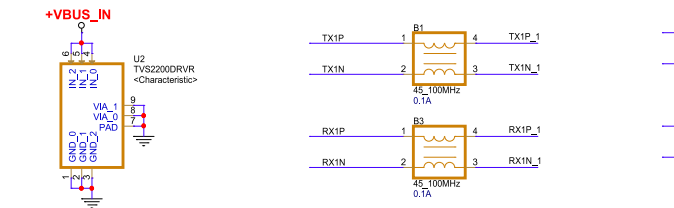
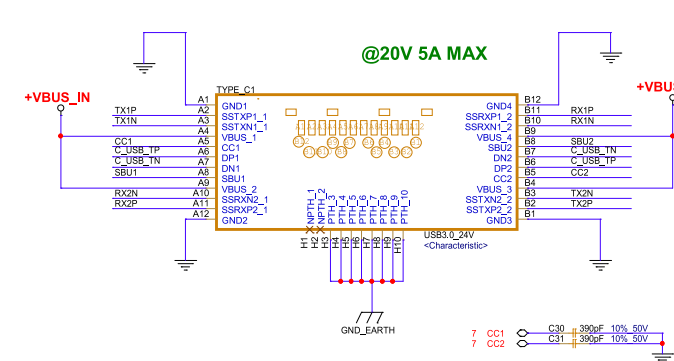
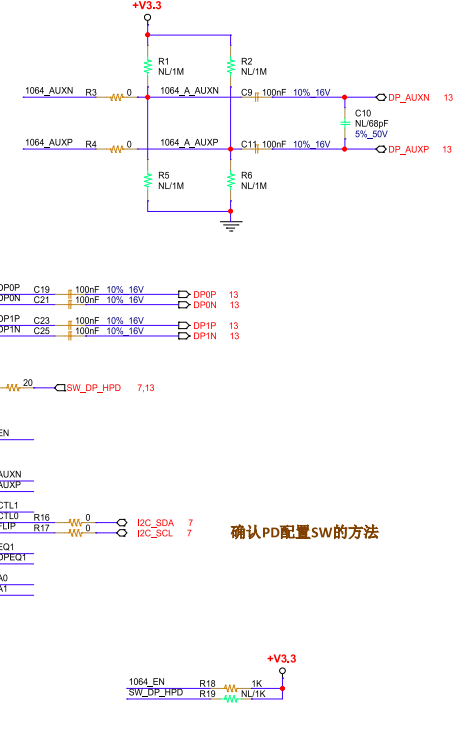
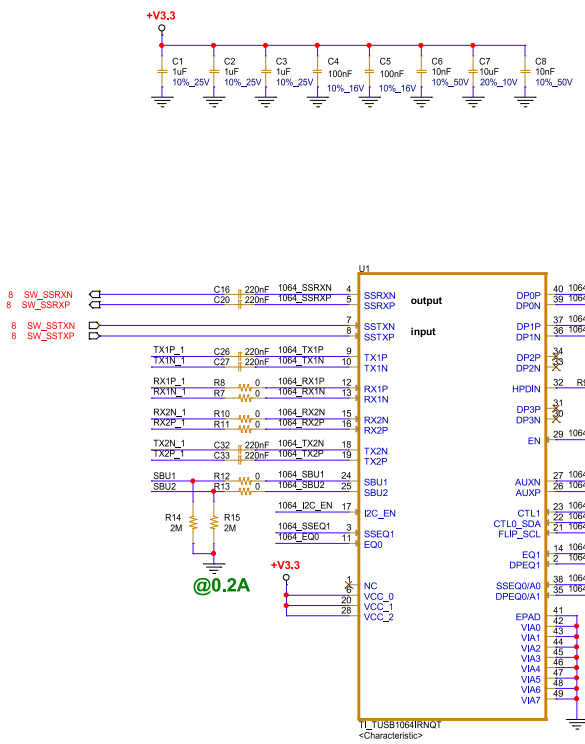


Table 3. GPIO AUXp or AUXn to SBU1 or SBU2 Mapping

CTL1 PIN	FLIP PIN	MAPPING
H	L	SBU1 → AUXn SBU2 → AUXp
H	H	SBU2 → AUXn SBU1 → AUXp
L > 2 ms	X	Open

LEVEL	SETTINGS
0	Option 1: Tie 1 KΩ 5% to GND. Option 2: Tie directly to GND.
R	Tie 20 KΩ 5% to GND.
F	Float (leave pin open)
1	Option 1: Tie 1 KΩ 5% to V _{CC} . Option 2: Tie directly to V _{CC} .



确认PD配置SW的方法

Table 9. TUSB1064 I²C Target Address

DPEQ0/A1 PIN LEVEL	SSEQ0/A0 PIN LEVEL	Bit 7 (MSB)	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0 (LSB)
0	0	1	0	0	0	1	0	0	01
0	R	1	0	0	0	1	0	1	01
0	F	1	0	0	0	1	1	0	01
0	1	1	0	0	0	1	1	1	01
R	0	0	1	0	0	0	0	0	01
R	R	0	1	0	0	0	0	1	01
R	F	0	1	0	0	0	1	0	01
R	1	0	1	0	0	0	1	1	01
F	0	0	0	1	0	0	0	0	01
F	R	0	0	1	0	0	0	1	01
F	F	0	0	1	0	0	1	0	01
F	1	0	0	1	0	0	1	1	01
1	0	0	0	0	1	1	0	0	01
1	R	0	0	0	1	1	0	1	01
1	F	0	0	0	1	1	1	0	01
1	1	0	0	0	1	1	1	1	01

I2C_EN

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4 Level I

I²C Programming Mode or GPIO Programming Select.
0 = GPIO mode (I²C disabled)
R = T1 Test Mode (I²C enabled at 3.3 V)
F = I²C enabled at 1.8 V
1 = I²C enabled at 3.3 V.

Type C PD

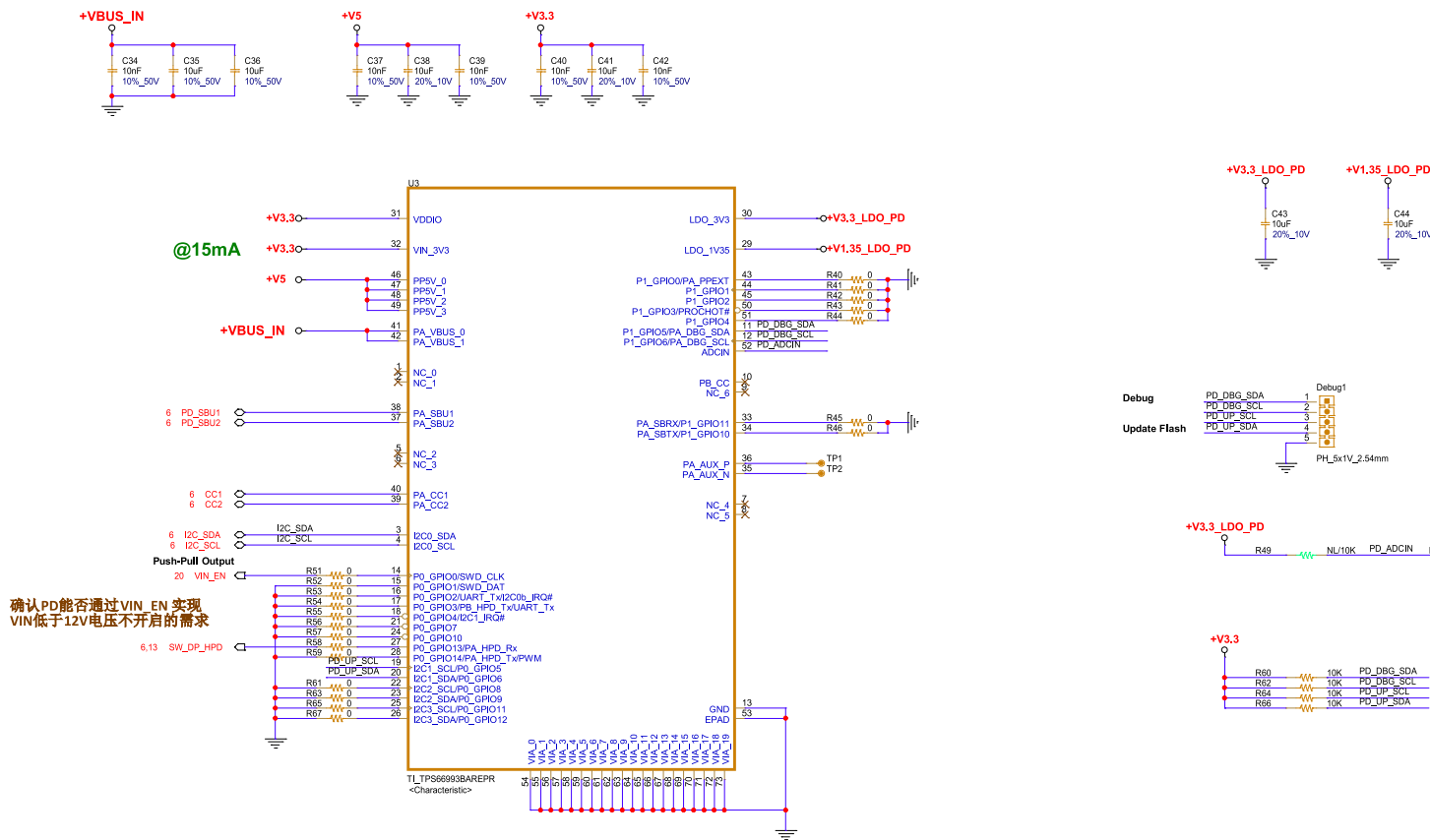


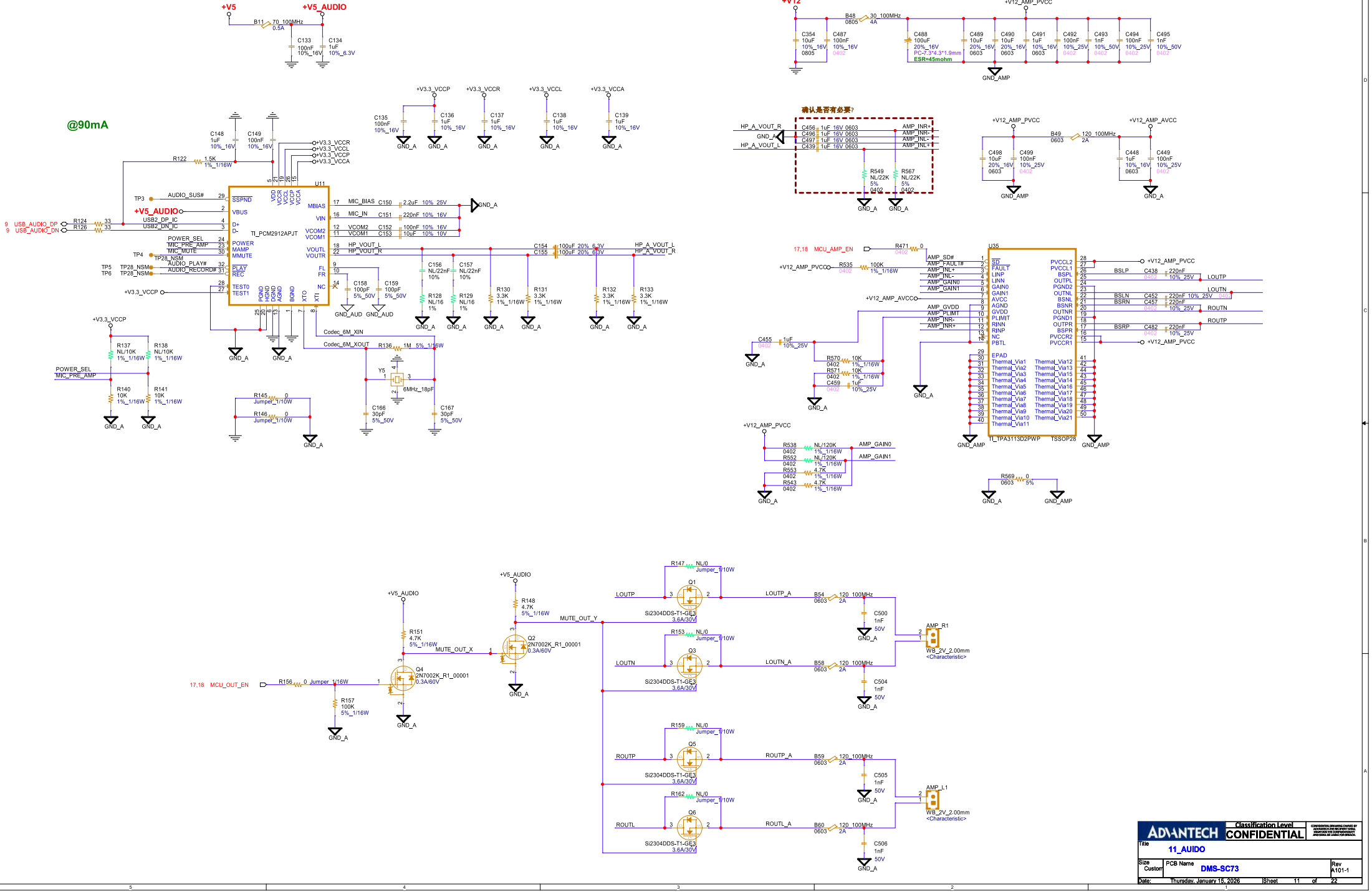
Table 8-17. ADCIN Default Behavior

Feature	ADCIN Decoded Value			
	ADCIN[3]	ADCIN[2]	ADCIN[1]	ADCIN[0]
Dead-Battery Configuration (only applicable if booting while dead-battery flag is asserted)				
SinkDisabled→ Follow flash configuration The device does not enable the sink path during BOOT mode. After entering the APP mode the device behaves based on the configuration stored in flash. This option is recommended since it provides the most flexible solution.	x	x	0	0
AlwaysEnableSink The device always enables the sink path regardless of the amount of current the attached source is offering as long as the voltage does not exceed the expected max for a 5V contract. USB PD is disabled until configuration is loaded. Do not use for sink paths that have active-low enable or systems that require more than 5V to power-on.	x	x	0	1
SinkRequires_1.5A The device enables the sink path if the attached source is offering at least 1.5A and as long as the voltage does not exceed the expected max for a 5V contract. USB PD is disabled until configuration is loaded. Do not use for sink paths that have active-low enable or systems that require more than 5V to power-on.	x	x	1	0
SinkRequires_3.0A The device enables the sink path if the attached source is offering 3.0A and as long as the voltage does not exceed the expected max for a 5V contract. USB PD is disabled until configuration is loaded. Do not use for sink paths that have active-low enable or systems that require more than 5V to power-on.	0	x	1	1

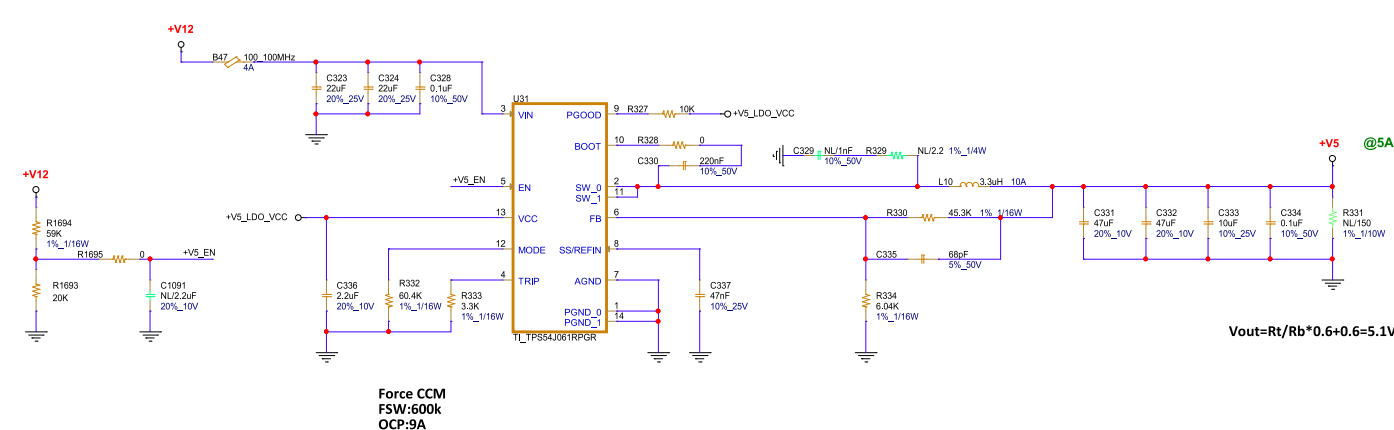
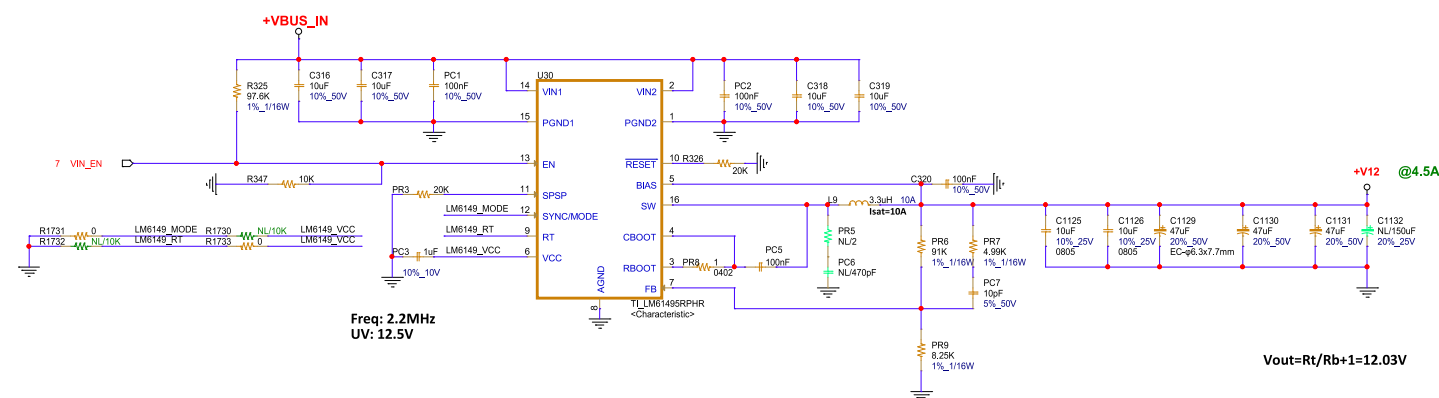
Table 8-16. ADCIN Decoding

Target R _{DI}	R _{UP} ⁽¹⁾	R _{DOWN} ⁽¹⁾	ADCIN decoded value			
			ADCIN[3]	ADCIN[2]	ADCIN[1]	ADCIN[0]
0	N/A	10kΩ	0	0	0	0
0.0497	348kΩ	18.2kΩ	0	0	0	1
0.0967	249kΩ	26.7kΩ	0	0	1	0
0.1540	340kΩ	61.9kΩ	0	0	1	1
0.2230	309kΩ	88.7kΩ	0	1	0	0
0.3065	267kΩ	118kΩ	0	1	0	1
0.4051	210kΩ	143kΩ	0	1	1	0
0.5238	130kΩ	143kΩ	0	1	1	1
0.6618	97.6kΩ	191kΩ	1	0	0	0
0.8190	59kΩ	267kΩ	1	0	0	1
1	10kΩ	N/A	1	0	1	0

Audio



POWER1



POWER2

