

Schematic

5.2 Schematic and Bill of Materials

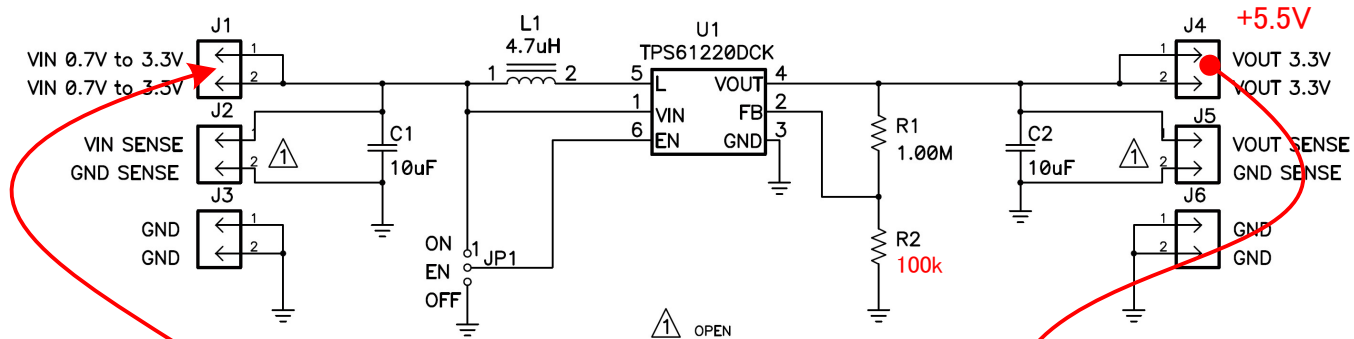
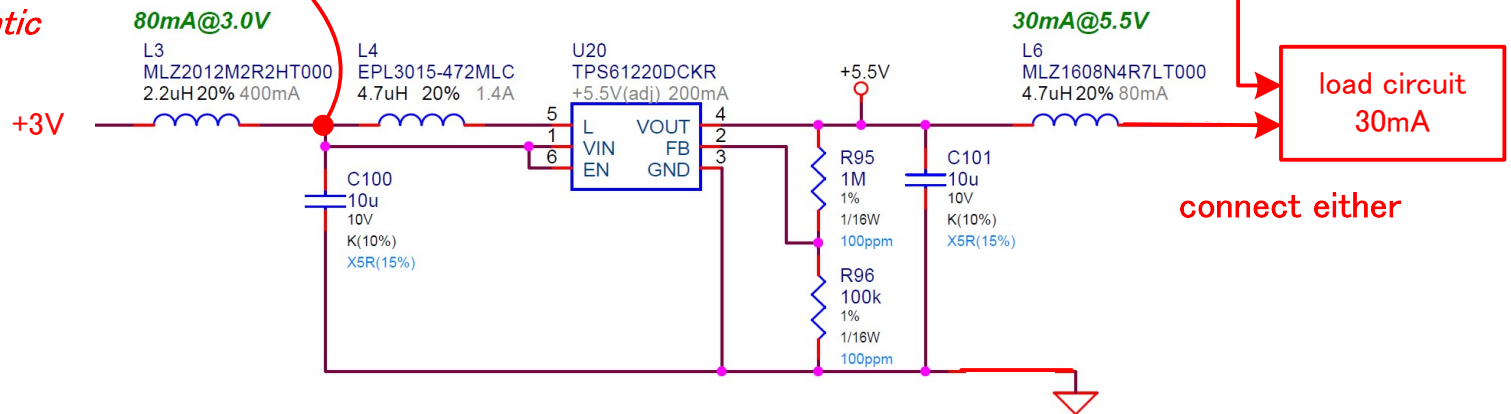


Figure 4. TPS61220EVM-319 Schematic

Our schematic



Layout

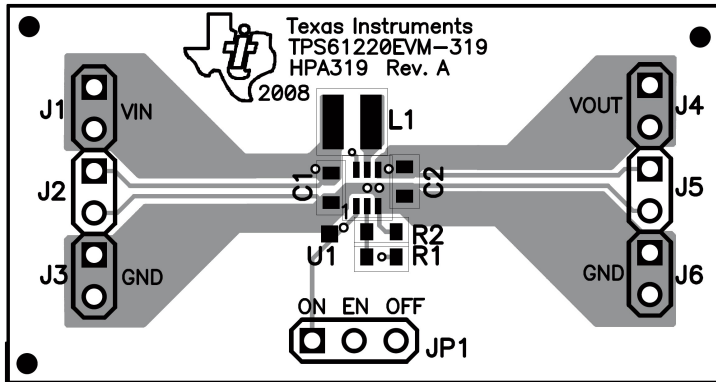


Figure 1. Top Assembly Layer

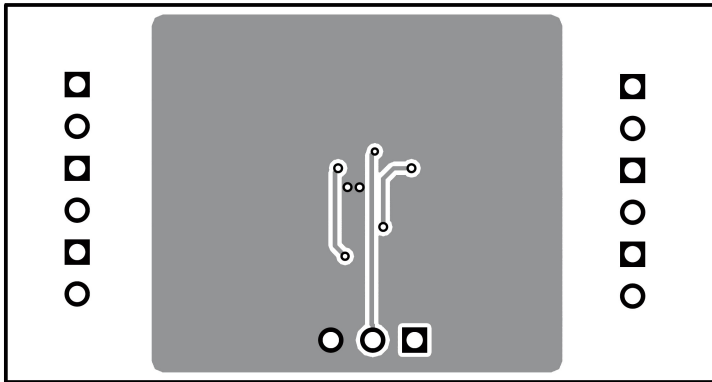
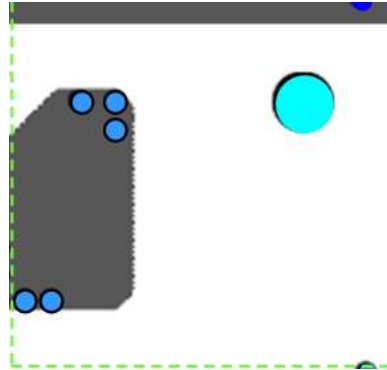
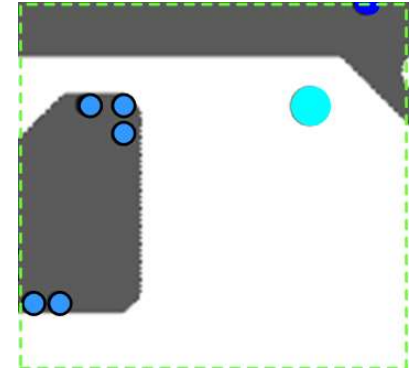


Figure 3. Bottom Layer

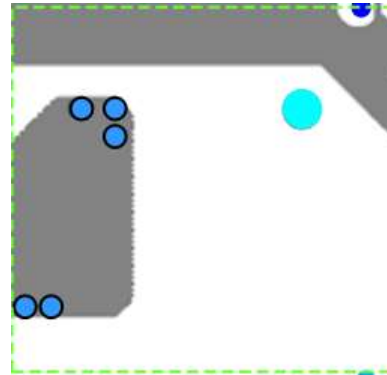
L1



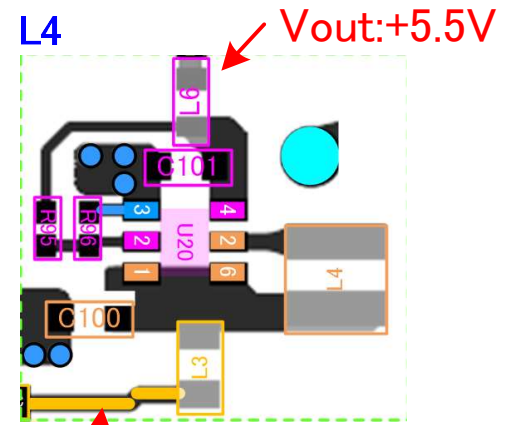
L2



L3



L4



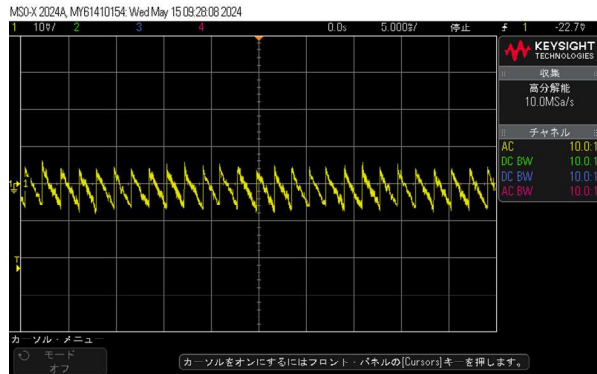
Vin:+3V

L4:bottom view

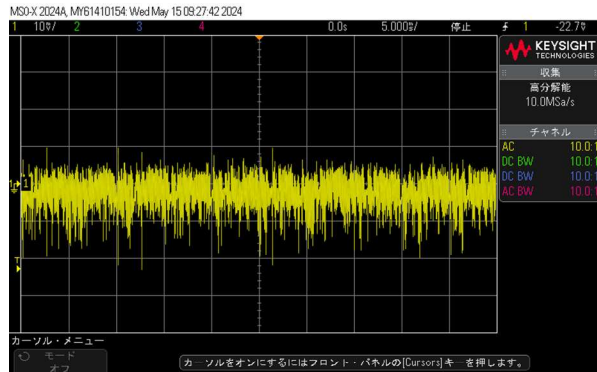
Waveform

TPS61220EVM-319

No load

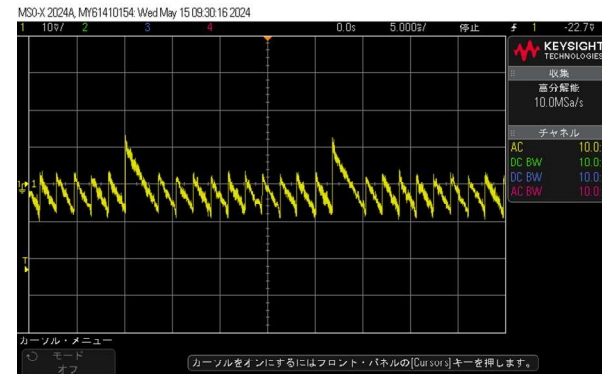


Actual load



our circuit

No load



Actual load

