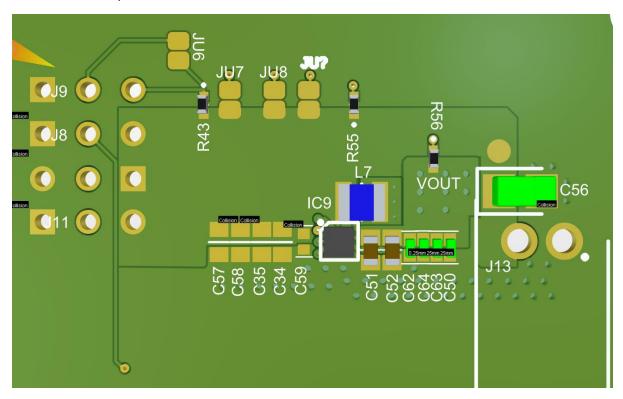
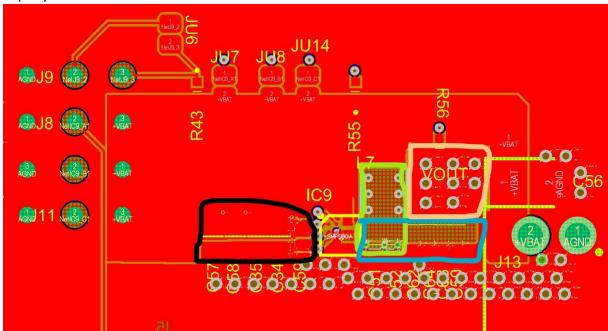
3D View of the Layout



Top Layer



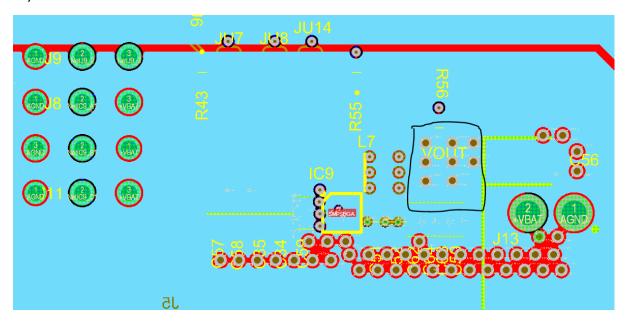
The area in Black is the Input capacitors (being fanned-out to the ground with VIAs)

The green area is the switching area which is only connected to L7 and C3,C4 (switching nodes)

The Blue area is where the output capacitors are connected (also fanned-out to the ground with VIAs)

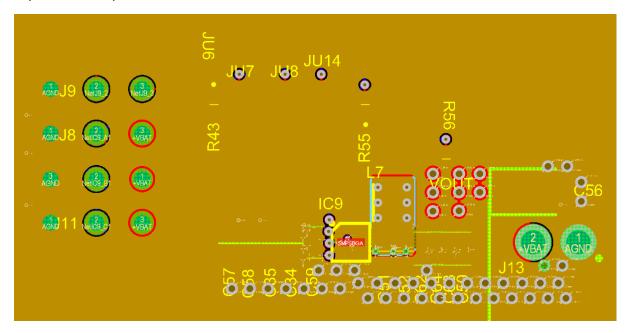
The Beige area is the Voltage Output area being fanned out Layer 3 – The Power Plane

Layer 3



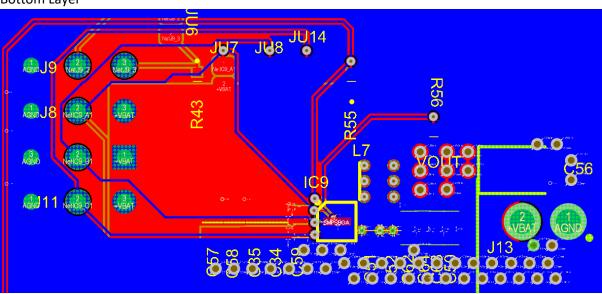
The black area is where the Vout is fanned out

Layer 2 – Mid Layer



Under L7 is the switching polygon

Bottom Layer



Ground Layer