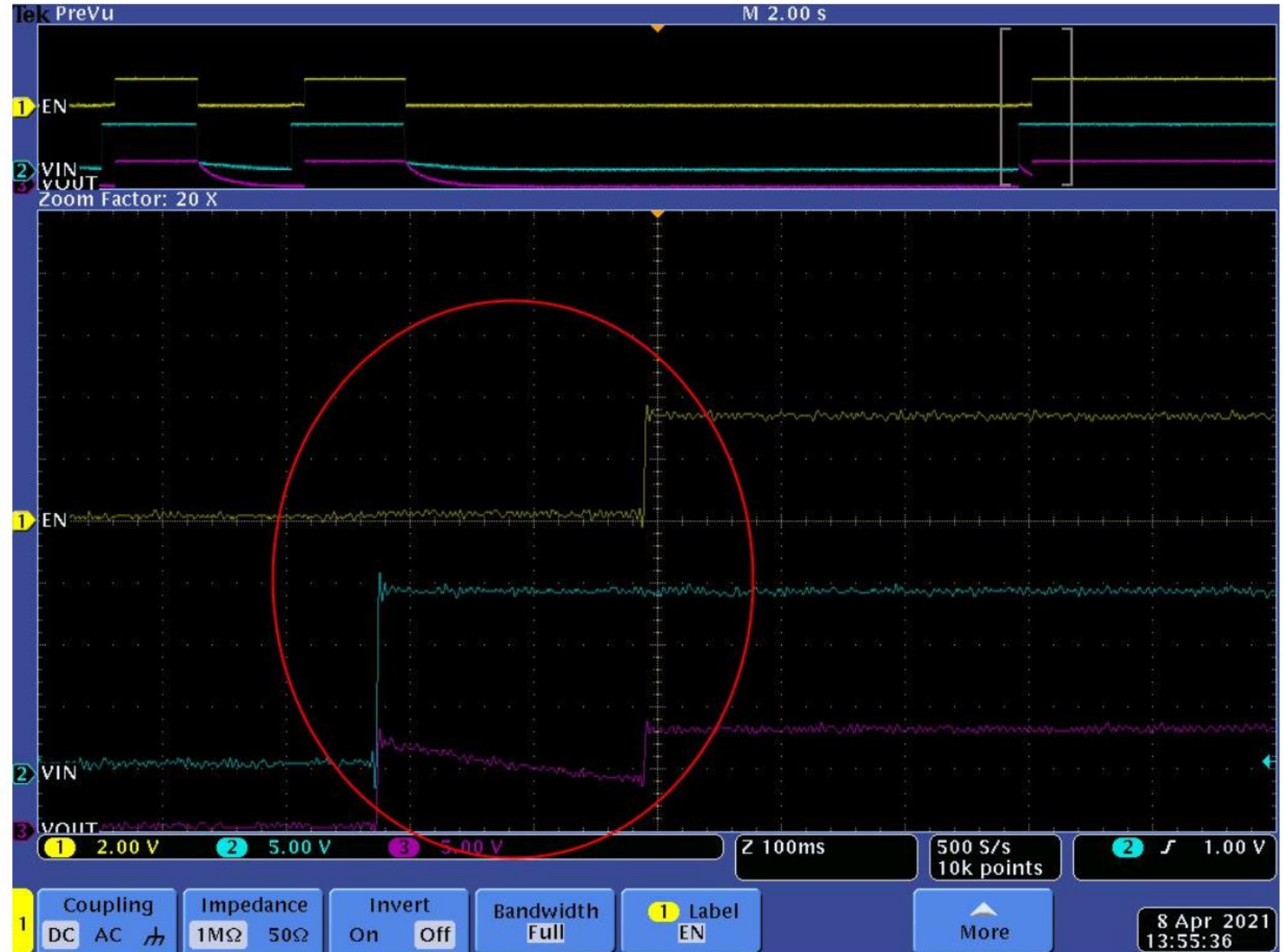
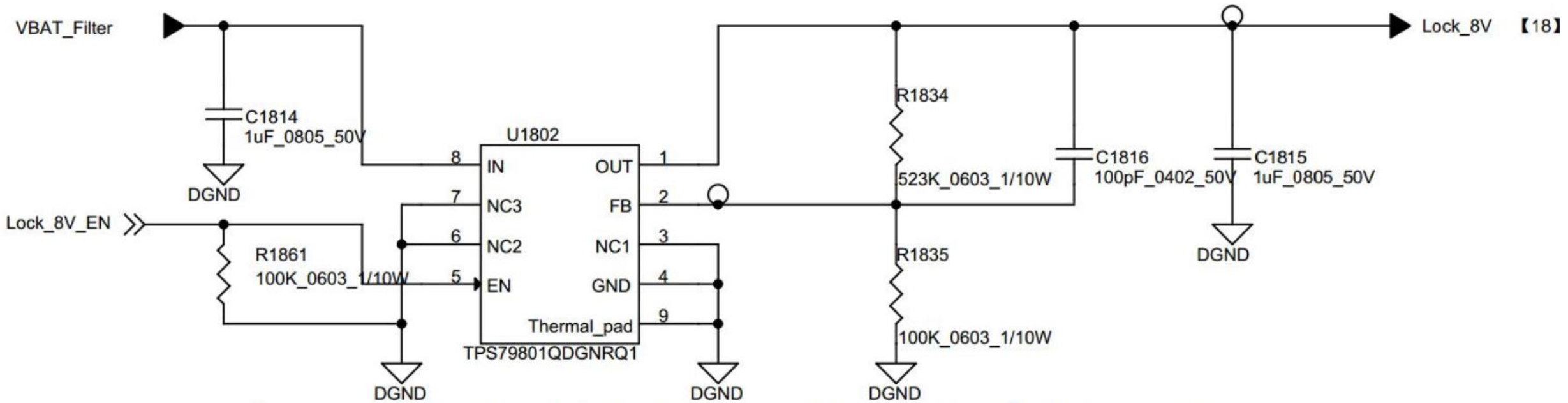


VOUT is existed before EN enabled, can you explain?





$$V_{OUT} = [(523 + 100) / 100] * 1.275 + (523 / 5000) = 8V$$



**Jason Song** 2天前

 TI\_Mastermind 20210 points

Hi Lin,

How fast is the Vin rising in the scope-shot? It seems to me that the rising edge of Vin is coupling to the output of the LDO. One way to confirm this is to try to create a slower ramp on the Vin of the LDO and observe if Vout is still doing this. Do you think that is something you could try on your board?

In addition, is there any other possible charging path that could be turned on with the input of the Vin?

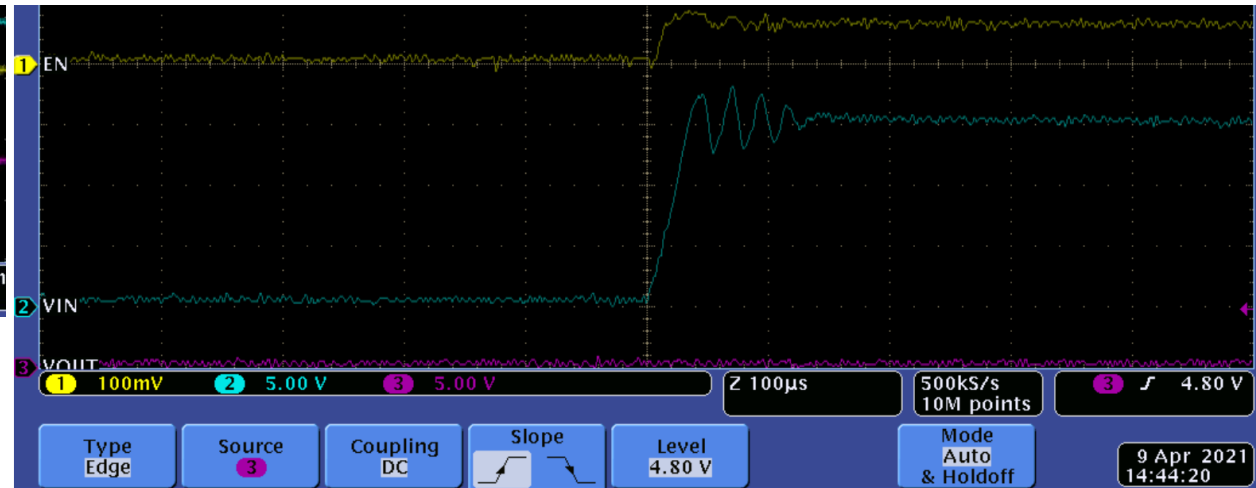
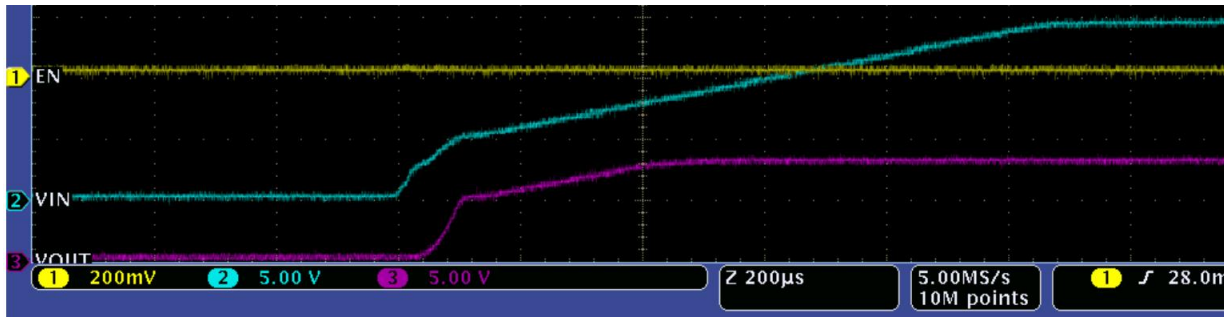
Do you have any type of load on the output during the startup? If so, what's the load?

Regards,

Jason Song

[SL] To verify Vin rising, power net(VBAT\_filter) and load are now removed. LDO Vin is supplied directly through 15V DC source and Cin. Results are listed below:

Cin (uf)	Vin rising	Vout existed before EN enabled?
4700	12 ms	Not existed
500	1 ms	Existed, see left figure
47	150 us	Not existed, see right figure



[SL] Cin seems critical, can you explain this? Are there any application hints about Cin selection?

Can you share Vih,min and Vil,max of EN pin?