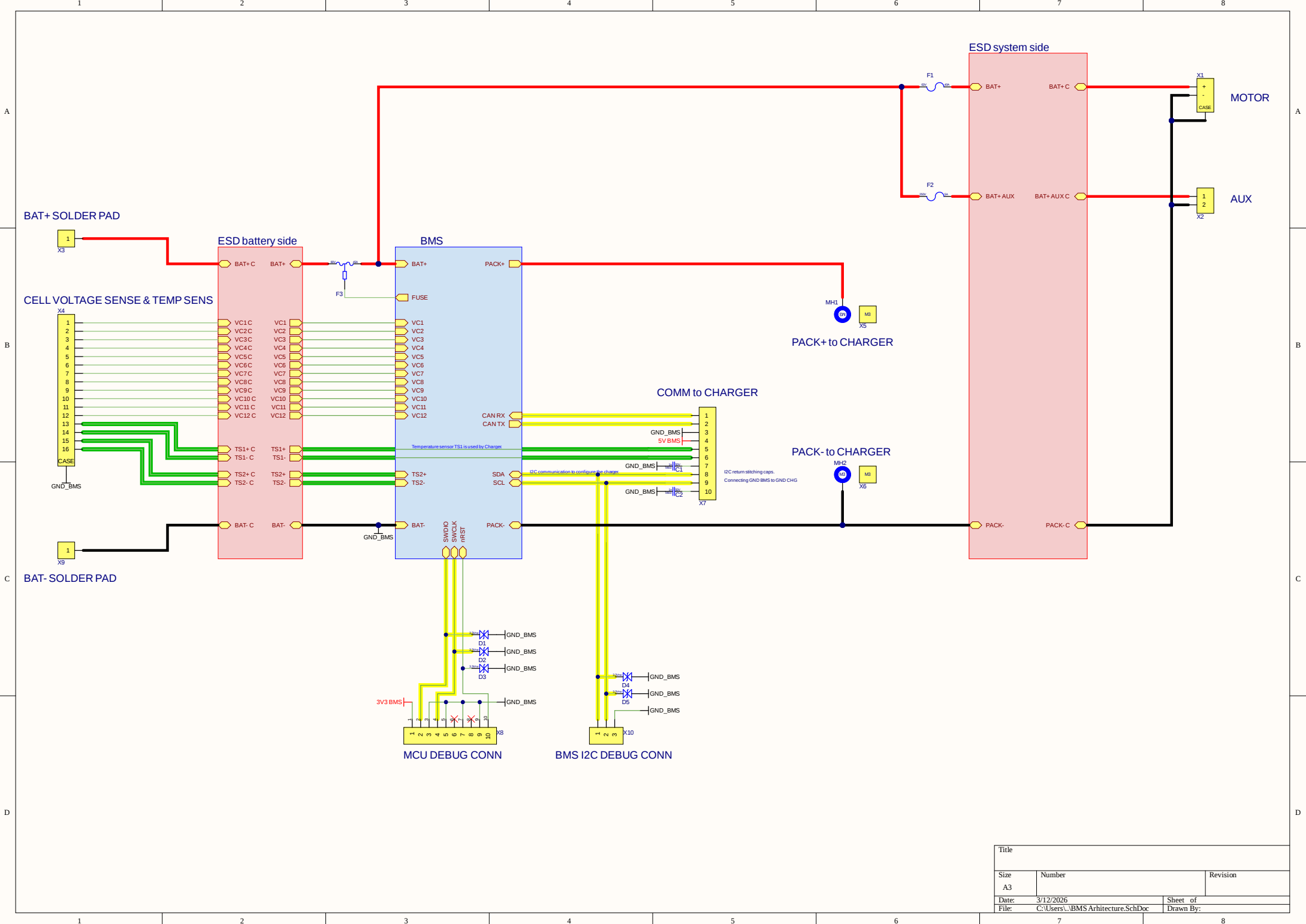
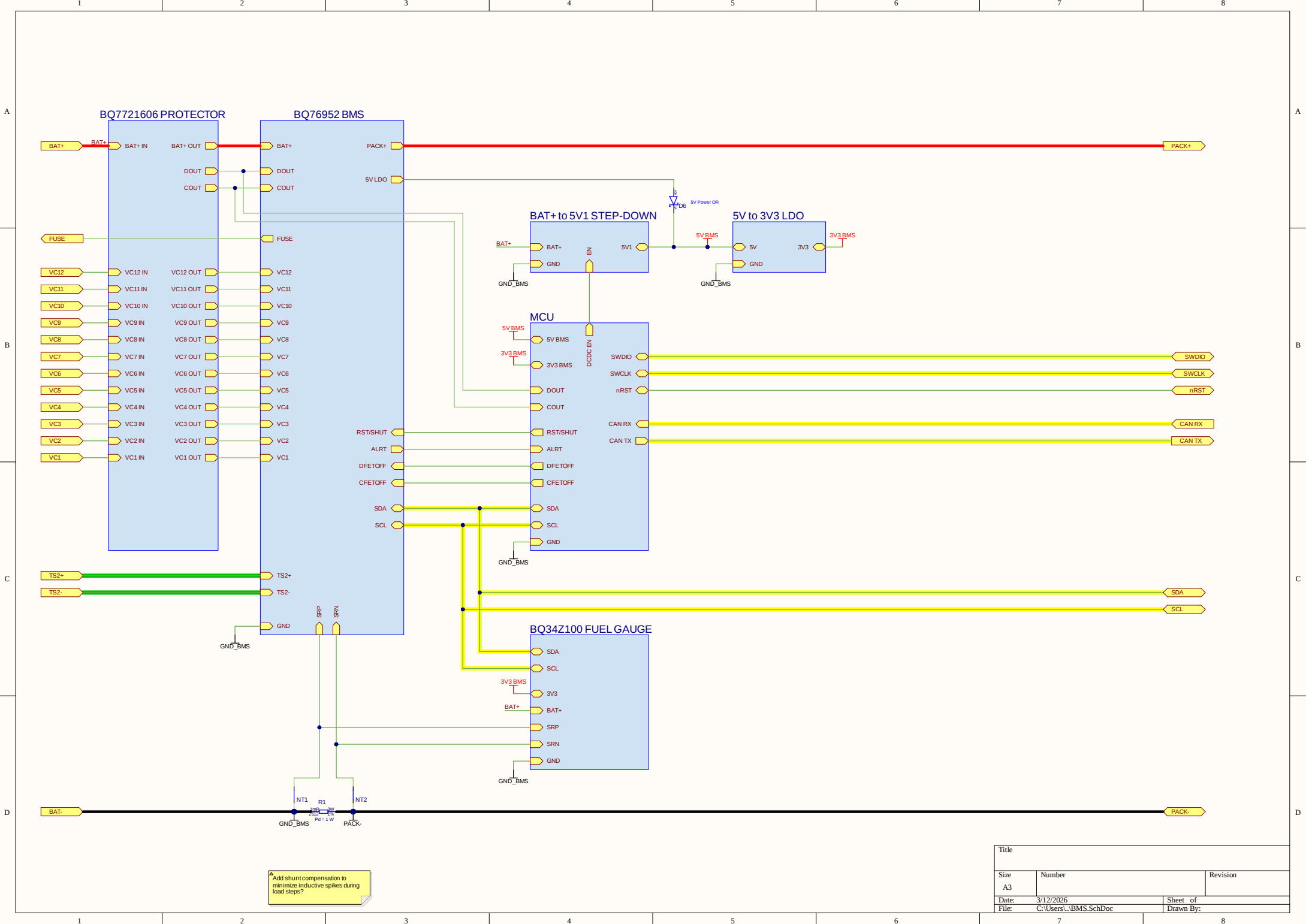




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File:	C:\Users\...\BMS Architecture.SchDoc	Drawn By:



Add shunt compensation to minimize inductive spikes during load steps?

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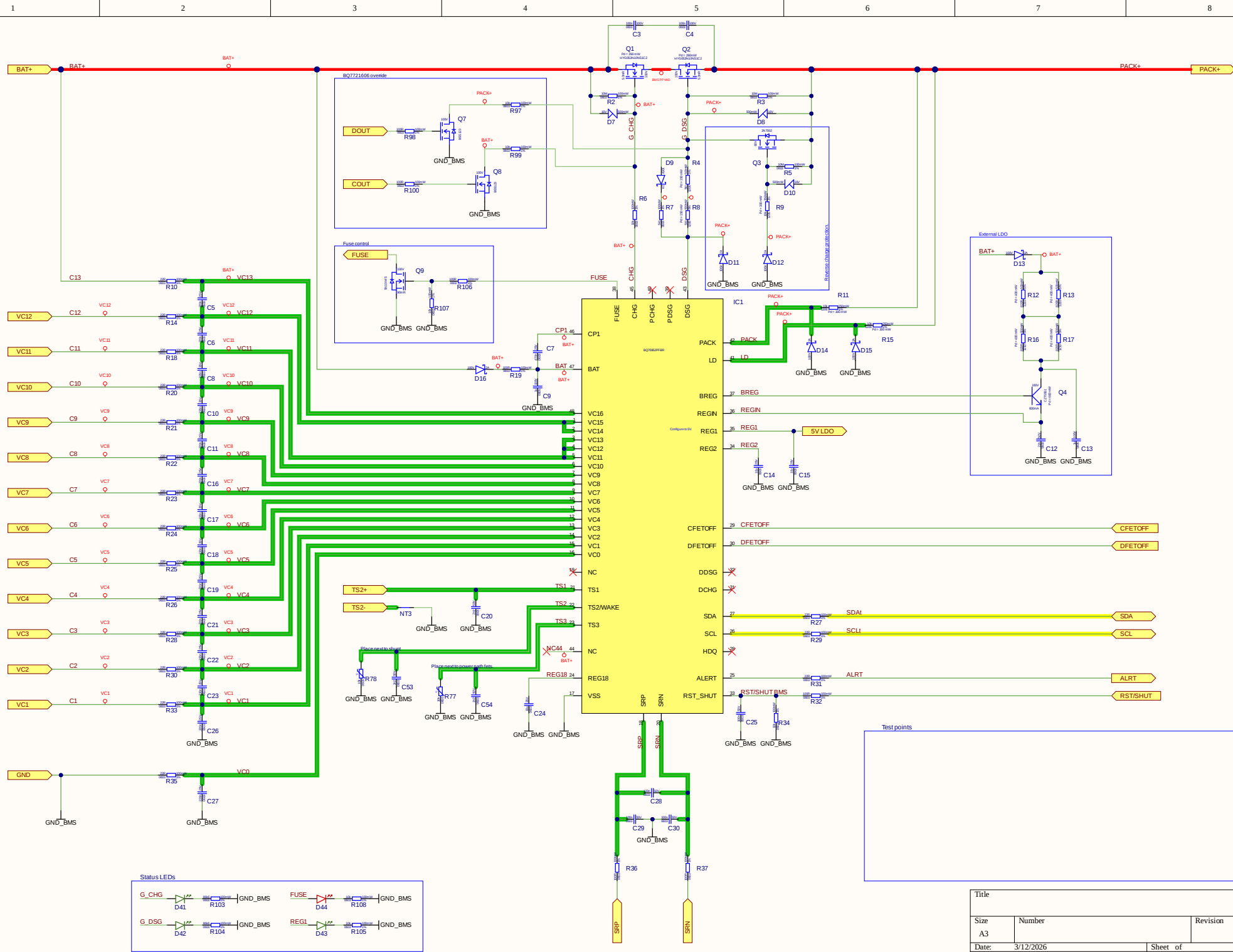
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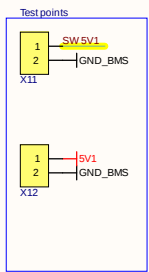
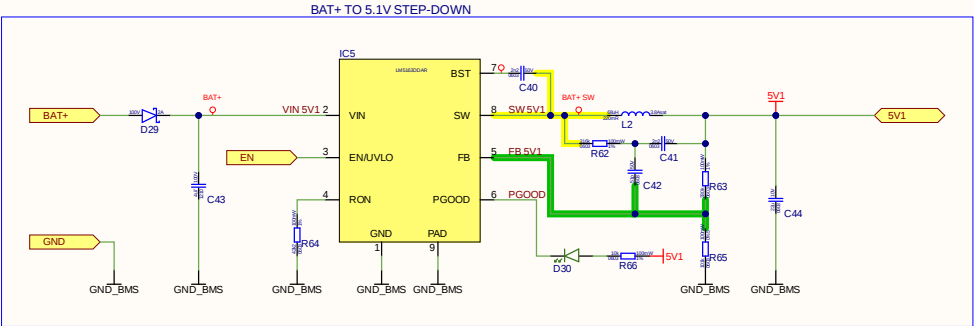
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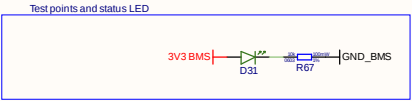
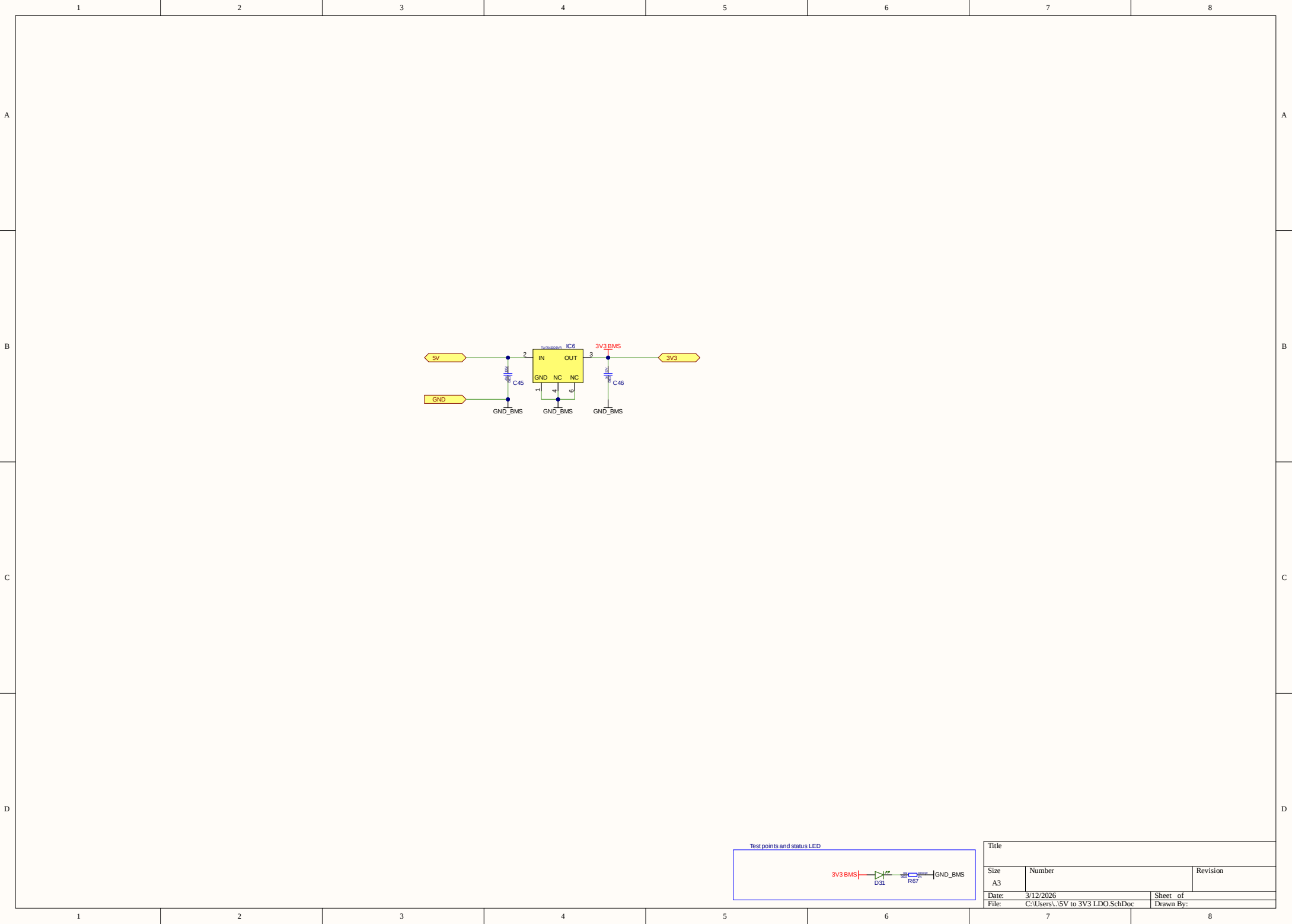
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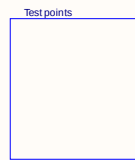
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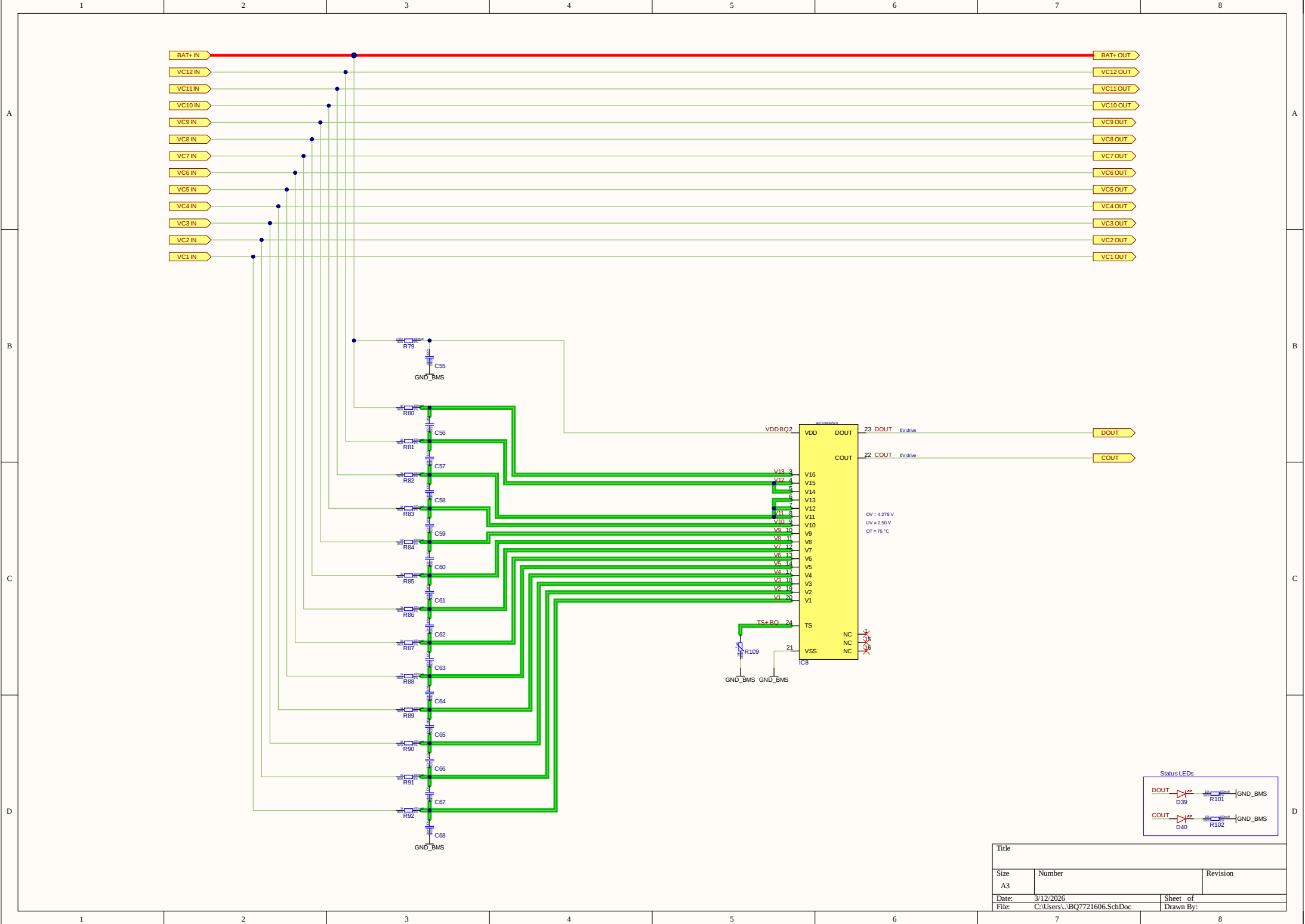


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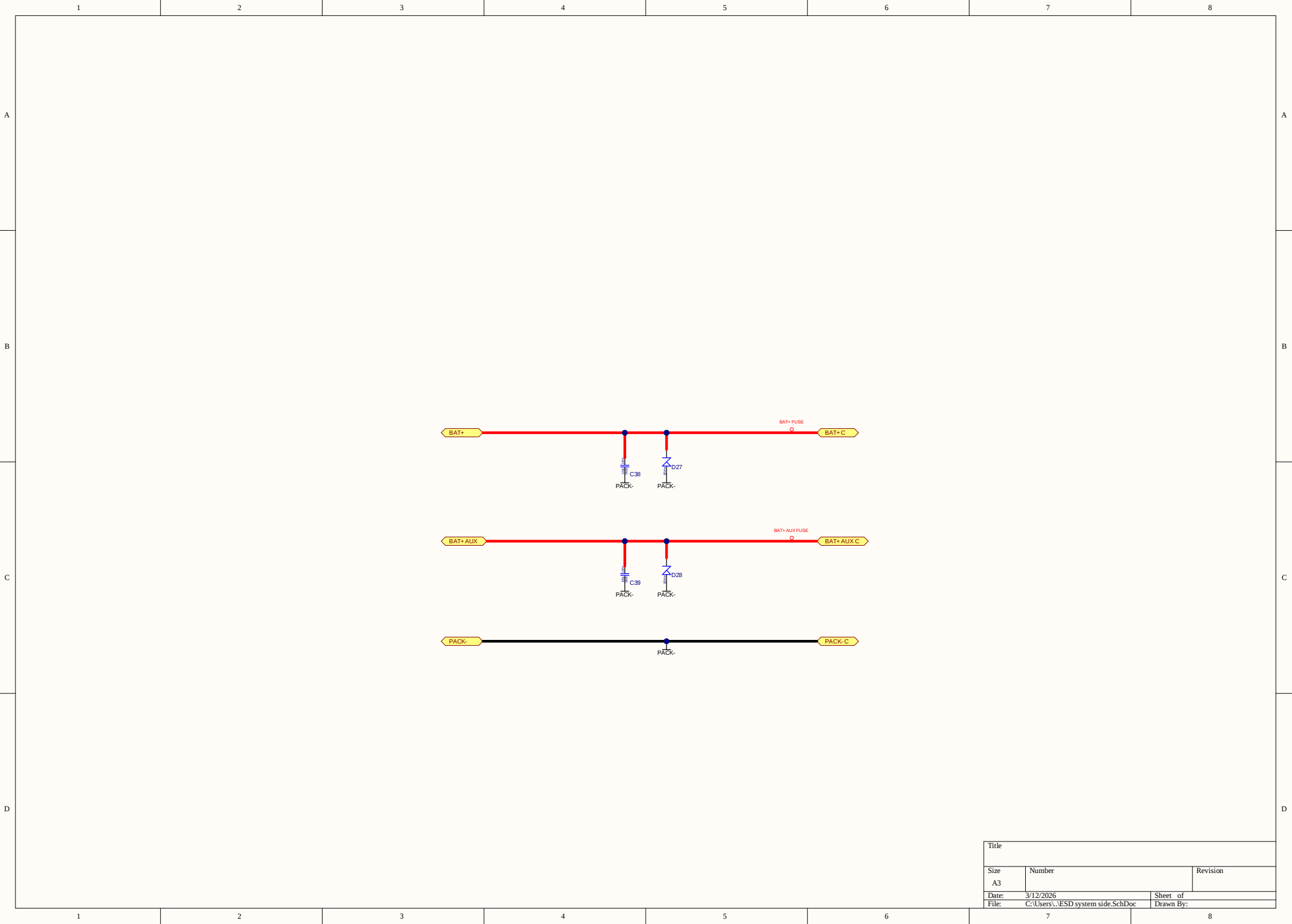


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Download By:



Title		
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Title		
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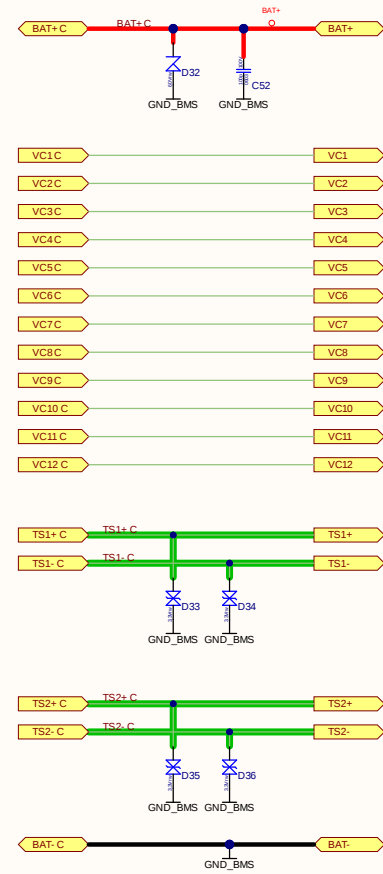
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Title		
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Date: 3/12/2026	Sheet of	
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