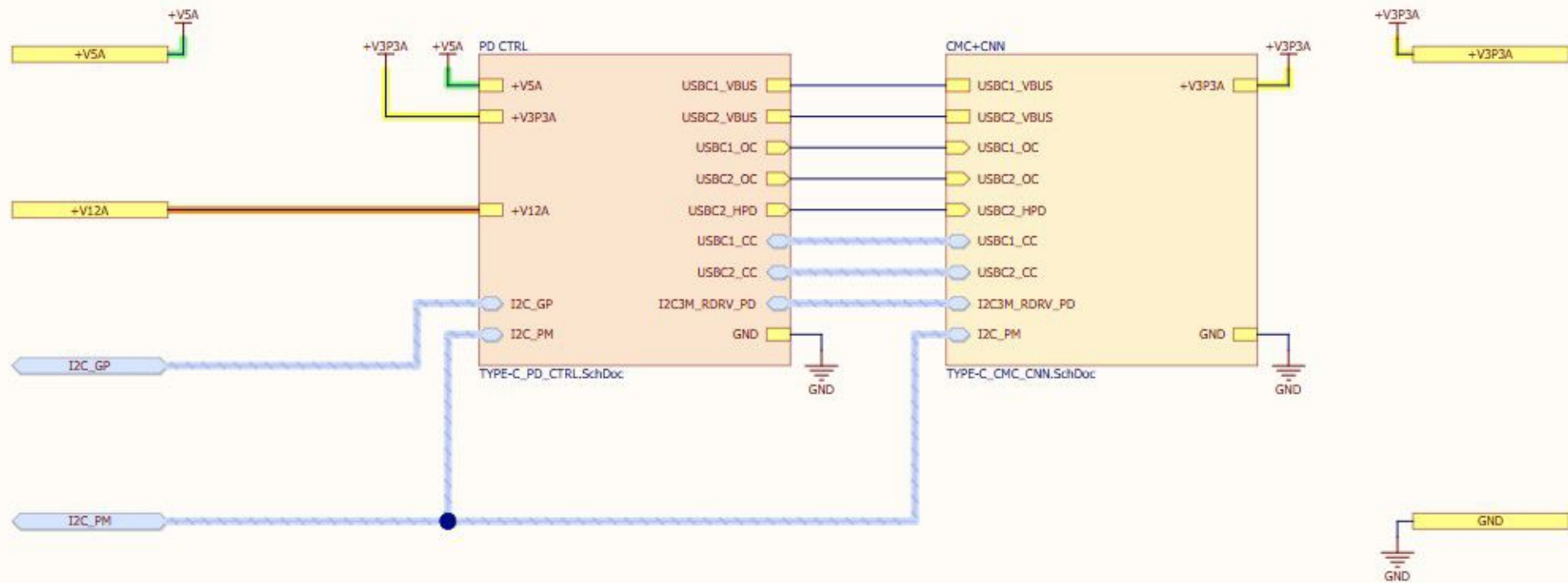
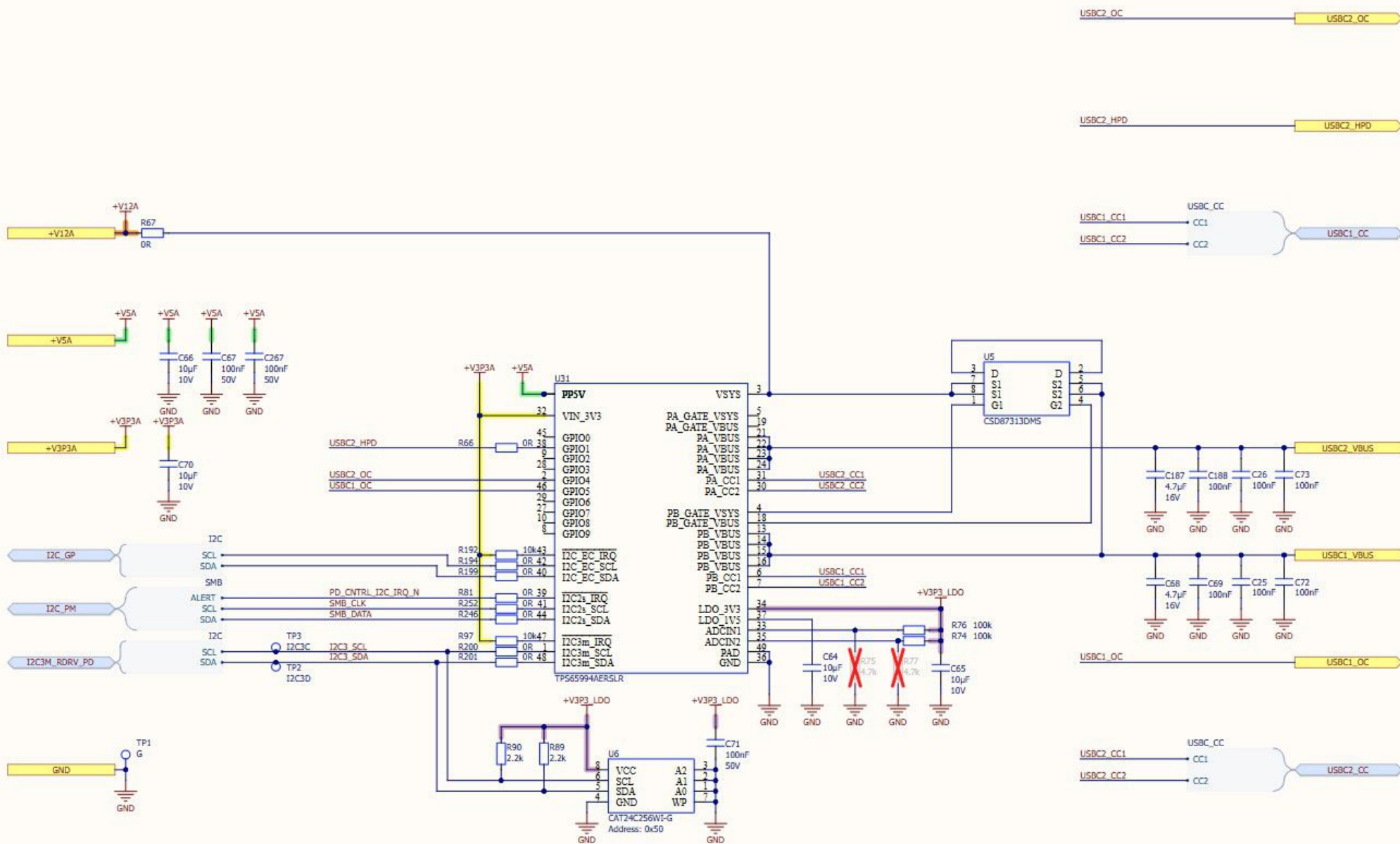
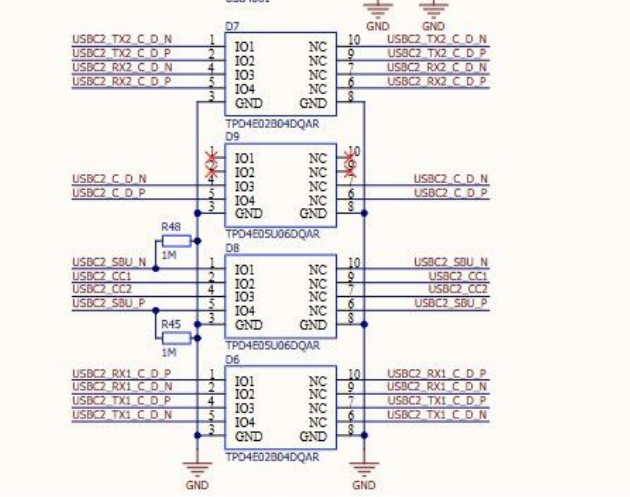
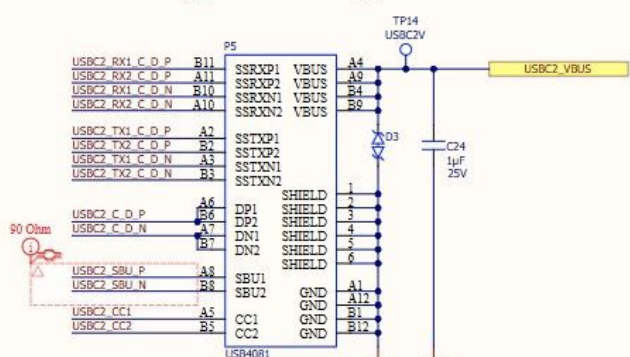
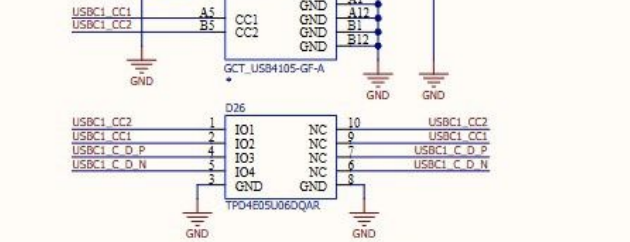
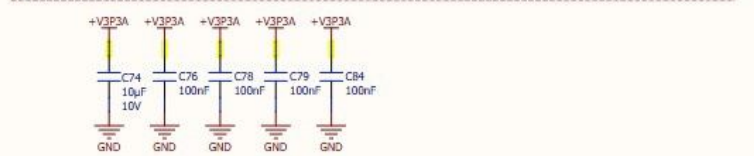
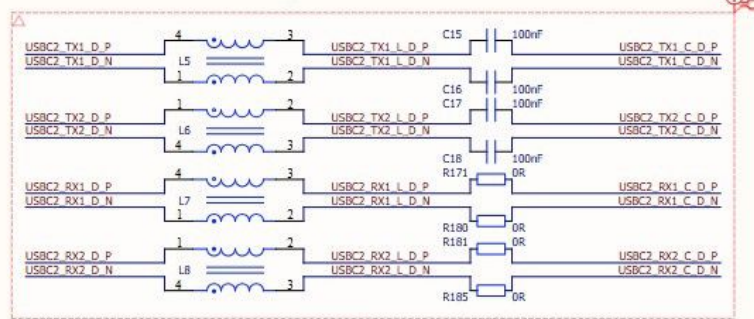
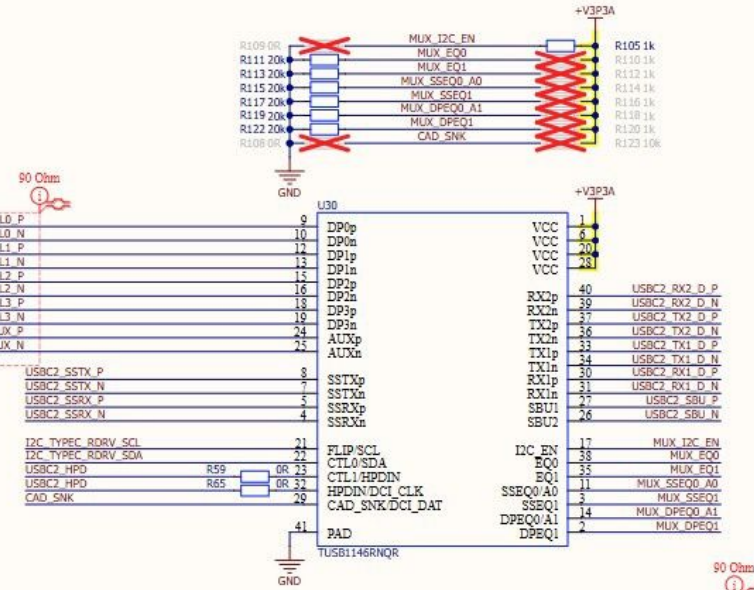
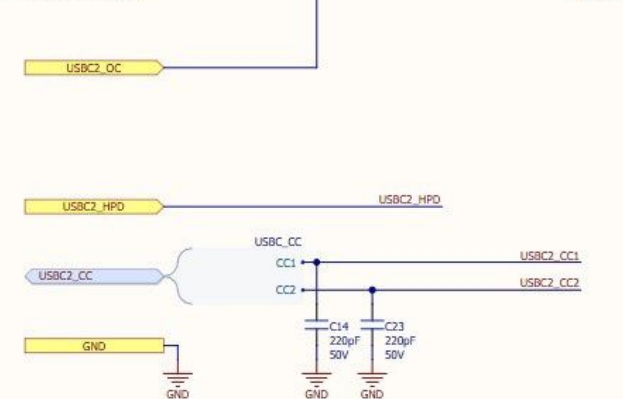
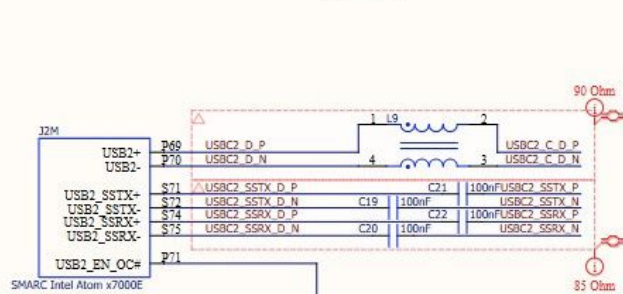
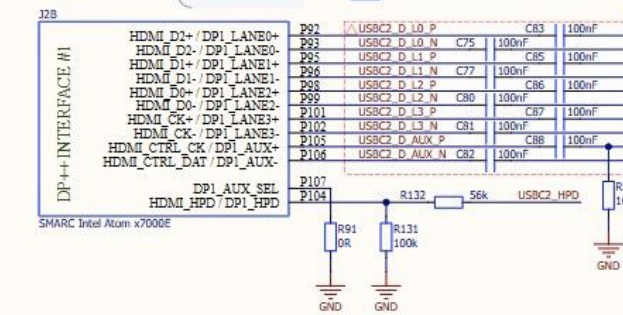
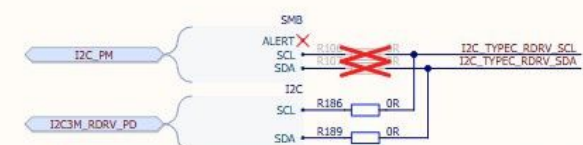
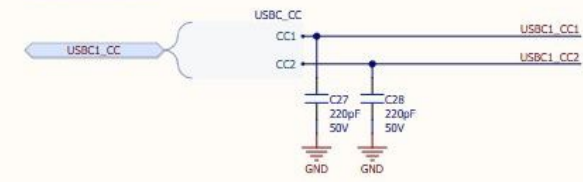
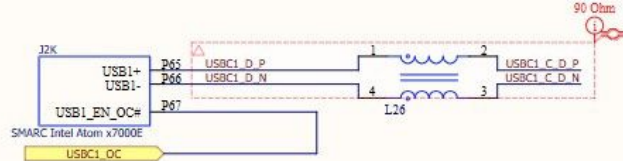








Configuration Mode

Enter the Register/Field Search Text Here

- Customer Use
- Interrupt Mask for I2C1
- Interrupt Mask for I2C2
- SX Config
- Set Sx App Config
- USB Config
- Global System Configuration
- Port Configuration
- Port Control
- Transmit Source Capabilities
- Transmit Sink Capabilities
- Autonegotiate Sink
- PD3 Configuration
- Delay Config
- Tx Identity
- User Alternate Mode Configuration
- Display Port Configuration
- Thunderbolt Configuration
- Prochot Configuration
- IO Config
- Retimer Debug Register
- APP Config Binary Data Indices
- I2C Controller Config
- App Config Register
- Sleep Control Register
- TX Manufacturer Info SOP
- TX Alert Data Object
- Tx Source Capabilities Extended Data Block
- Transmitted Status Data Block (SDB) Register
- Transmitted Battery Status Data Objects (BSDO) Register
- Tx Battery Capabilities
- Transmit Sink Capabilities Extended Data Block
- Raw View

Display Port Configuration (0x51)					
Field			Value		
Enable DP SID			☒		
DP Mode			☒		
DP Transport Signalling			Supports DP signaling rates and electrical specification		
USB2 Signalling Not Used			May be required		
DFPD Pin Assignment			A B C D E F		
Multi Function Preferred			☐		
DP Mode Auto Entry Allowed			☒		

Configuration Mode

Enter the Register/Field Search Text Here

[Raw View](#)[illegible]

GUI Build Version : 6.4.33

Configuration File Version : 6.4.33

Configuration File Supported Device : TPS65994AE EVM

Configuration File Name: TPS65994AE_DEFAULT_OC.pjt

USB to I2C/SPI Adapter : TIVA

Configuration Mode

Enter the Register/Field Search Text Here

Customer Use
Interrupt Mask for I2C1
Interrupt Mask for I2C2
SX Config
Set Sx App Config
USB Config
Port Configuration
Port Control
Transmit Source Capabilities
Transmit Sink Capabilities
Autonegotiate Sink
Tx Identity
User Alternate Mode Configuration
Display Port Configuration
Thunderbolt Configuration
Prochot Configuration
Retimer Debug Register
I2C Controller Config
App Config Register
Transmitted Status Data Block (SDB) Register
Raw View

Display Port Configuration (0x51)

Field	Value
Enable DP SID	☒
DP Mode	☒
DP Transport Signalling	Supports DP signalling rates and electrical specification
USB2 Signalling Not Used	May be required
DFPD Pin Assignment	A B C D E F
Multi Function Preferred	☐
DP Mode Auto Entry Allowed	☒

- Customer Use
- Interrupt Mask for IC2C1
- Interrupt Mask for IC2C2
- SX Config
- Set Sx App Config
- USB Config
- Port Configuration
- Port Control
- Transmit Source Capabilities
- Transmit Sink Capabilities
- Autonegotiate Sink
- Tx Identity
- User Alternate Mode Configuration
- Display Port Configuration
- Thunderbolt Configuration
- Prochot Configuration
- Retimer Debug Register
- IC2C Controller Config
- App Config Register
- Transmitted Status Data Block (SDB) Register

[Raw View](#)

[illegible]