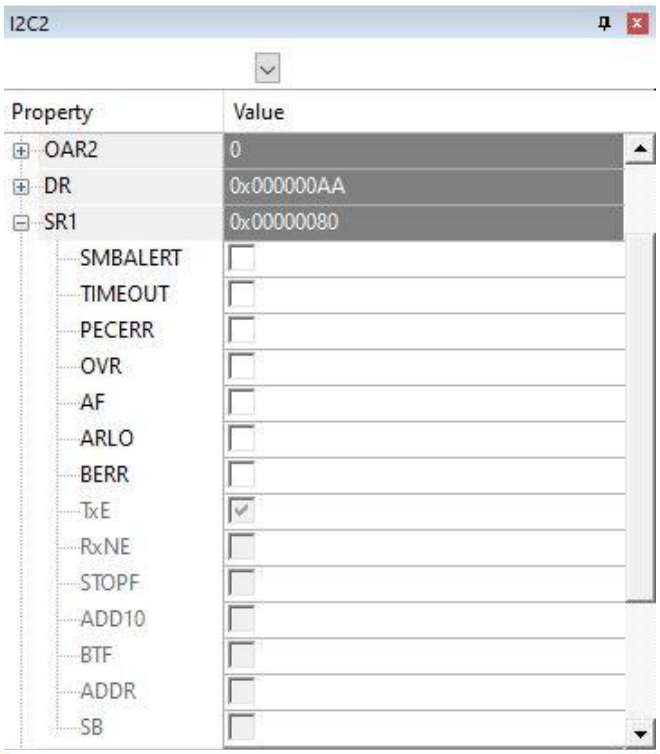
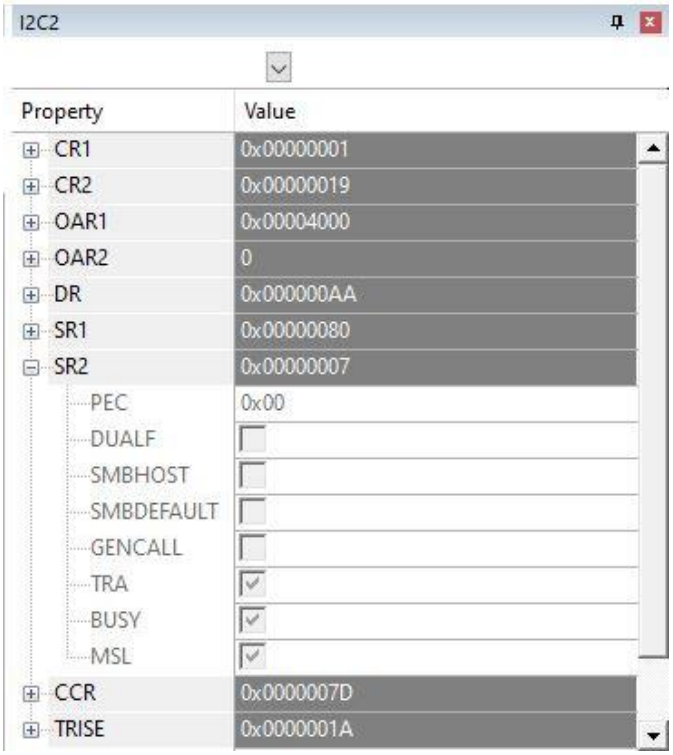

I2C2
Property
Value
OAR2
0
DR
0x000000AA
SR1
0x00000080
SMBALERT
TIMEOUT
PECERR
OVR
AF
ARLO
BERR
TxE
RxNE
STOPF
ADD10
BTF
ADDR
SB


I2C2
Property
Value
CR1
0x00000001
CR2
0x00000019
OAR1
0x00004000
OAR2
0
DR
0x000000AA
SR1
0x00000080
SR2
0x00000007
PEC
0x00
DUALF
SMBHOST
SMBDEFAULT
GENCALL
TRA
BUSY
MSL
CCR
0x0000007D
TRISE
0x0000001A

```c
uint16_t BQ34Z100_Read(uint8_t reg, int size)
{
//uint16_t hex_value;
union
{
  uint8_t b[2];
  uint16_t w;
} data;

if(HAL_I2C_IsDeviceReady(&hi2c2,0xAA,5,10) == HAL_OK)
{
  if (size == 1)
  {
    data.b[1] = 0x00;

    HAL_I2C_Master_Transmit(&hi2c2,0xAA,&reg,1,100);
    HAL_Delay(5);
    HAL_I2C_Master_Receive(&hi2c2,0xAB,&data.b[0],size,100);
  }

  else
  {
     HAL_I2C_Master_Transmit(&hi2c2,0xAA,&reg,1,100);
     HAL_Delay(5);
     HAL_I2C_Master_Receive(&hi2c2,0xAB,data.b,size,100);
  }
}

return (data.w);

}
```

```c
  SOC1 = BQ34Z100_Read(0x02,1);

  HAL_Delay(2);

  Voltage = BQ34Z100_Read(0x08,2);
  V1 = (uint16_t) (Voltage*0.1);

  HAL_Delay(2);

  RC = BQ34Z100_Read(0x04,2);
  RCC1 = (uint16_t) (RC*0.2);

  HAL_Delay(2);

  FCC = BQ34Z100_Read(0x06,2);
  FCC1 = (uint16_t) (FCC*0.2);

  HAL_Delay(2);

  Temp = BQ34Z100_Read(0x0C,2);
  T_CELL_1 = (uint16_t) ((Temp*0.1)-273);

  HAL_Delay(2);

  I_code = BQ34Z100_Read(0x10,2);
  Current = I_code*0.002;
  C1 = I_code*2;
```