KNOWN PARAMETERS (*This Color in **Bold** = Changes*)

FSW -- 105KHZ

MIN/MAX DUTY CYCLE -- 5% - 46%

Q1, Q2 -- IKFW40N60D (IGBT)

HV SOURCE -- 85-260VDC

PIN #7 (VDD) -- 16.2VDC

SH, SL -- IRG41BC10UDPbF (IGBT)

VDD SOURCE -- 18VDC, 5.5A

LI, HI -- 16.5V

PIN #13, PIN #11 Calc'd -- Min 15.3, Max 16.3

- **Change Q!, Q2 gate resistors from 5R to 8R**
- **Cboot now 100Nf**
- **Added 20K to Q1 & Q2 gate to emitter**
- **Added 1000pF cap flyback transformer primary pos to secondary neg**
- **Re-calc'd Cies=Qg/V, V=Qg/Cies = 100nC/100nF = 1 Volt drop at Cboot**
- **Tied S2 collector directly to S1 gate**
- **Changed S1 Rshg from 5R to 1000R**

I believe I have addressed what was mentioned in previous discussion. Please review and see if there is anything that I might have missed. When I get the new parts in I will send you a report with actual waveforms of the project.