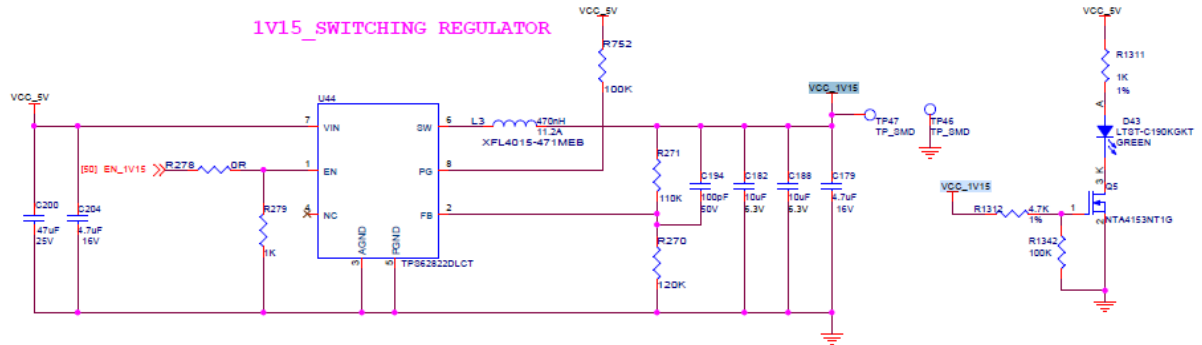
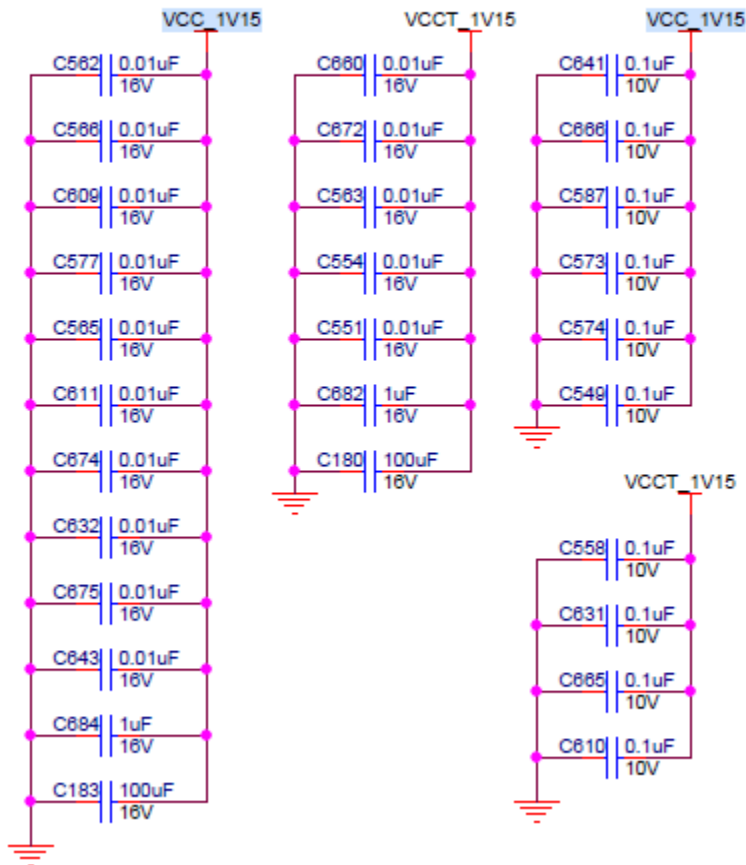


Power Supply from 5V to 1.15V:



FPGA Decoupling Capacitors:

VCC_1V15



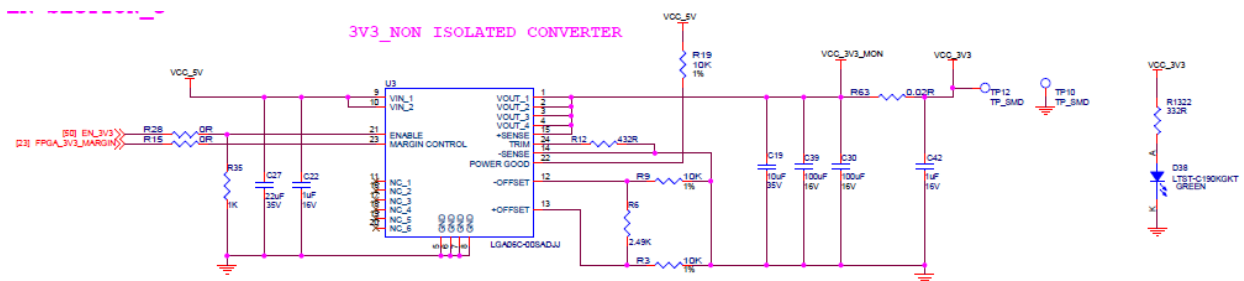
FPGA

VCCR_GXBL
VCCR_GXBR

VCCT_GXBL
VCCT_GXBR

VCCL_GXBL
VCCL_GXBR

[illegible]



IC's Connected to 3.3V Rail:

DS26LV31TMMX/
NOPB(2*100uA)
DS26LV32ATMMX/
NOPB(1*15mA)
74FCT164245TPVG
(3*4.25mA)
SN74GTLPH16912GR
(2*100mA)
5535DCGI(2*22mA)
MC100EPT25DG(1*17mA)
5T8008BC-83-33E-
25.000000Y(1*4.5mA)
S52142-AD1AGM(2*40mA)
MC100LVEL92DWG(1*20mA)
MC100LVEL90DWG(1*26mA)
SY89295UMGTR(3*220mA)
NB6L611MNGR(1*70mA)
S25FL512\$AGMFR10
(2*35mA)
TMP75AIDR(2*0.11mA)
SN74LVC06AD(3*5mA)
DM3AT-SF-PJMS
MT28EW512ABA1LS-0S/IT
TR(1*51mA)
AT24CMD1-SSHD-T(1*3mA)
MAX5232ECPWR(1*1mA)
TLC59116IPWR(1*37mA)
AD9314BCP2(2*68mA)
SN74ALVC245PWR(4*6mA)
MC100EPT23DG(1*40mA)
TPD6E001R5ER(2*100nA)
BAT54A(1*0.002mA)
LTST-C190KGKT(2*7.5mA)
LTC2914CGN-1#PBR(1*100uA)
WP4060VH/2GDI(7*6.3mA)
TSSA23157DGSRI(3*5mA)
MC100EPT23DG(1*40mA)
WP4060VH/GID(2*7.5mA)

As 3.3V rail is connected to multiple IC's, so schematic for 3.3V rail is not attached here.