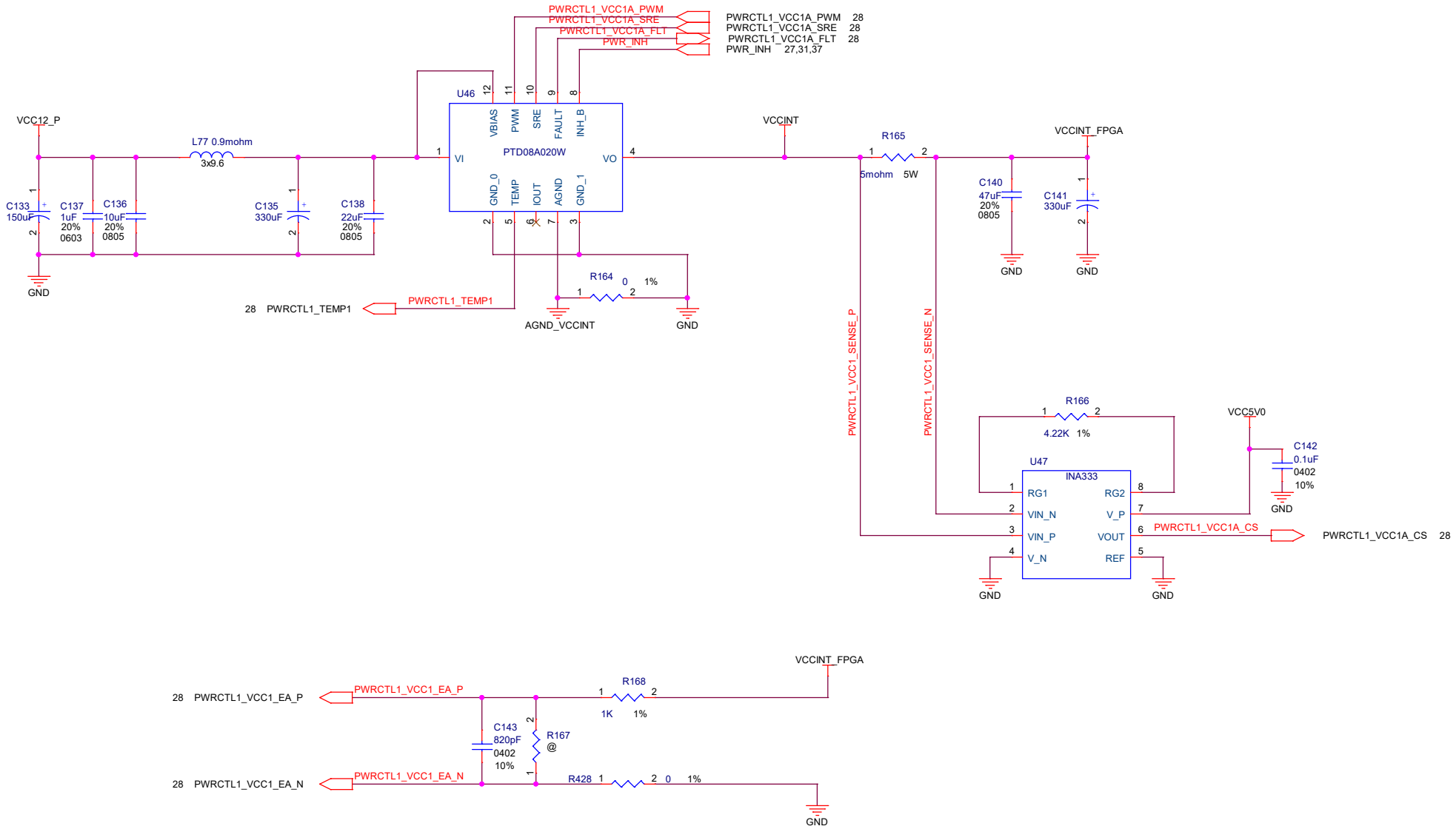
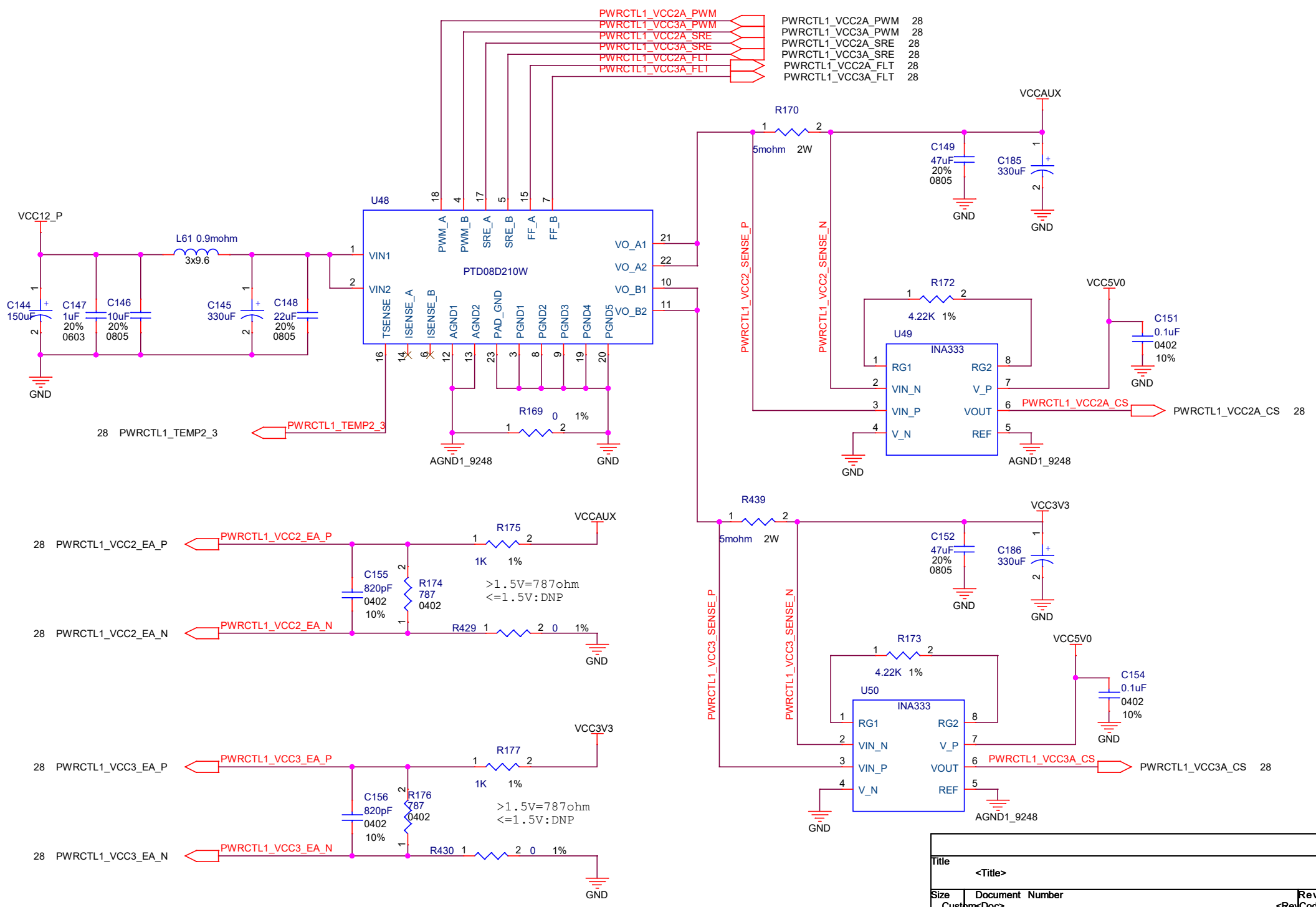


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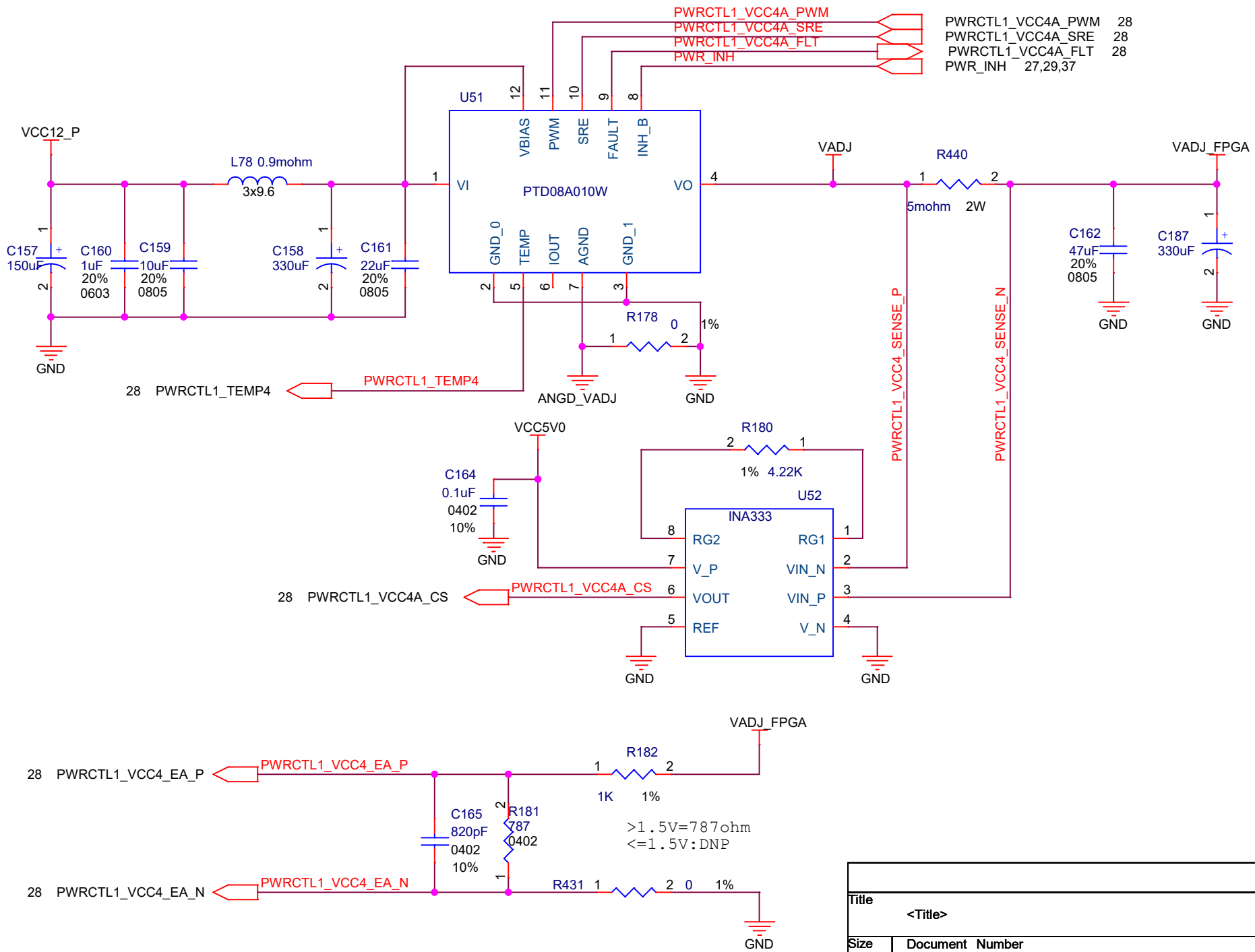


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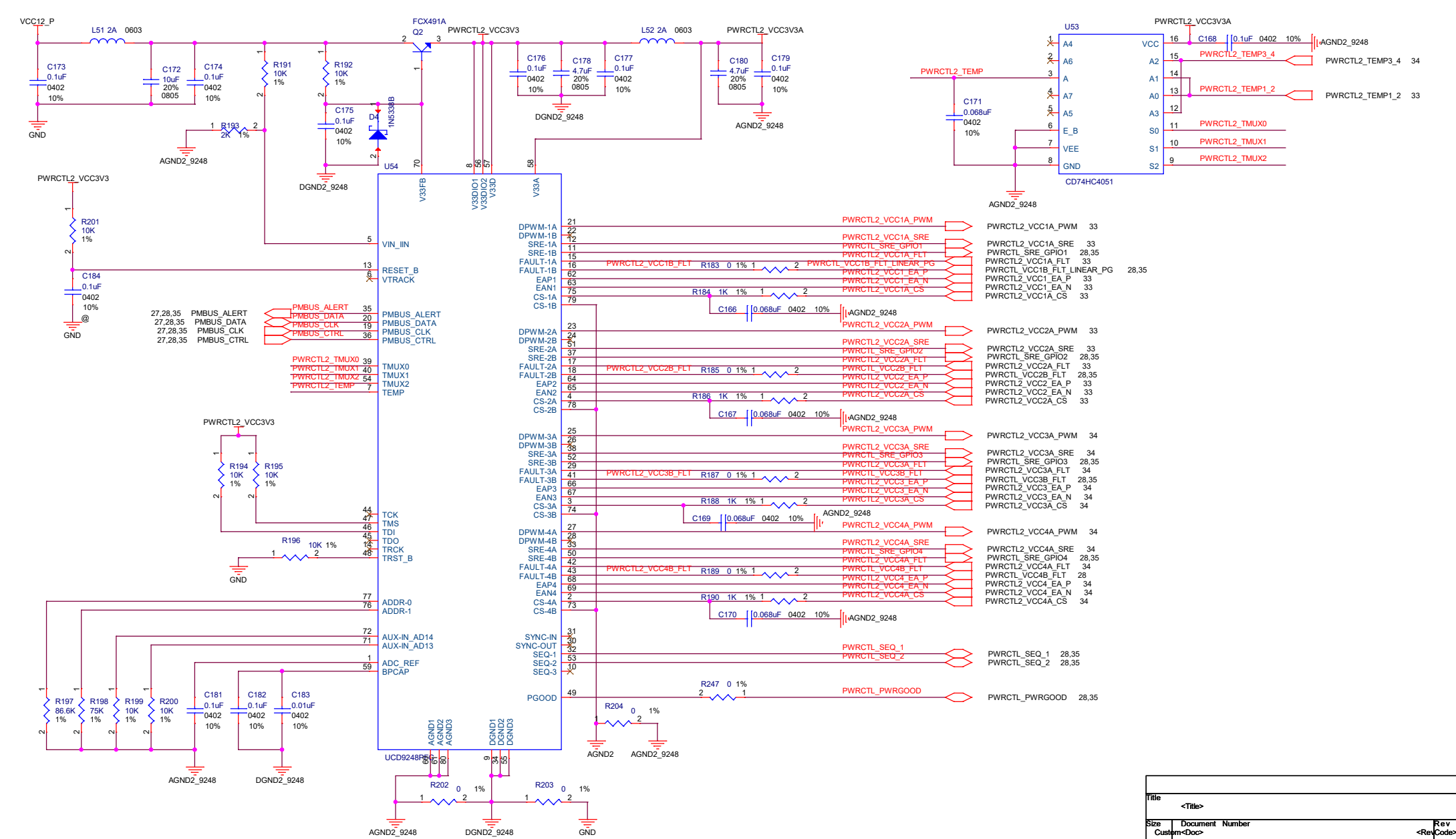


- PWRCTL1\_VCC2A\_PWM 28
- PWRCTL1\_VCC3A\_PWM 28
- PWRCTL1\_VCC2A\_SRE 28
- PWRCTL1\_VCC3A\_SRE 28
- PWRCTL1\_VCC2A\_FLT 28
- PWRCTL1\_VCC3A\_FLT 28

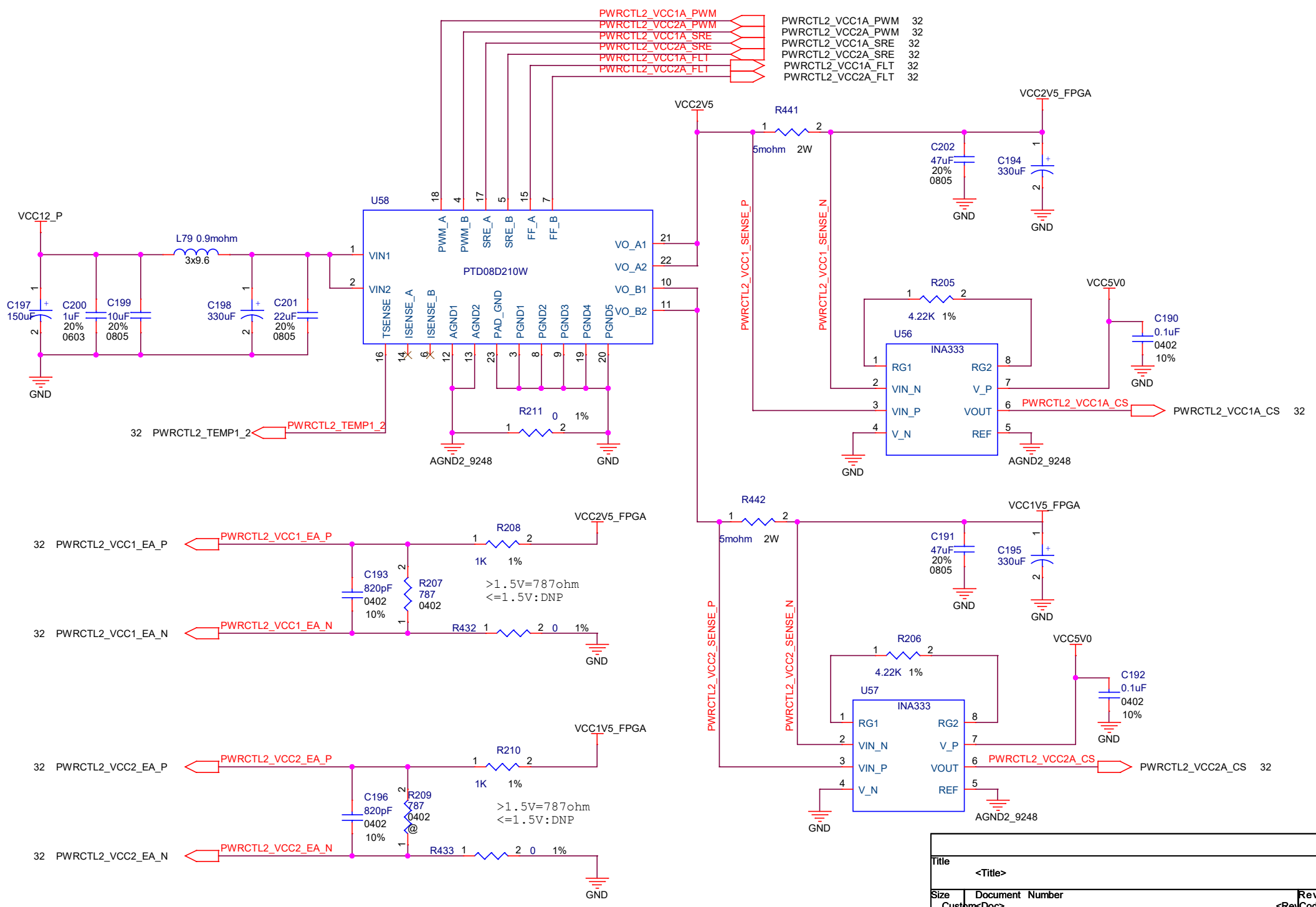
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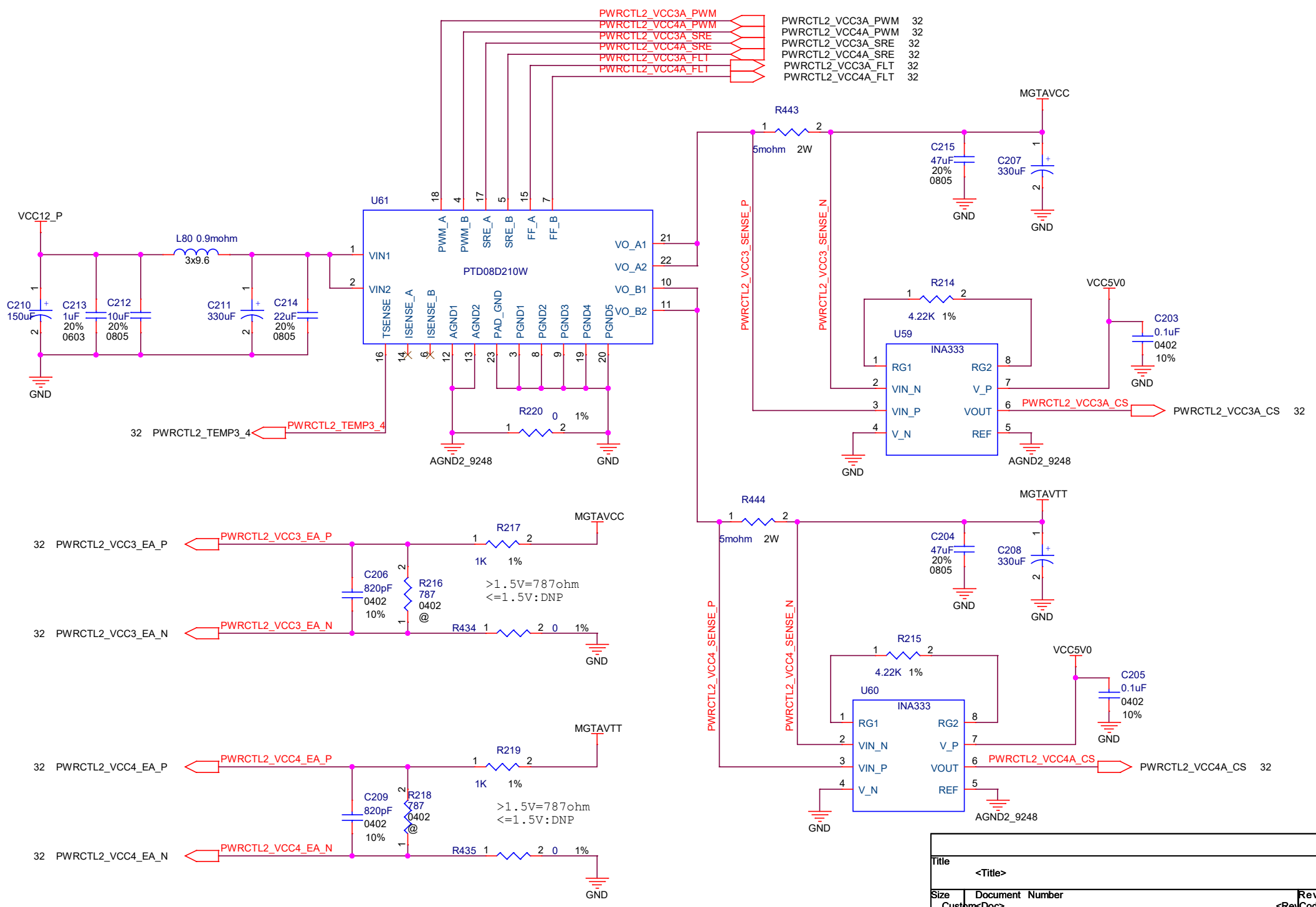
- PWRCTL2\_VCC1A\_PWM 32
- PWRCTL2\_VCC2A\_PWM 32
- PWRCTL2\_VCC1A\_SRE 32
- PWRCTL2\_VCC2A\_SRE 32
- PWRCTL2\_VCC1A\_FLT 32
- PWRCTL2\_VCC2A\_FLT 32

32 PWRCTL2\_TEMP1\_2 ← PWRCTL2\_TEMP1\_2

PWRCTL2\_VCC1A\_CS → PWRCTL2\_VCC1A\_CS 32

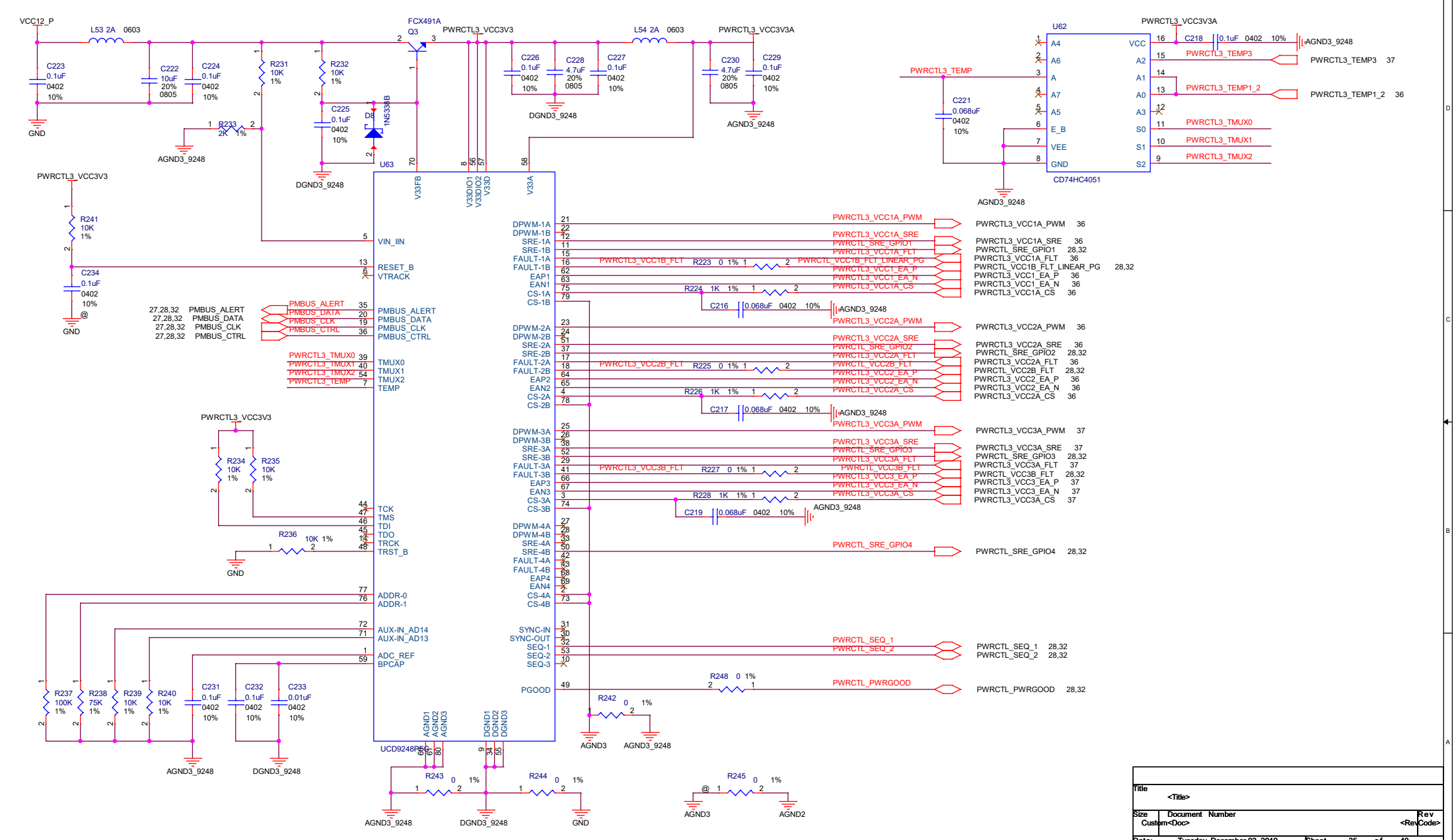
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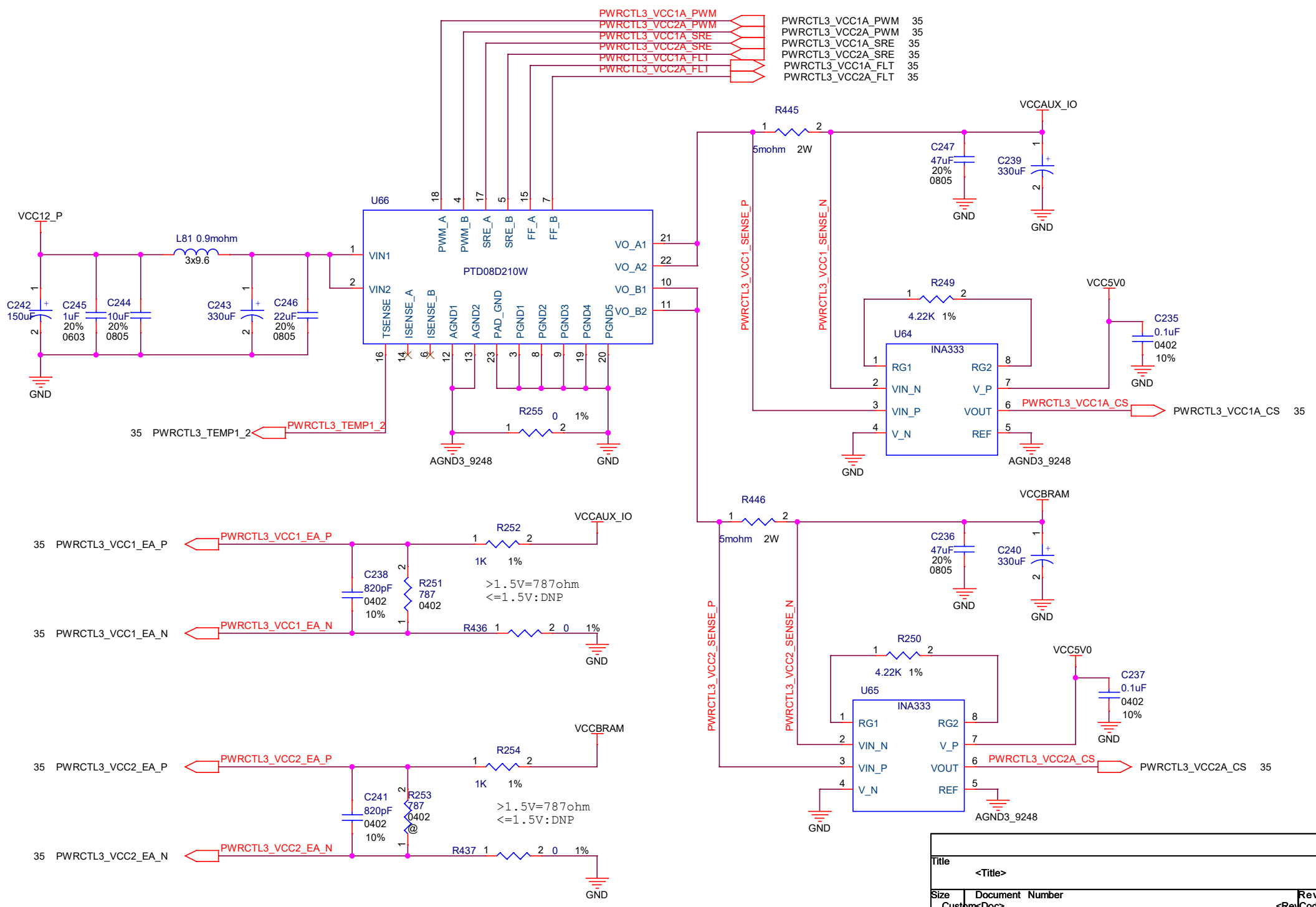
- PWRCTL2\_VCC3A\_PWM 32
- PWRCTL2\_VCC4A\_PWM 32
- PWRCTL2\_VCC3A\_SRE 32
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- PWRCTL2\_VCC3A\_FLT 32
- PWRCTL2\_VCC4A\_FLT 32

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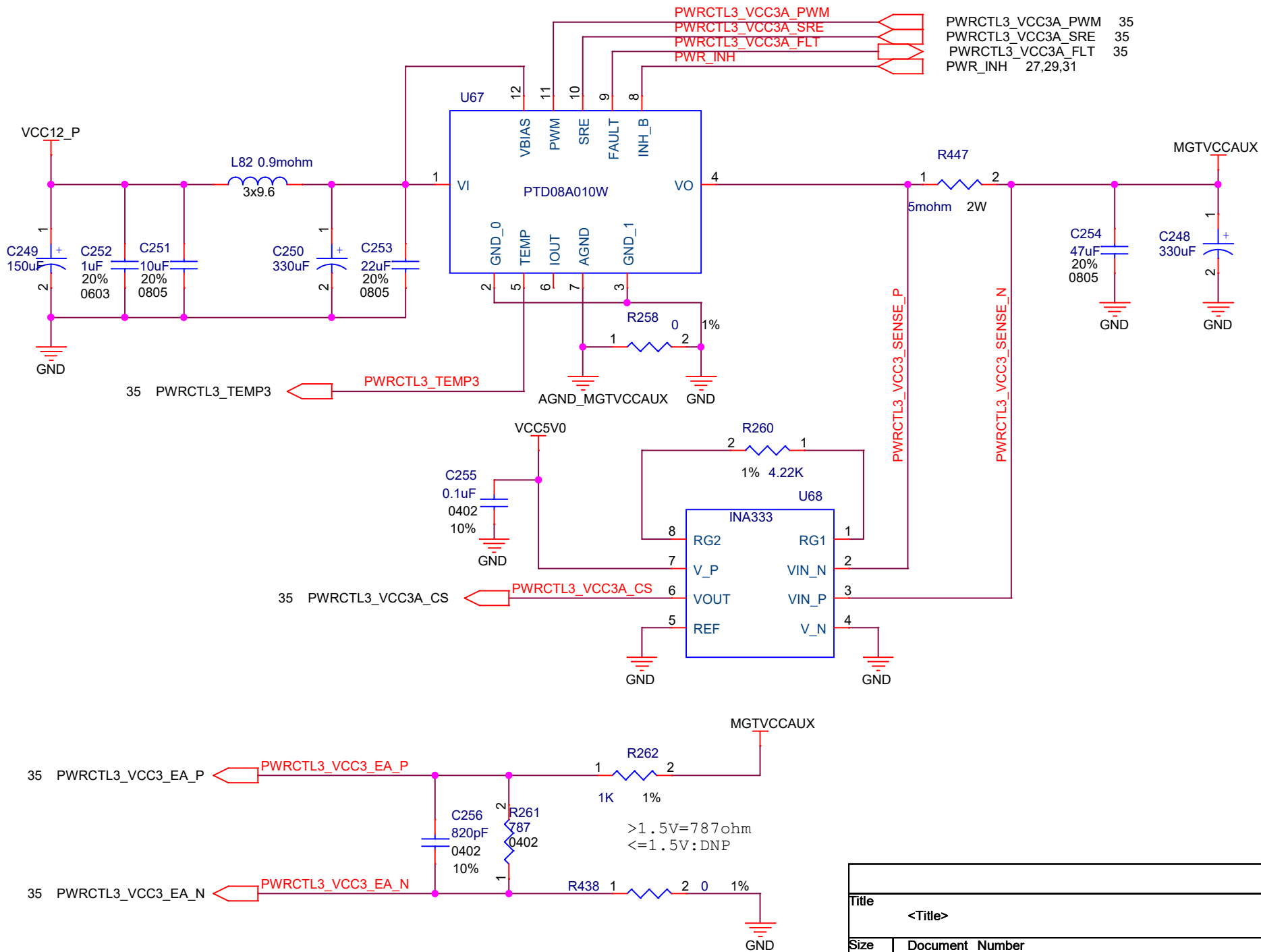
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- PWRCTL3\_VCC1A\_PWM 35
- PWRCTL3\_VCC2A\_PWM 35
- PWRCTL3\_VCC1A\_SRE 35
- PWRCTL3\_VCC2A\_SRE 35
- PWRCTL3\_VCC1A\_FLT 35
- PWRCTL3\_VCC2A\_FLT 35

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PWRCTL3_VCC3A_PWM	35
PWRCTL3_VCC3A_SRE	35
PWRCTL3_VCC3A_FLT	35
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