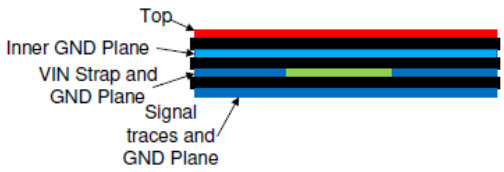
STACK

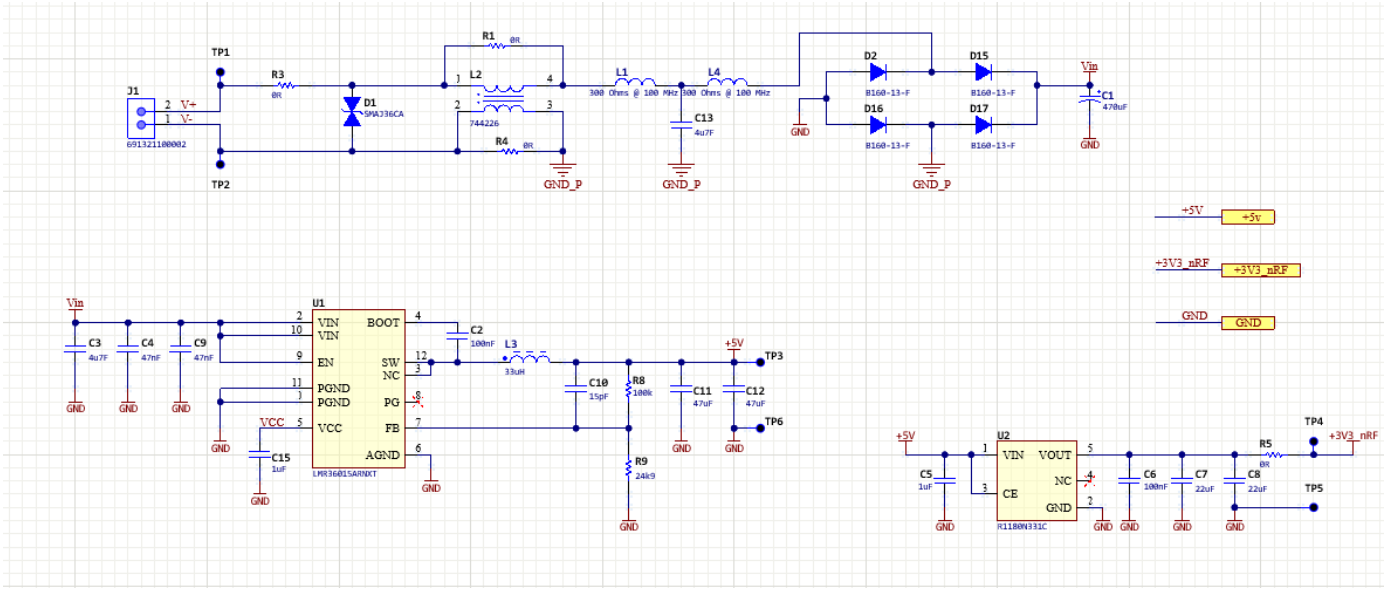
Four layers, 35um/35um, 1.6mm

#	Name	Material	Type	Weight	Thickness	Dk	Df	Coverl
	Top Overlay		Overlay					
	Top Solder	Solder Resist	Solder Mask		0.01016mm	3.5		
	Top Surface Finish	Lead-Free	Surface Finish		0.02mm			
1	Top Layer		Signal	1oz	0.035mm			
	Dielectric 1	FR-4	Prepreg		0.11mm	4.8		
2	Mid-Layer 1		Signal	1oz	0.03556mm			
	Dielectric3	FR-4	Core		1.2mm	4.8		
3	Mid-Layer 2		Signal	1oz	0.03556mm			
	Dielectric2	FR-4	Prepreg		0.11mm	4.8		
4	Bottom Layer		Signal	1oz	0.035mm			
	Bottom Surface Finish	Lead-Free	Surface Finish		0.02mm			
	Bottom Solder	Solder Resist	Solder Mask		0.01016mm	3.5		
	Bottom Overlay		Overlay					

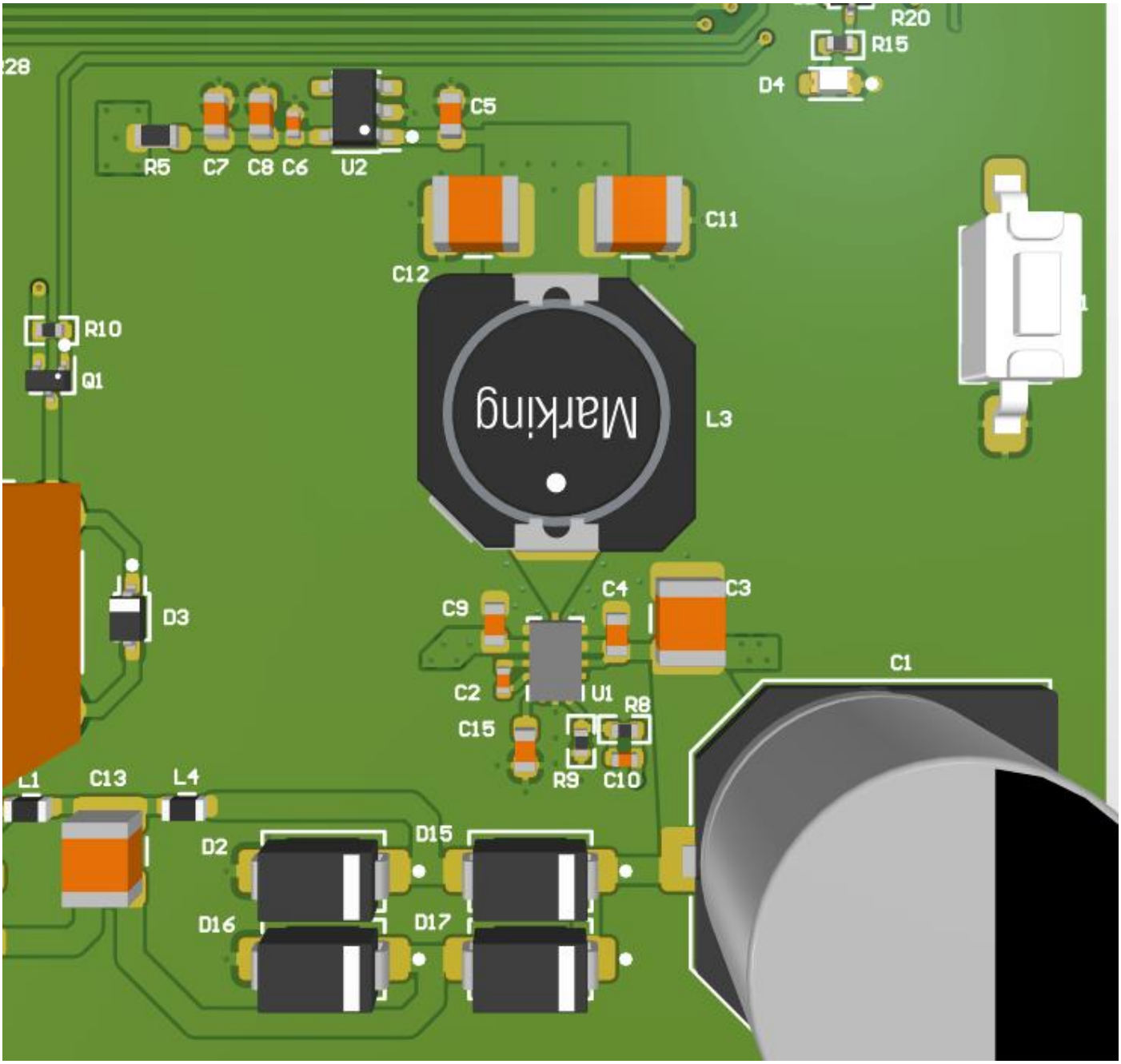
When I needed to route some tracks from different layers, I used the next layout.



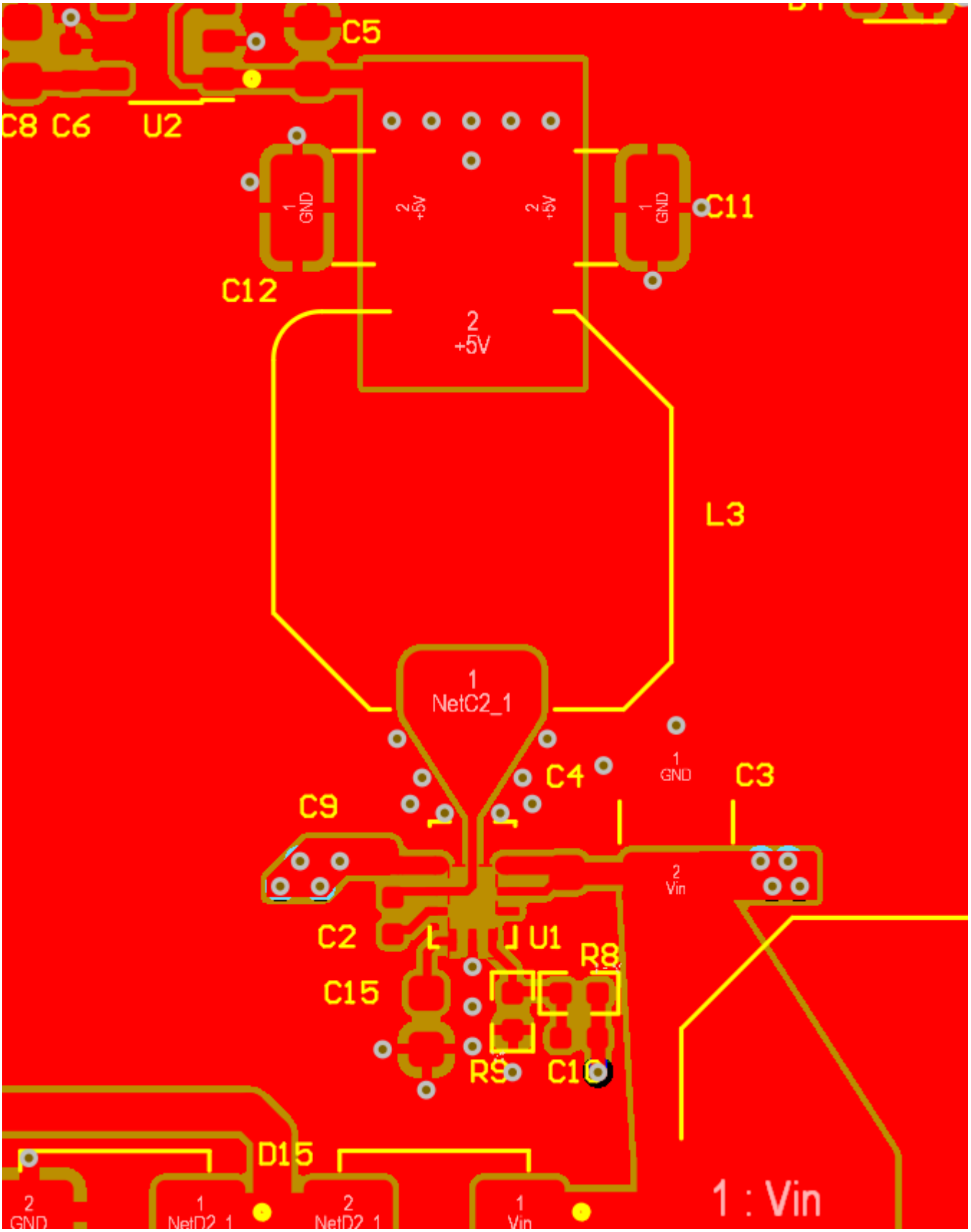
Schematic of the design



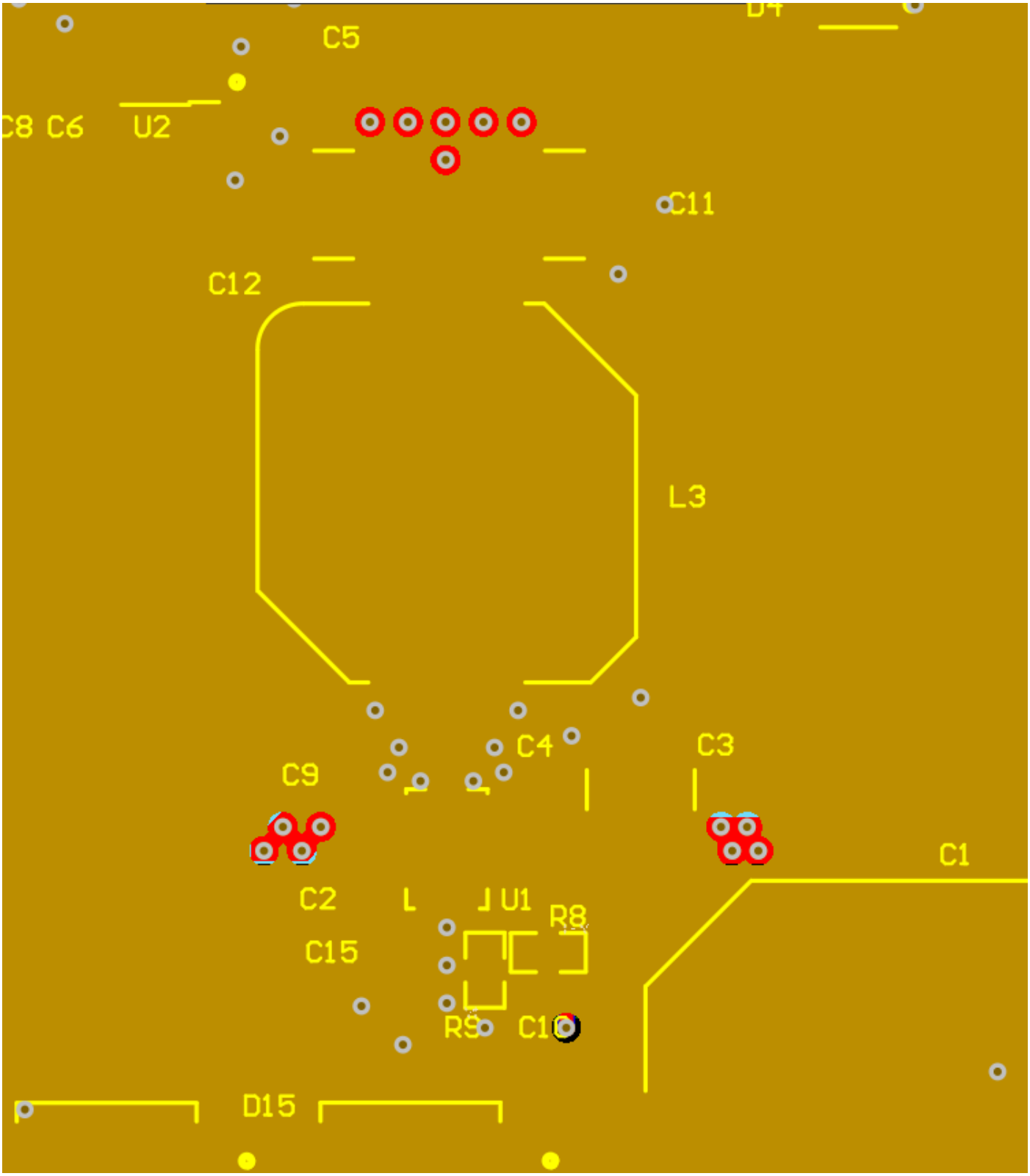
3D view of the DC/DC converter.



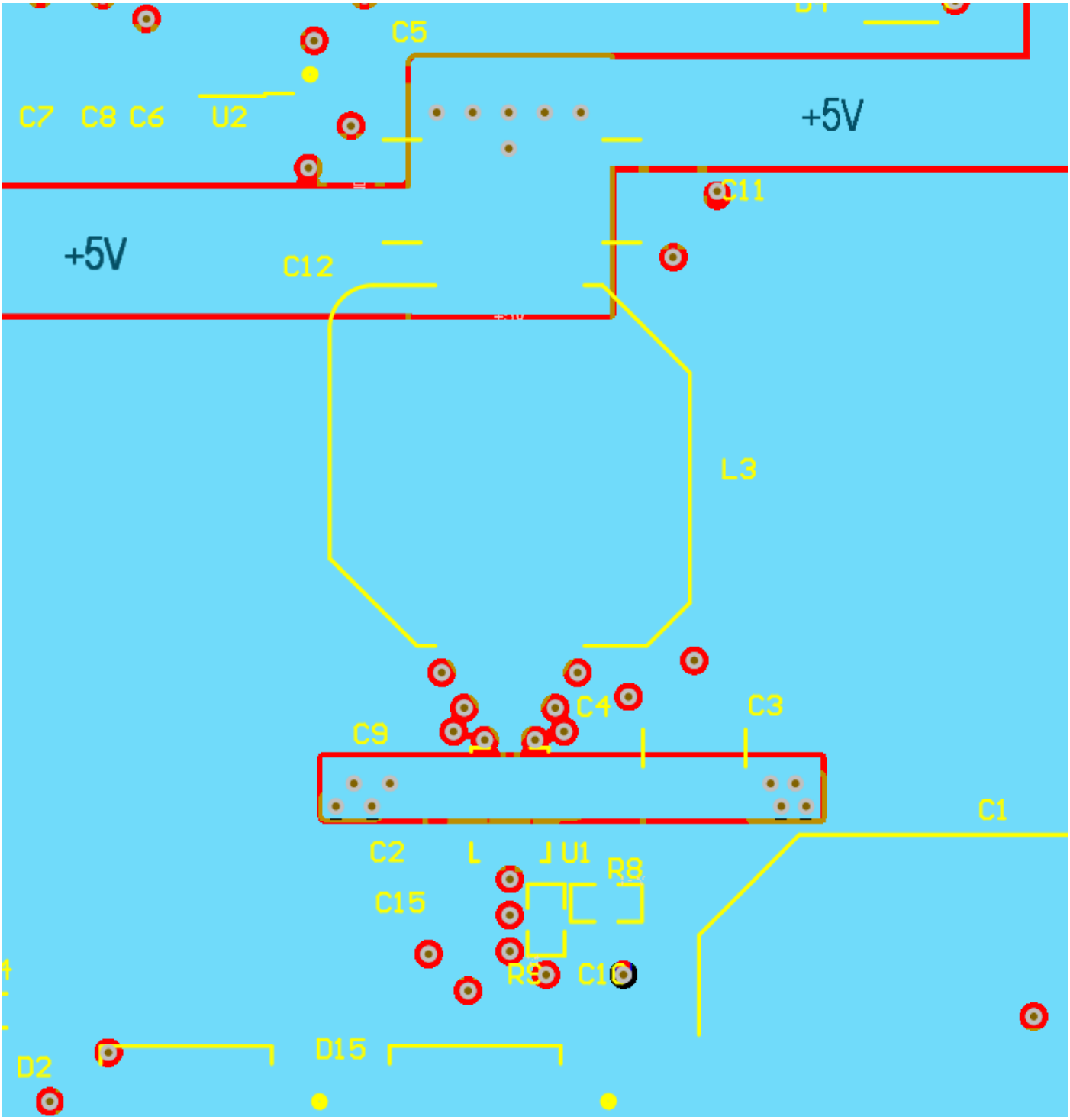
LAYER 1 TOP


C5
C8 C6
U2
1
GND
2
+5V
2
+5V
1
GND
C11
C12
2
+5V
L3
1
NetC2_1
C4
1
GND
C3
C9
2
Vin
C2
L
U1
R8
C15
R5
C1
D15
2
GND
1
NetD2_1
2
NetD2_1
1
Vin
1 : Vin

LAYER 2 GND



LAYER 3 VIN / +5V / GND



LAYER 4 SIGNAL TRACES / GND