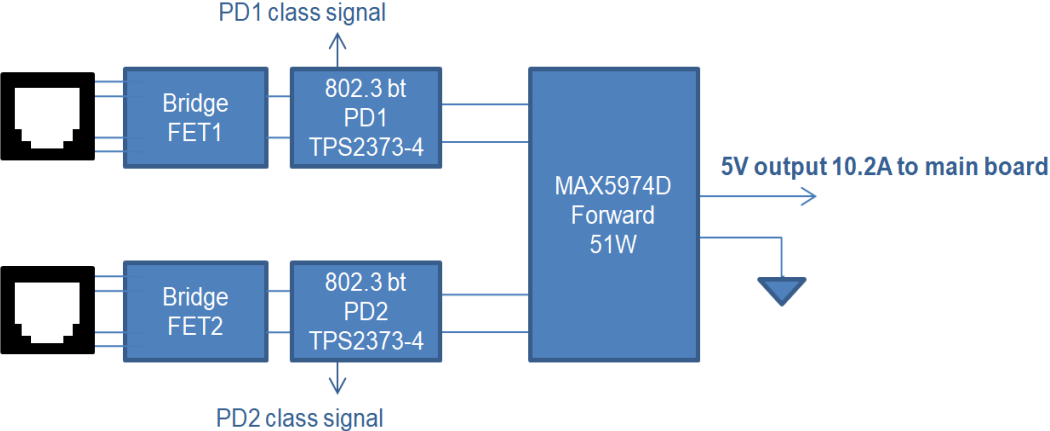
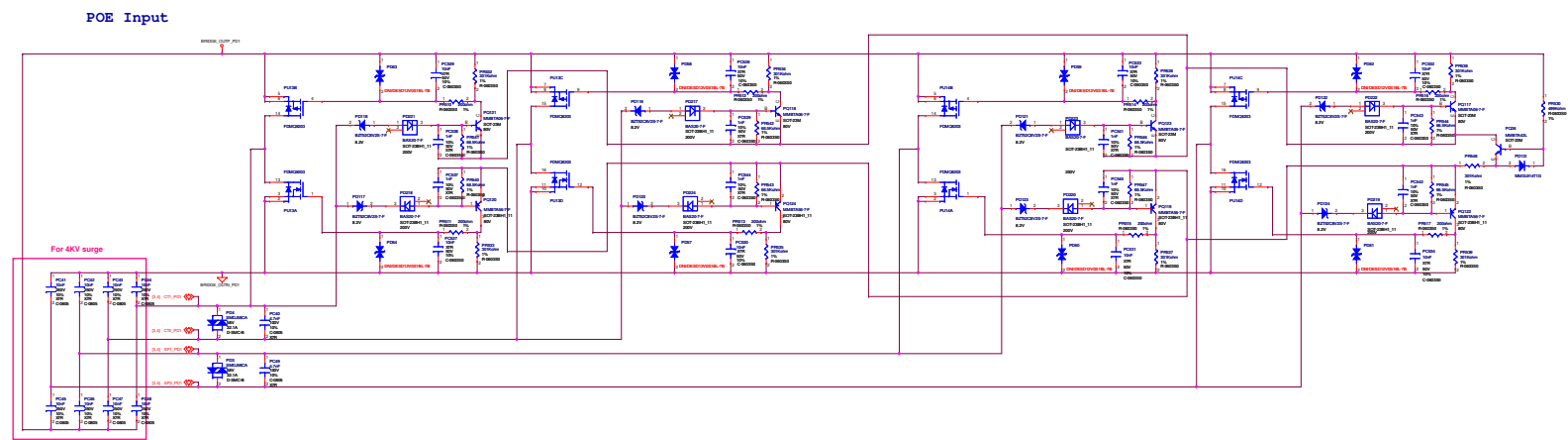


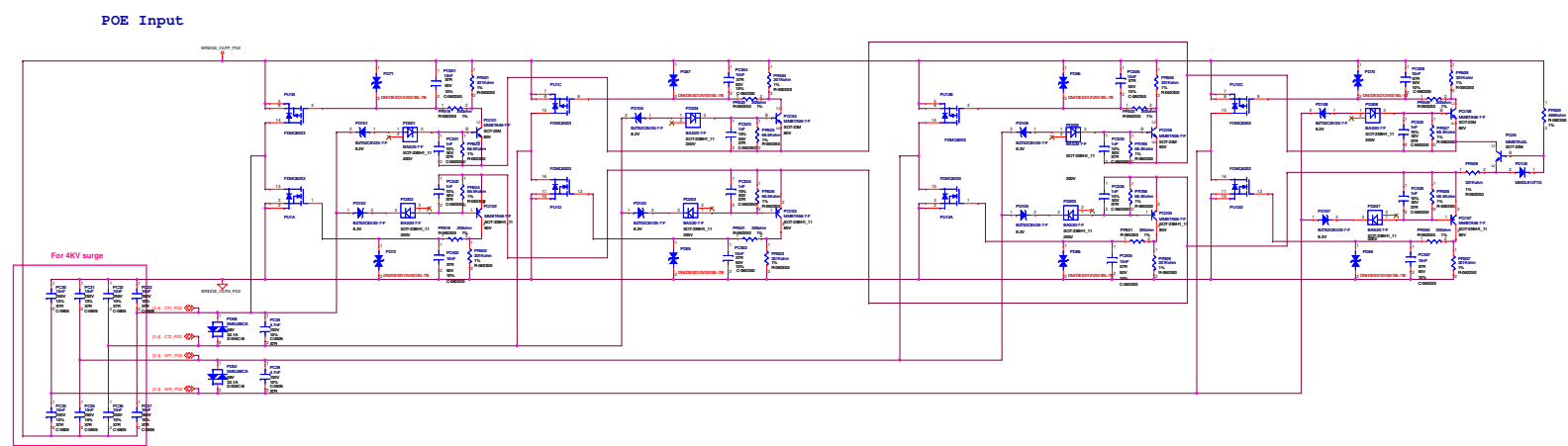
Block Diagram



Bridge FEM PD1



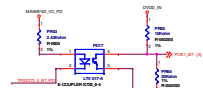
Bridge FEM PD2



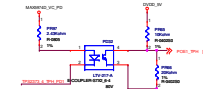
PD1

GPIO connection

Indicate the PD Type



Indicate the PD Type



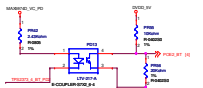
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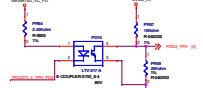
PD2

GPIO connection

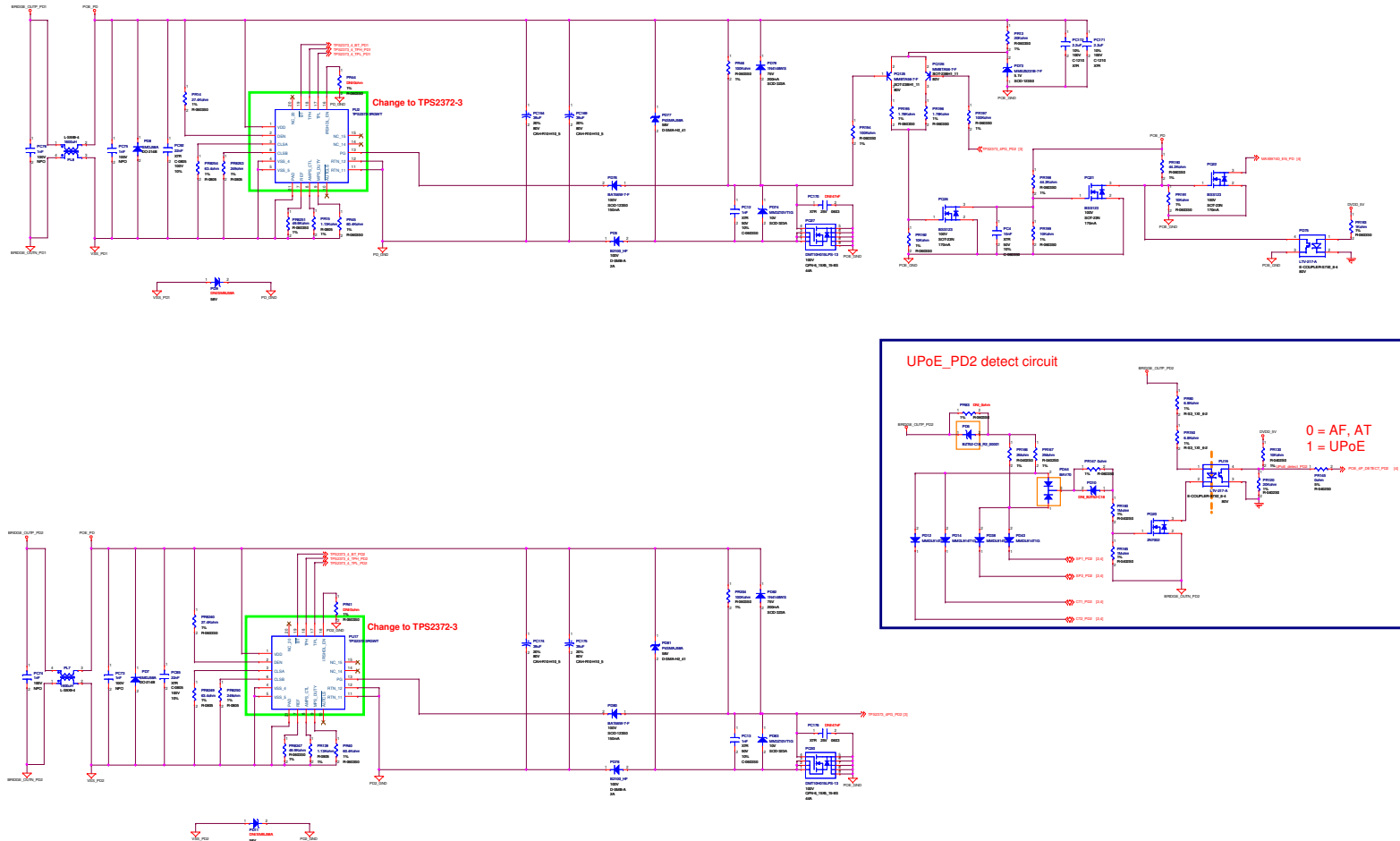
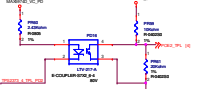
Indicate the PD Type



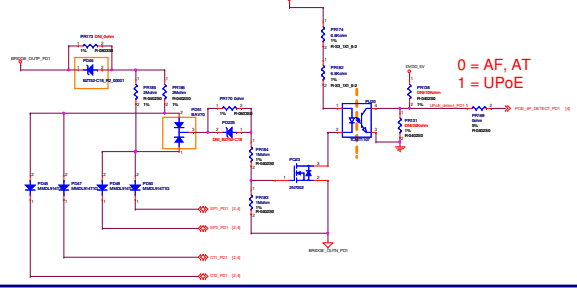
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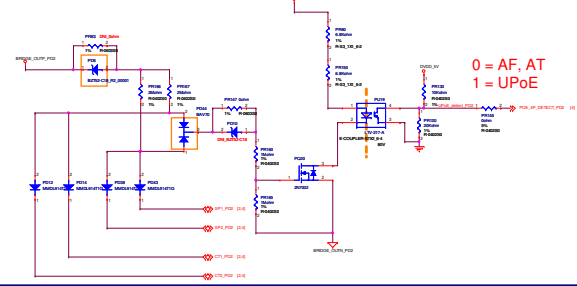
Indicate the PD Type



UPoE_PD1 detect circuit



UPoE_PD2 detect circuit



2018/5/24 - Remove surge FET based on SIFOS Voff test result.

2018/5/25 - Reserve PD9 and PD11 footprint for surge and ESD test.

2018/6/14 - Change M1 / M2 / M3 / M4 footprint.

- 2018/10/01
- 1. Add M1 / M2 / M3 / M4 CAP footprint to GND.
 - 2. Reserved PD57 ~ PD72 footprint for VGS protection diode.

- 2019/02/01
- 1. DNI PR138, PR131, Use pull high circuit in main board.

- 2019/02/18
- 1. Remove PD2 PWM circuit, change the function to dual- input with seamless transition.
 - 2. Change PR559/560/561/562/563/564/565/566 to 0ohm.

- 2019/02/22
- 1. Change PU2/PU17 to TPS2372-3RGWR for seamless transition feature.

2019/02/28 - Change PD IC to TPS2372-3 to support hitless feature.

2019/03/04 - Change PR47/PR43/PR99/PR97/PR68/PR63/PR174/PR182/PR50/
PR42/PR69/PR54/PR72/PR60/PR90/PR150 to 6.8Kohm for 3mA optocoupler LED current.

2019/03/05 - Change PD7/PD8 to SMDJ58A.
Change PD77/PD81 to P4SMAJ58A.

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