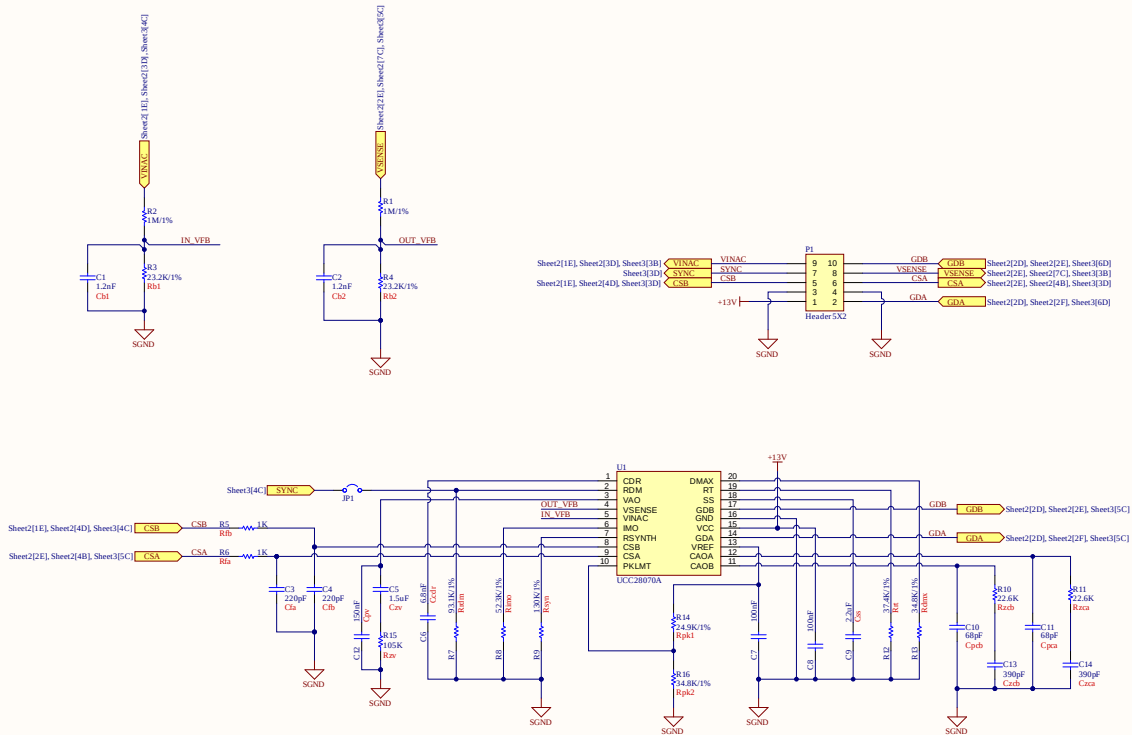
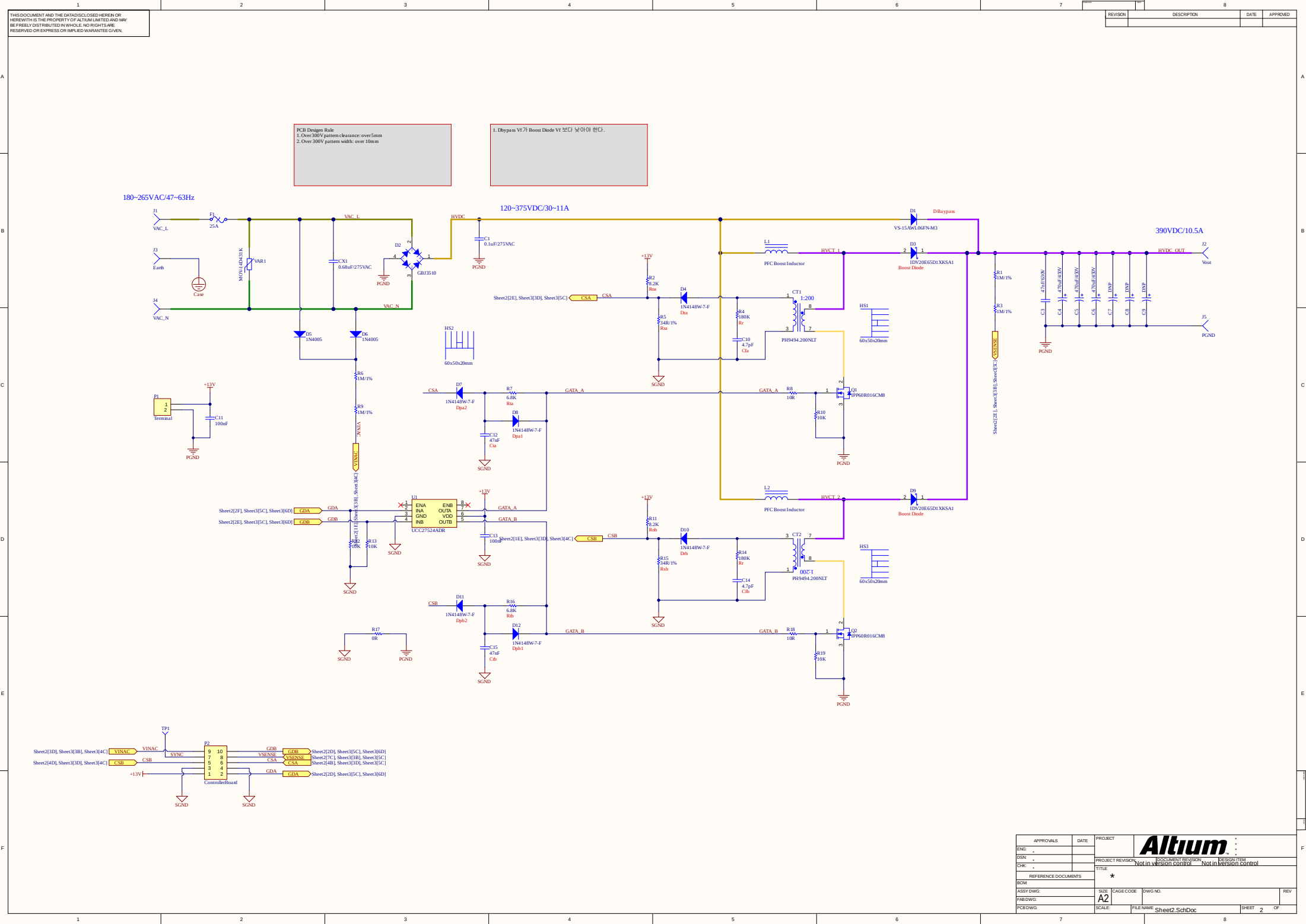


		8	
REVISION	DESCRIPTION	DATE	APPROVED



APPROVALS		DATE	PROJECT		
ENG	*				
DSR	*				
CWK	*				
REFERENCE DOCUMENTS			PROJECT REVISION: _____ DOCUMENT REVISION: _____ DESIGN ITEM: _____ <i>Not in version control</i> <i>Not in version control</i>		
TITLE			★		
BOM					
ASSEMBLY DWG:			SIZE	CAGE CODE	DWG NO.
FAB DWG:			A2		
PCB DWG:			SCALE:	FILE NAME: Sheet3.SchDoc	SHEET 1 OF



PCB Design Rule
1. Over 300V pattern clearance: over 5mm
2. Over 300V pattern width: over 10mm

1. Drypass VI가 Boost Diode VI 보다 낮아야 한다.

APPROVALS		DATE	PROJECT	Altium	
ENG	*		PROJECT REVISION	DOCUMENT REVISION	DESIGN ITEM
DSR	*		Not in version control		
CHK	*		Not in version control		
REFERENCE DOCUMENTS			TITLE		
ROOM			★		
ASSY DWG:			SIZE	CAGE CODE	DWG NO.
FAB DWG:			A2		
PCB DWG:			SCALE	FILE NAME	SHEET 2 OF
				Sheet2.SchDoc	