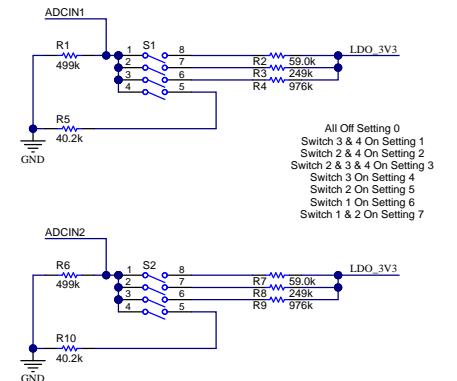
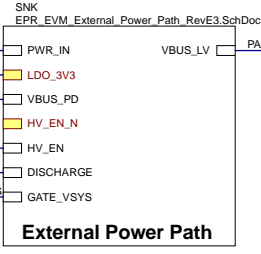
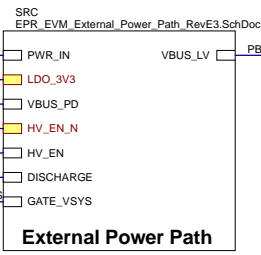
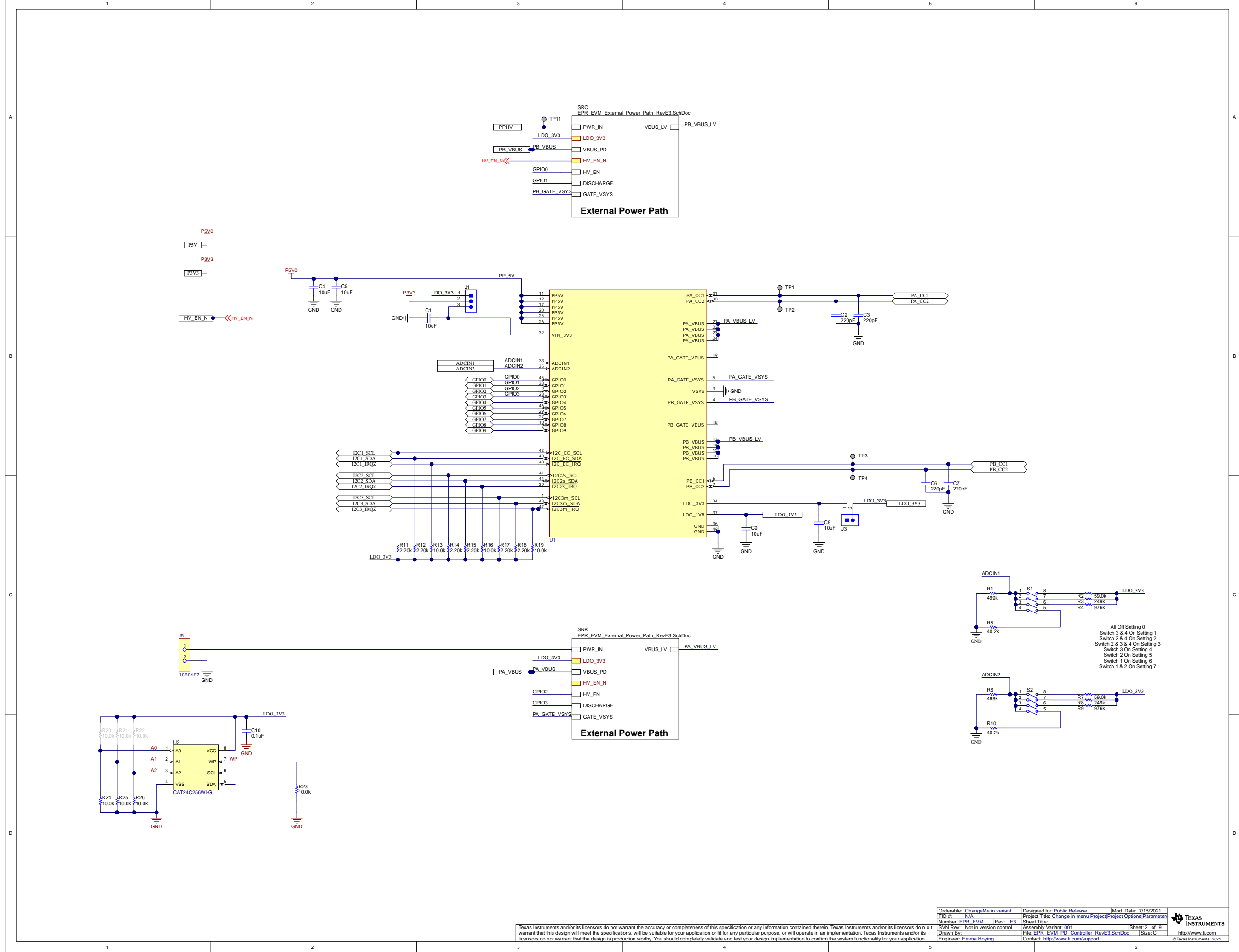


U_EVM_Hardware
EPR_EVM_Hardware_RevE3.SchDoc

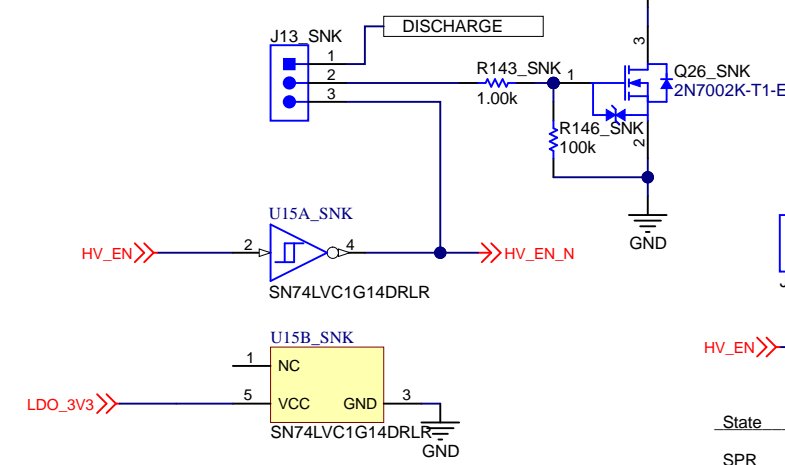
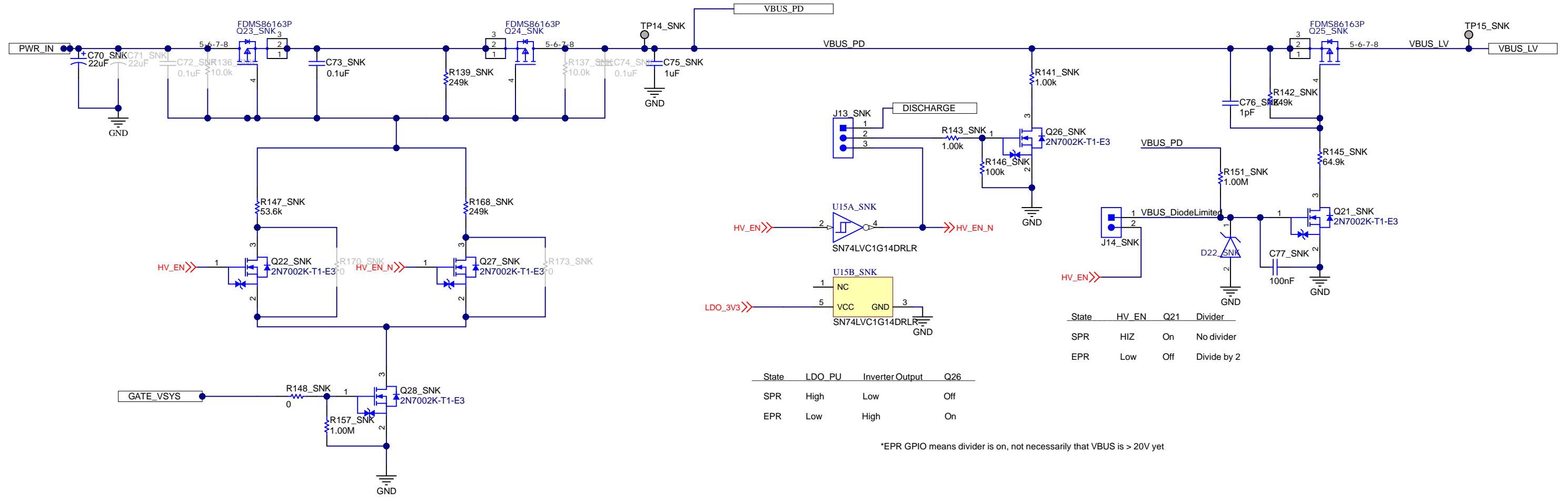
U_Tiva_Debug
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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 7/16/2021	
TID: N/A	Project Title: Change in menu Project/Project Options/Parameter	Sheet Title:	
Number: EPR_EVM Rev: E3	Assembly Variant: 001	Sheet: 1 of 9	
SVN Rev: Not in version control	File: EPR_EVM_Block_Diagram_RevE3.SchDoc	Size: C	
Drawn By:	Contact: http://www.ti.com/Support	http://www.ti.com	© Texas Instruments 2021
Engineer: Emma Hoying			



External Power Path



State	HV_EN	Q21	Divider
SPR	HIZ	On	No divider
EPR	Low	Off	Divide by 2

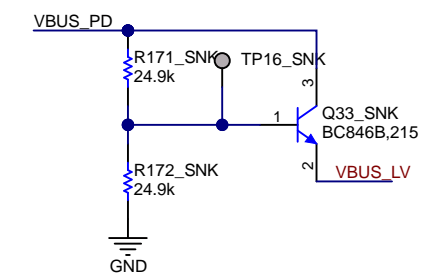
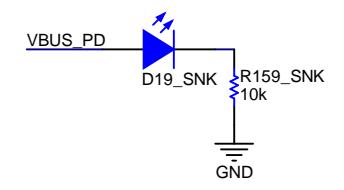
State	LDO_PU	Inverter Output	Q26
SPR	High	Low	Off
EPR	Low	High	On

*EPR GPIO means divider is on, not necessarily that VBUS is > 20V yet

LDO_3V3 → LDO_3V3

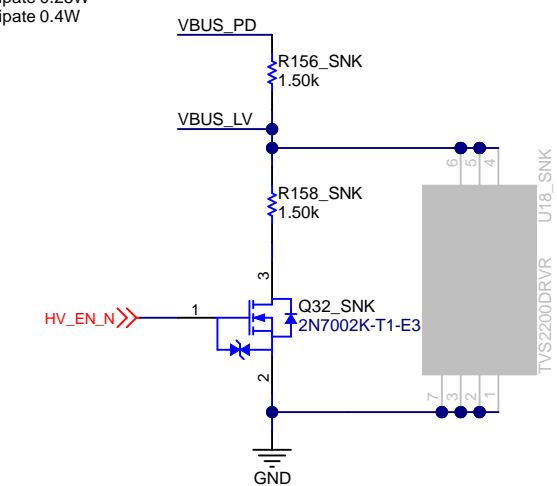
HV_EN → HV_EN

HV_EN_N → HV_EN_N



Resistor Divider

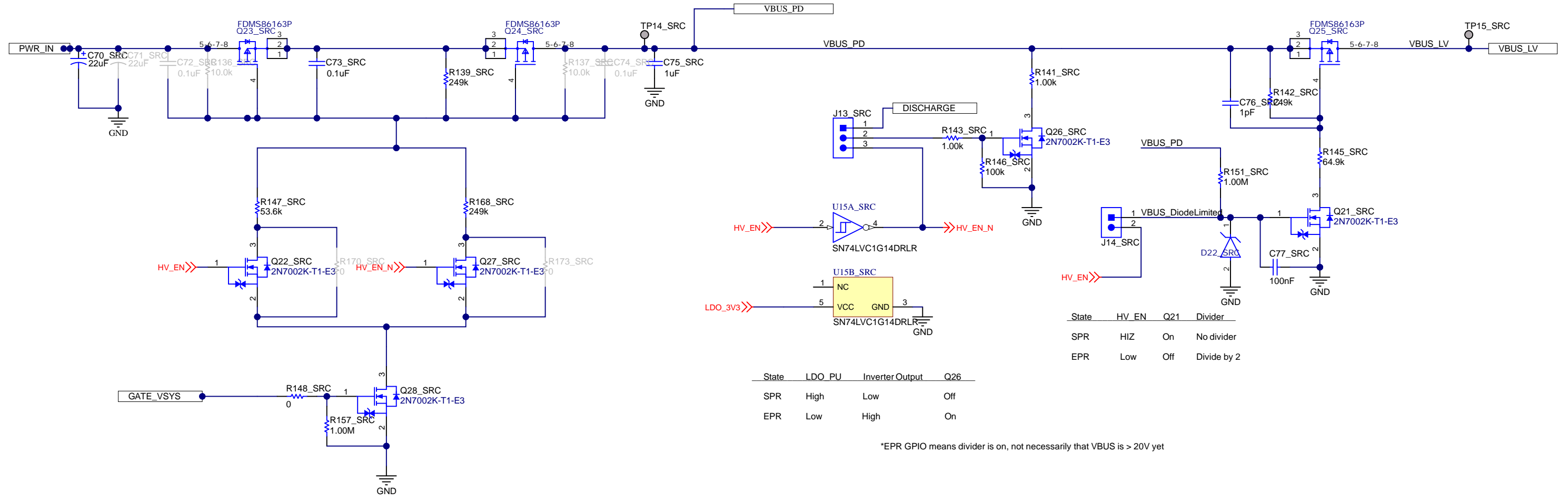
For 36V systems, resistors need to dissipate 0.25W
For 48V systems, resistors need to dissipate 0.4W



DNP TVS for above 44V on VBUS
Resistor divider needs to be changed for 48V

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External Power Path



State	HV_EN	Q21	Divider
SPR	HIZ	On	No divider
EPR	Low	Off	Divide by 2

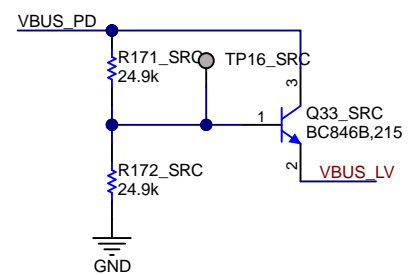
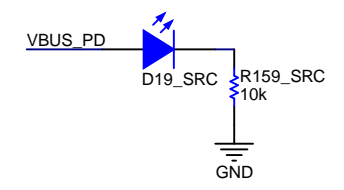
State	LDO_PU	Inverter Output	Q26
SPR	High	Low	Off
EPR	Low	High	On

*EPR GPIO means divider is on, not necessarily that VBUS is > 20V yet

LDO_3V3 → LDO_3V3

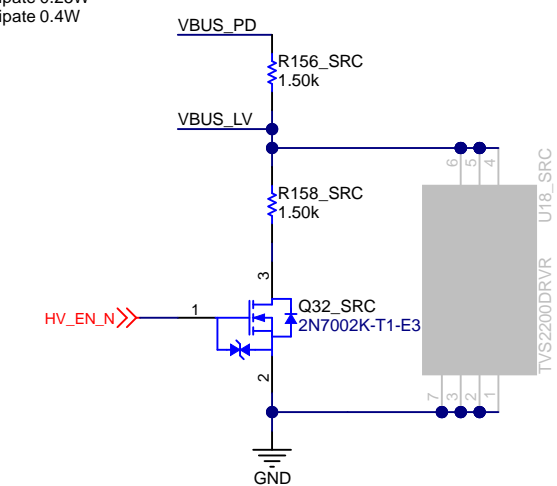
HV_EN → HV_EN

HV_EN_N → HV_EN_N



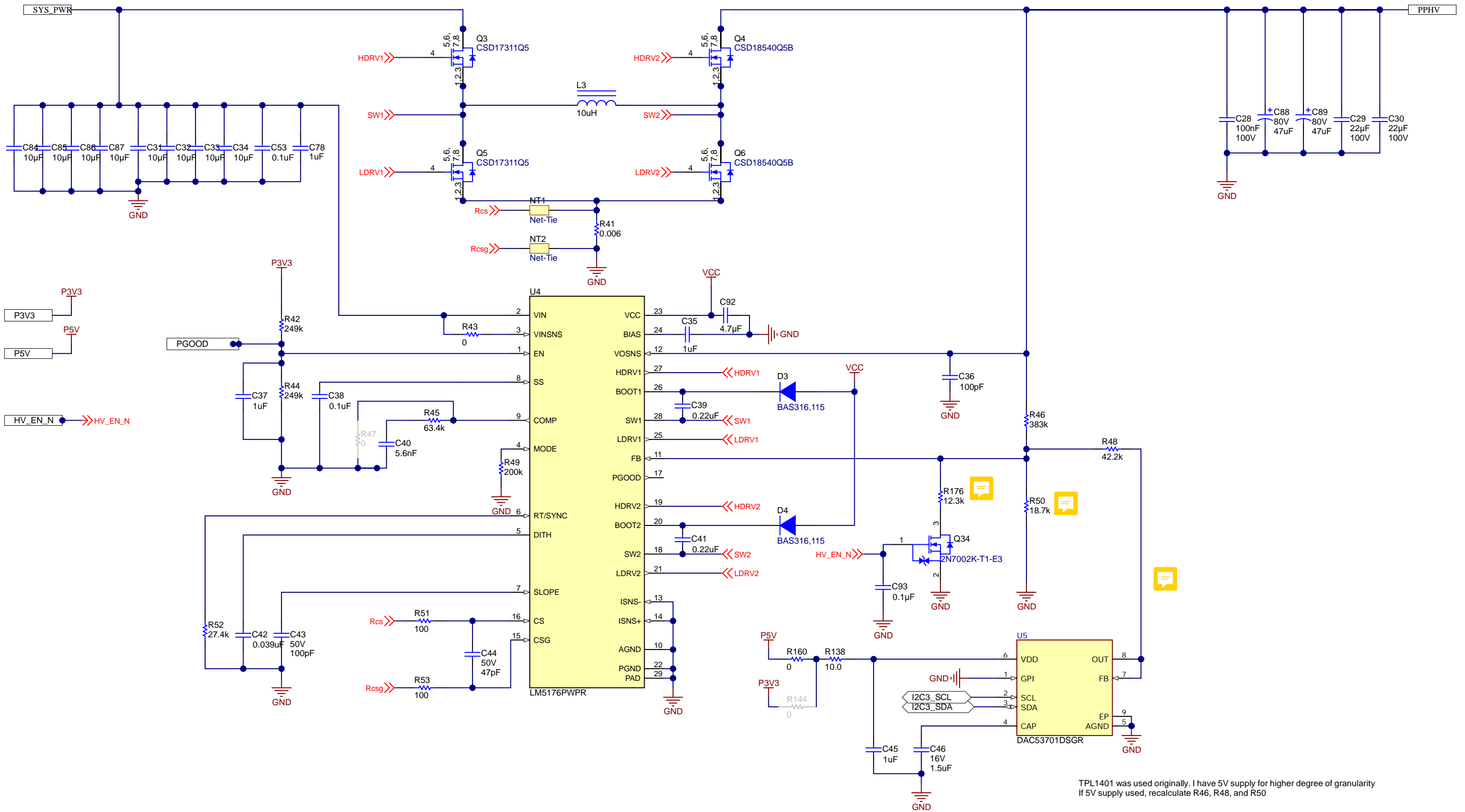
Resistor Divider

For 36V systems, resistors need to dissipate 0.25W
For 48V systems, resistors need to dissipate 0.4W



DNP TVS for above 44V on VBUS
Resistor divider needs to be changed for 48V

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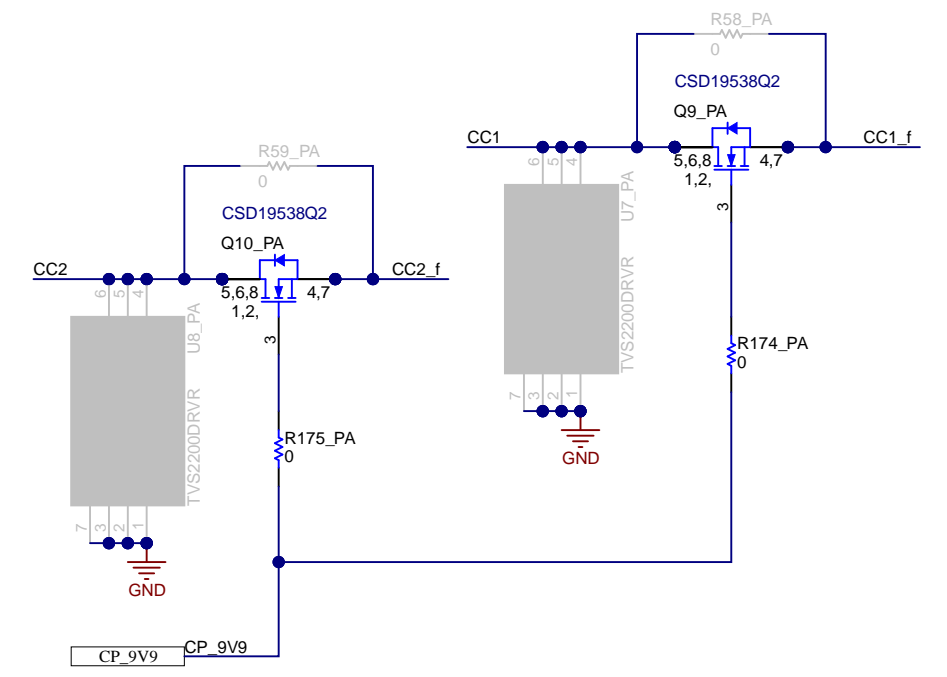
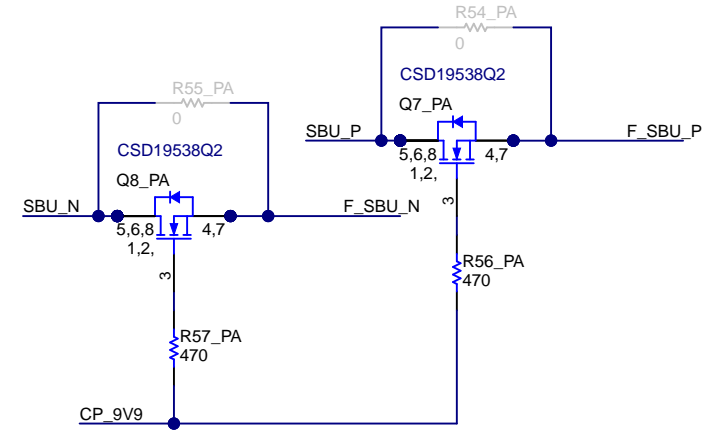
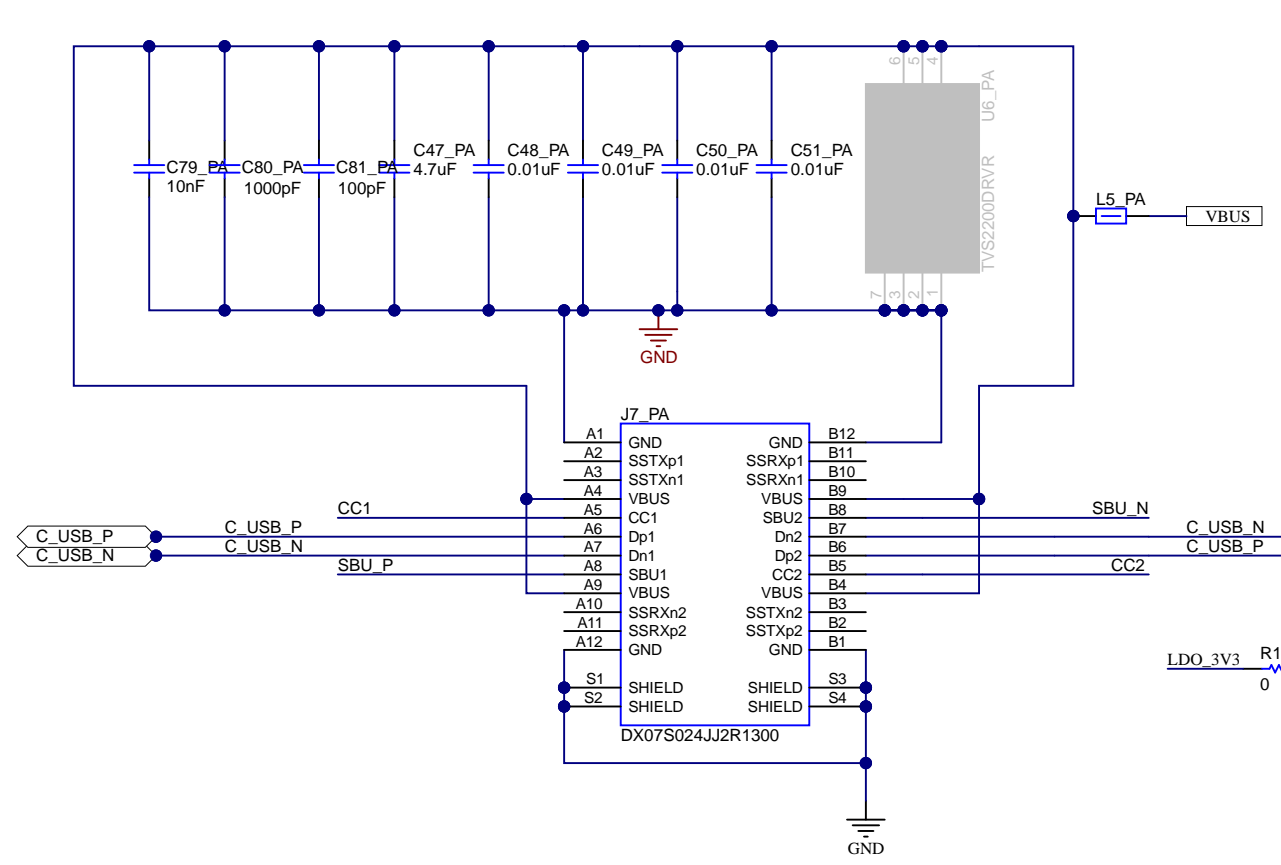
TPL1401 was used originally. I have 5V supply for higher degree of granularity
 If 5V supply used, recalculate R46, R48, and R50

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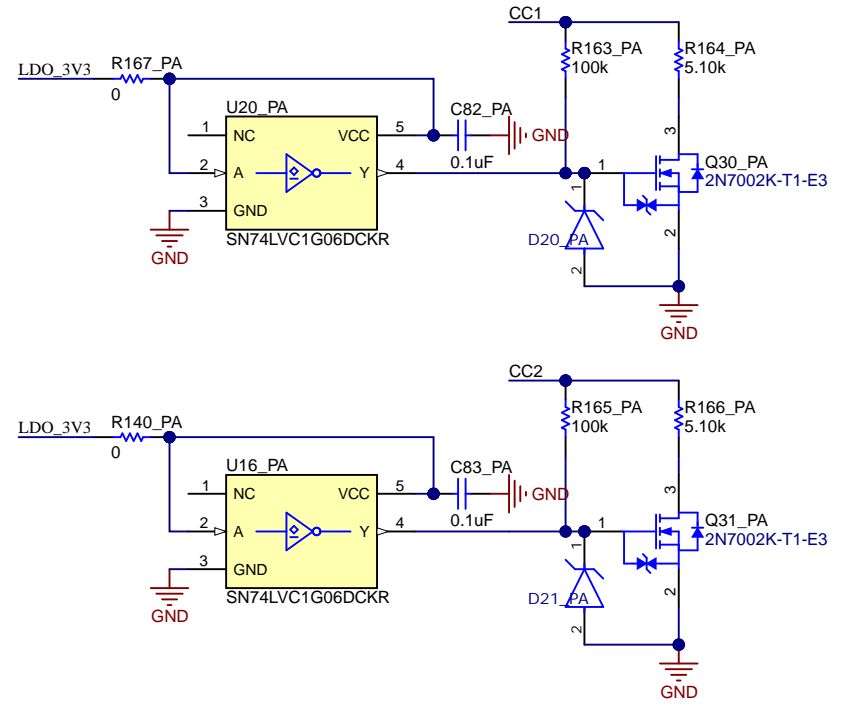
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Number: EPR_EVM	Rev: E3	Sheet: 4 of 9
SVN Rev: Not in version control	File: EPR_EVM_High_Power_Supply_RevE3.SchDoc	Size: B
Drawn By: Emma Hoying	Contact: http://www.ti.com/support	



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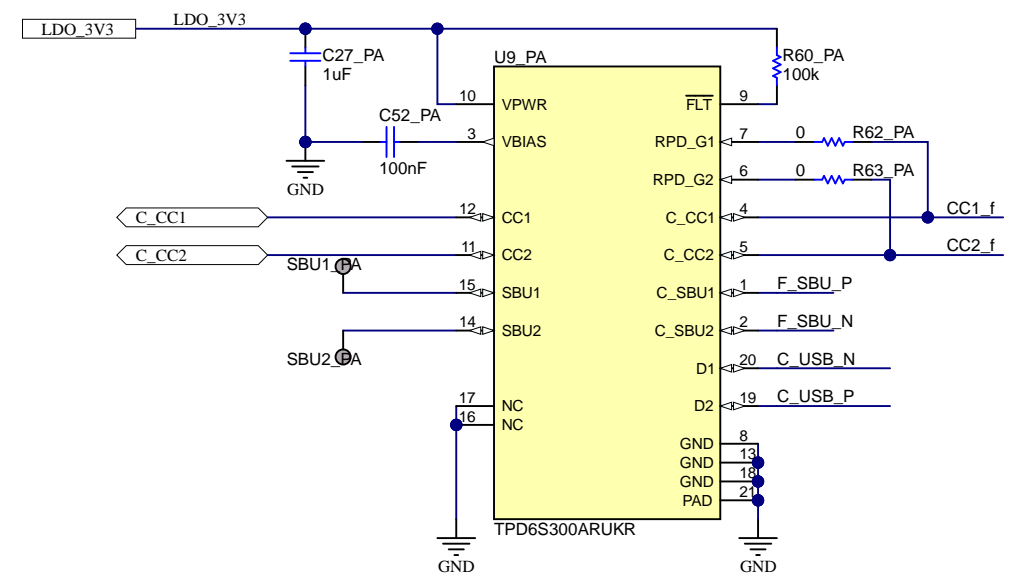


Dead Battery Resistors

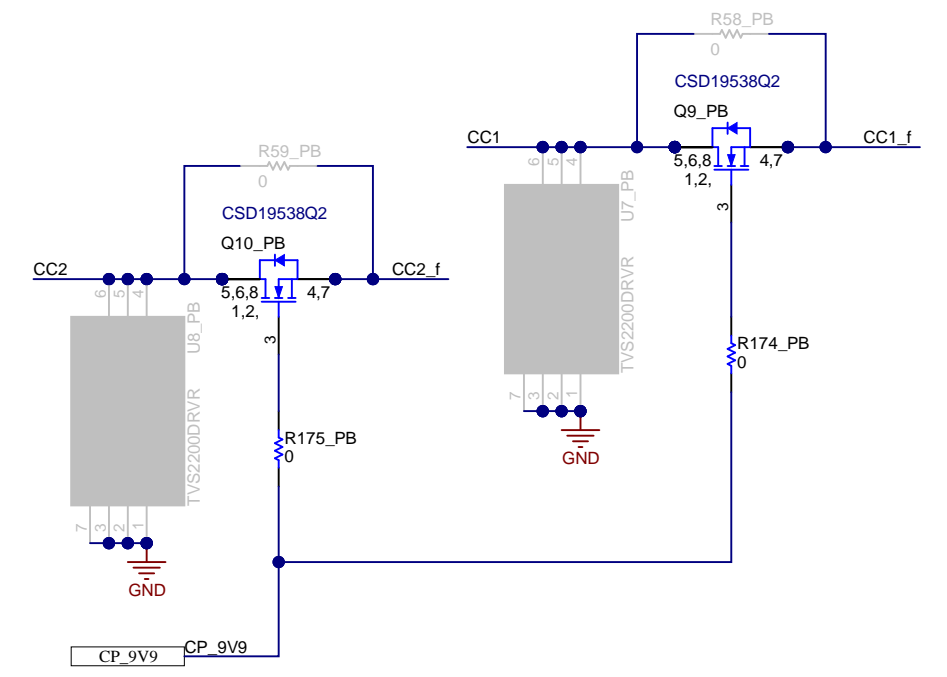
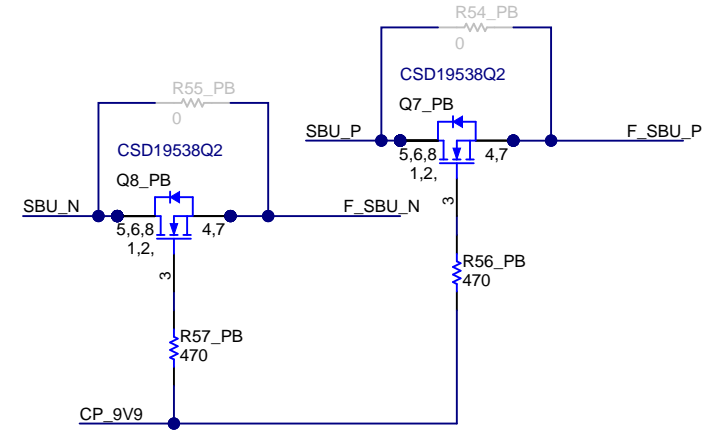
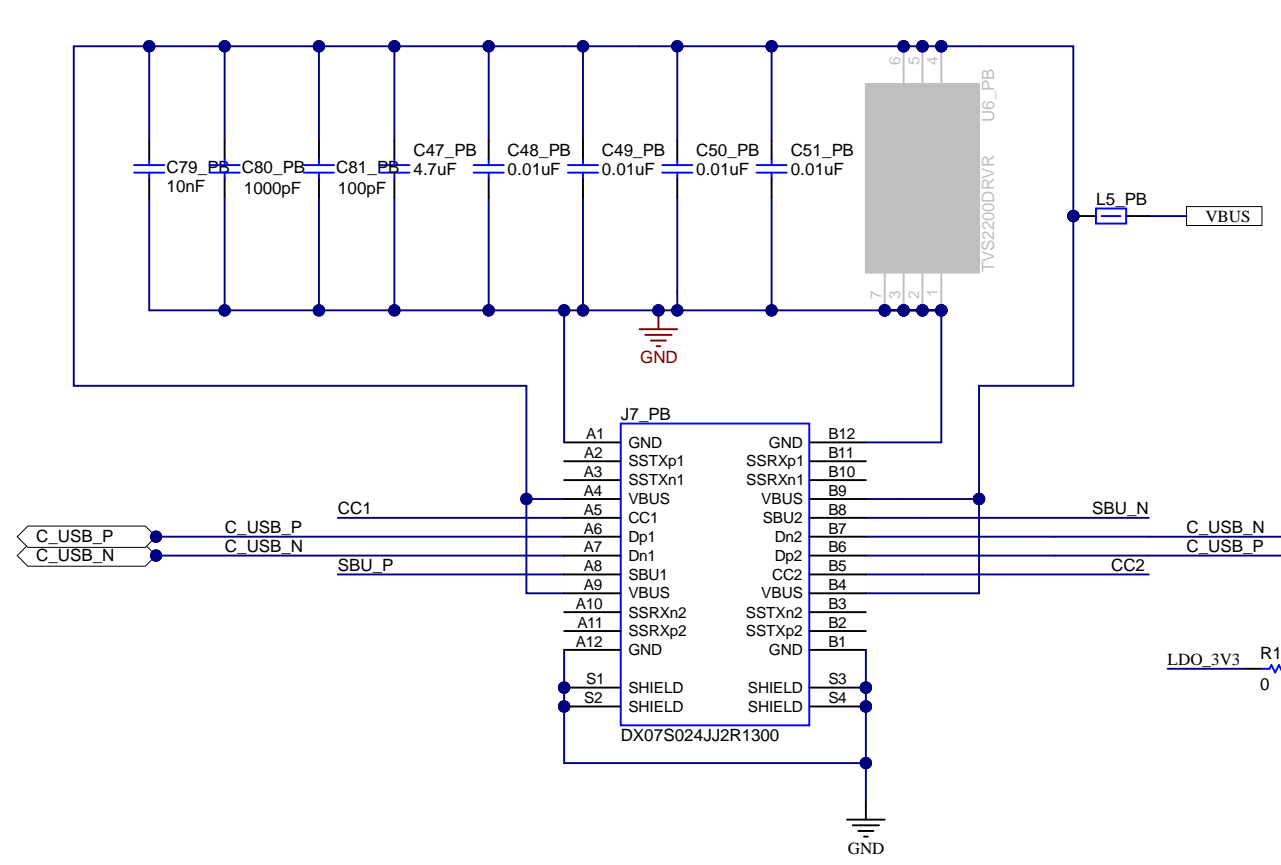


State	LDO3V3	Inverter	Q31
Dead Bat	Low	HIZ	On
Normal On	High	Low	Off

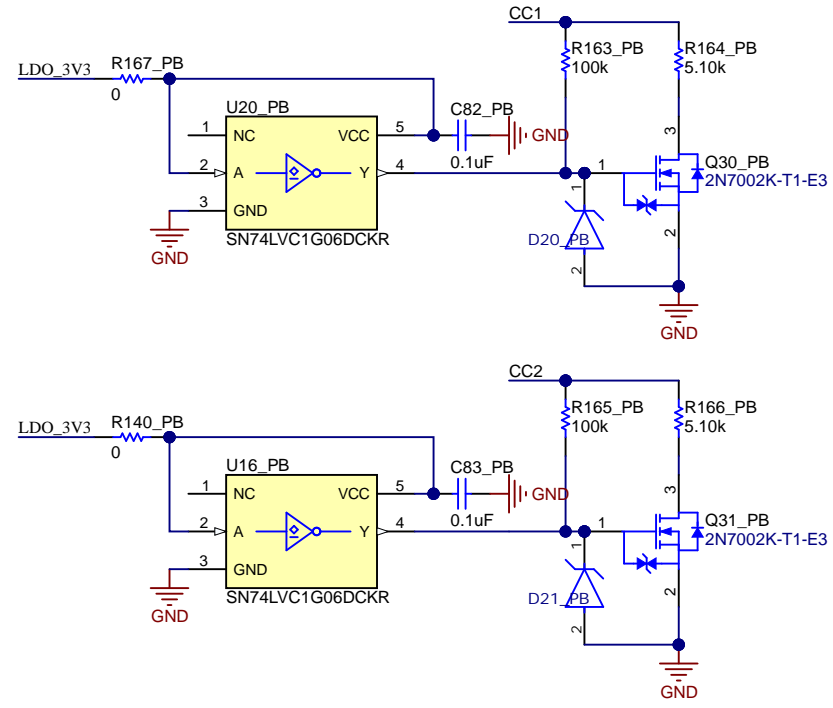
Note, the Vgs = 20V for Q30 and Q31.



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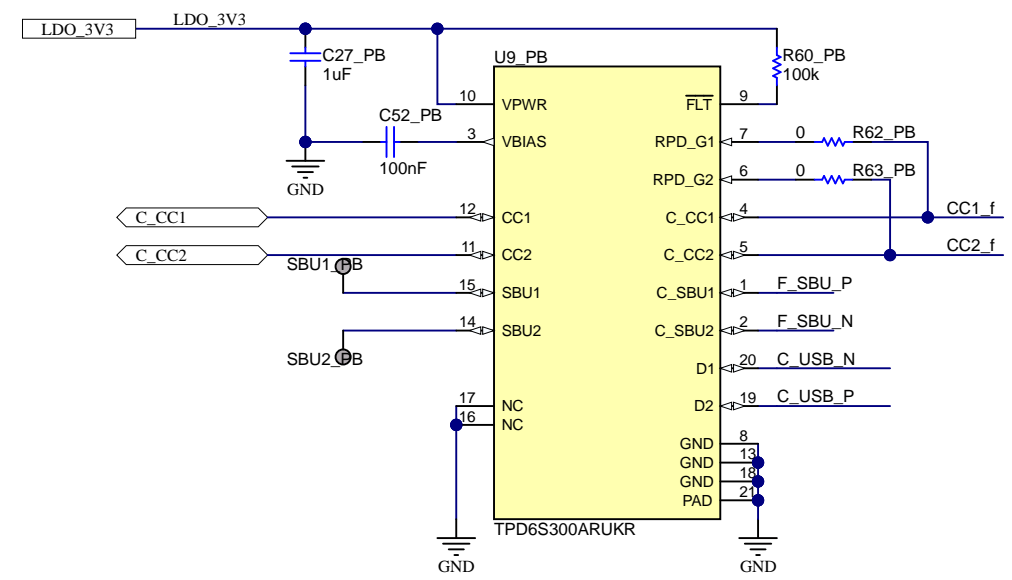


Dead Battery Resistors

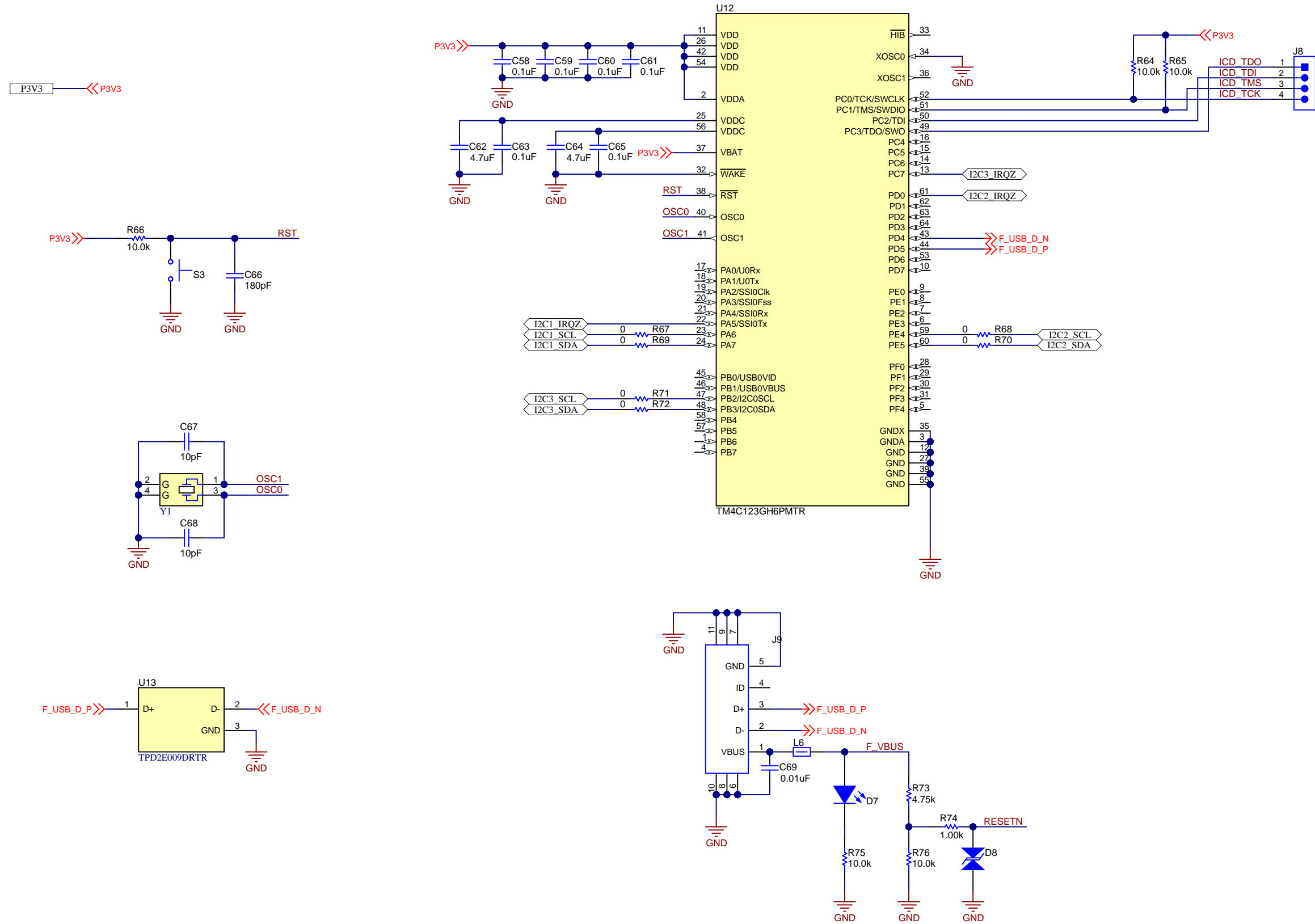


State	LDO3V3	Inverter	Q31
Dead Bat	Low	HIZ	On
Normal On	High	Low	Off

Note, the Vgs = 20V for Q30 and Q31.

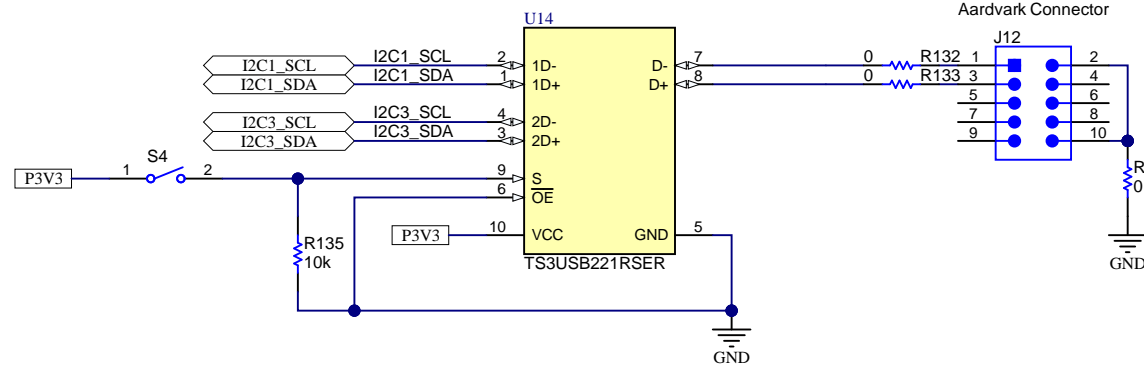
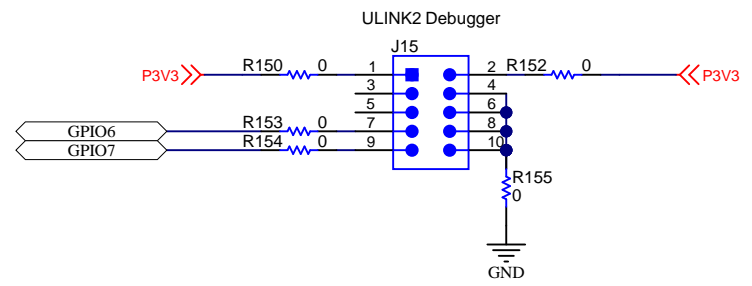
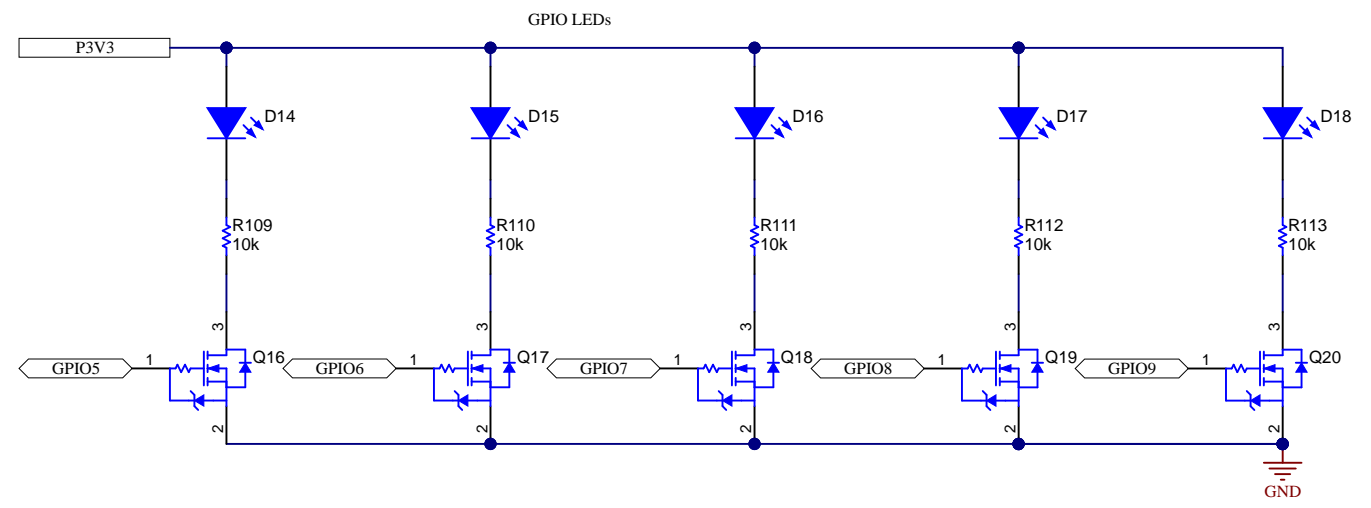
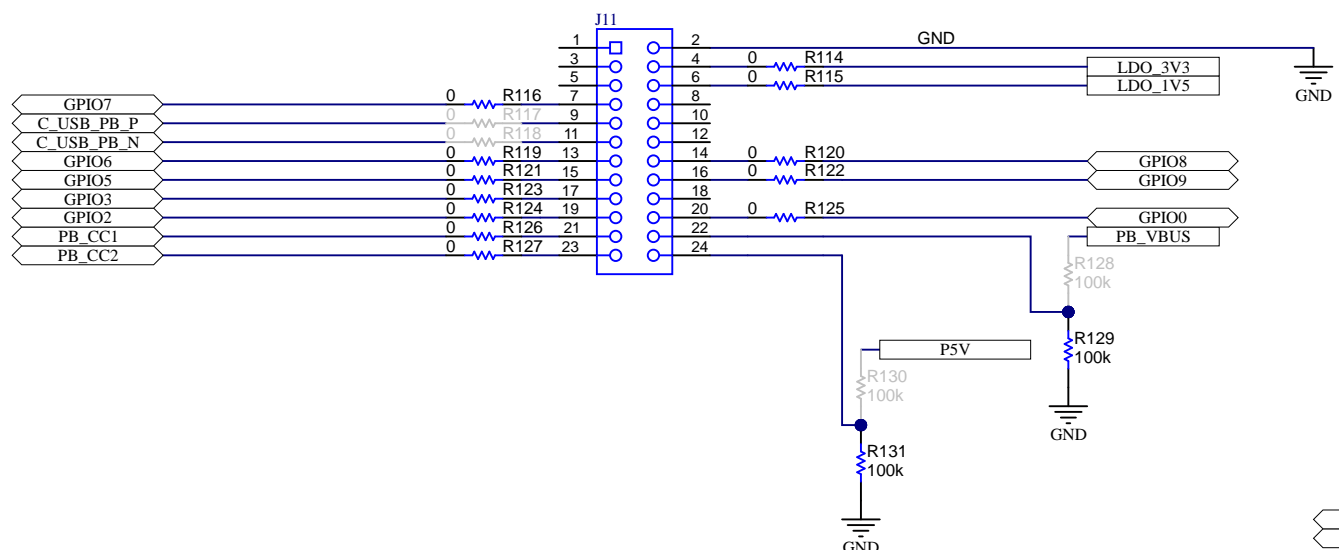
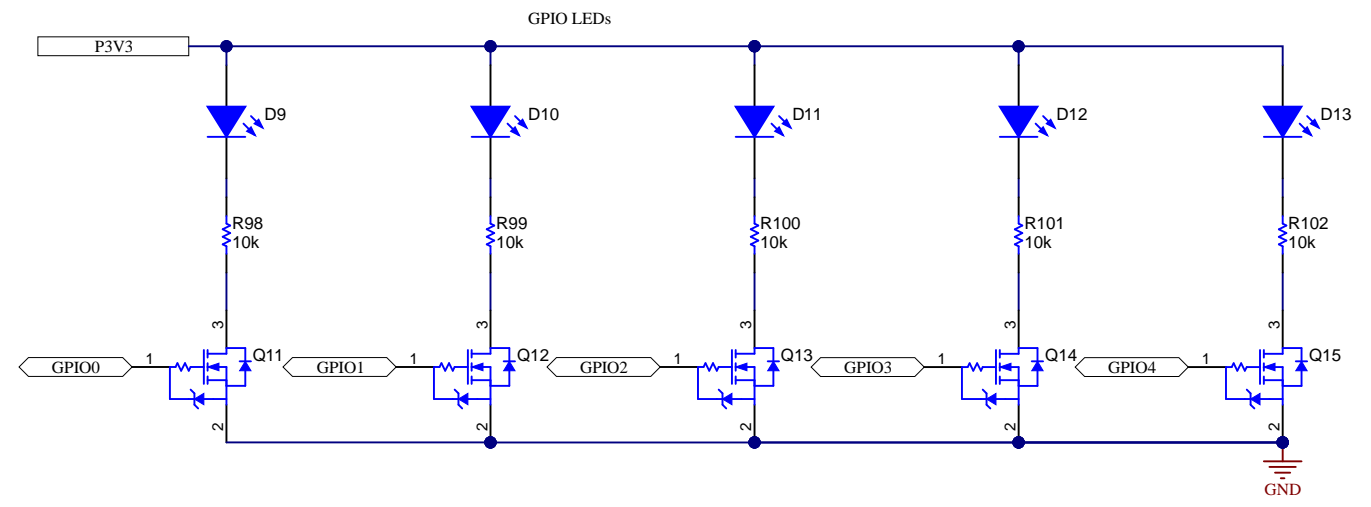
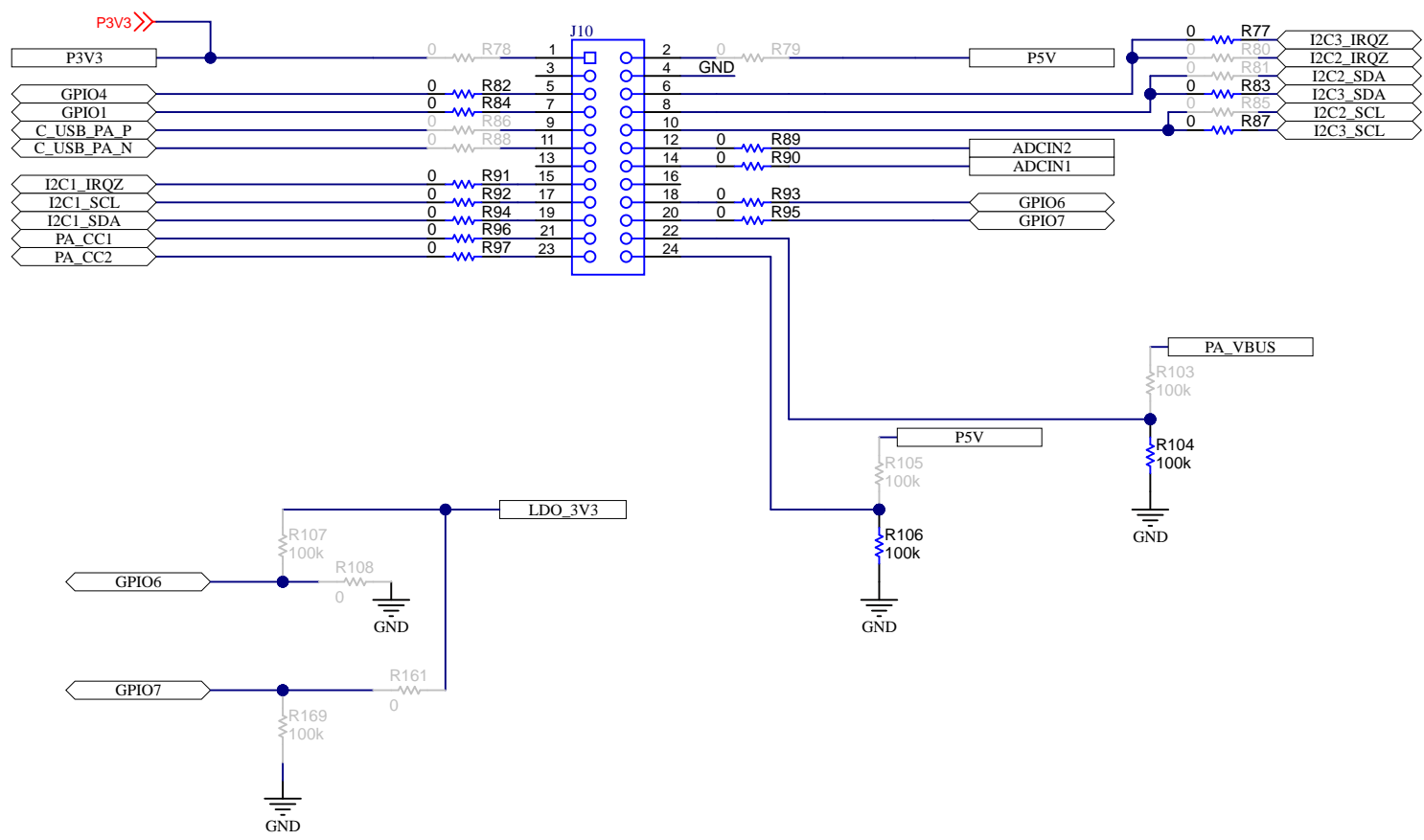


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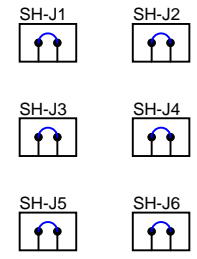
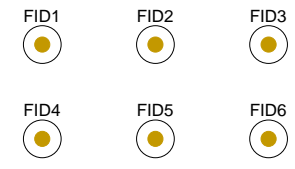
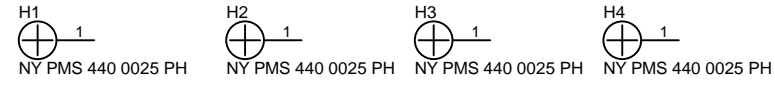
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TID #: N/A	Project Title: Change in menu Project	Parameter
Number: EPR_EVM	Rev: E3	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 9
Drawn By:	File: EPR_EVM_Tiva_Debug_RevE3.SchDoc	Size: B
Engineer: Emma Hoying	Contact: http://www.ti.com/support	



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 4/29/2021	
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Number: EPR_EVM	Rev: E3	Sheet Title:	http://www.ti.com
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 7 of 9	
Drawn By:	File: EPR_EVM_Debug_Connectors_RevE3.SchDoc	Size: B	
Engineer: Emma Hoying	Contact: http://www.ti.com/support	© Texas Instruments 2021	



PCB Number: EPR_EVM
PCB Rev: E3



PCB LOGO
ESD Susceptible

PCB LOGO
WEEE logo

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FCC disclaimer

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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TID #: N/A	Project Title: Change in menu Project Project Options Parameter		
Number: EPR_EVM	Rev: E3	Sheet Title:	
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Drawn By:	File: EPR_EVM_Hardware_RevE3.SchDoc	Size: B	
Engineer: Emma Hoying	Contact: http://www.ti.com/support		